MMBT6520L, NSVMMBT6520L

High Voltage Transistor PNP Silicon

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-350	Vdc
Collector-Base Voltage	V _{CBO}	-350	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Base Current	Ι _Β	-250	mA
Collector Current – Continuous	Ι _C	-500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board, (Note 1) T _A = 25°C Derate above 25°C	PD	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

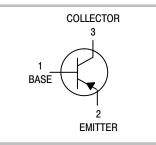
1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.

2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.



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MARKING DIAGRAM



2Z = Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT6520LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
MMBT6520LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel
NSVMMBT6520LT1G	SOT–23 (Pb–Free)	3,000 / Tape & Reel

⁺For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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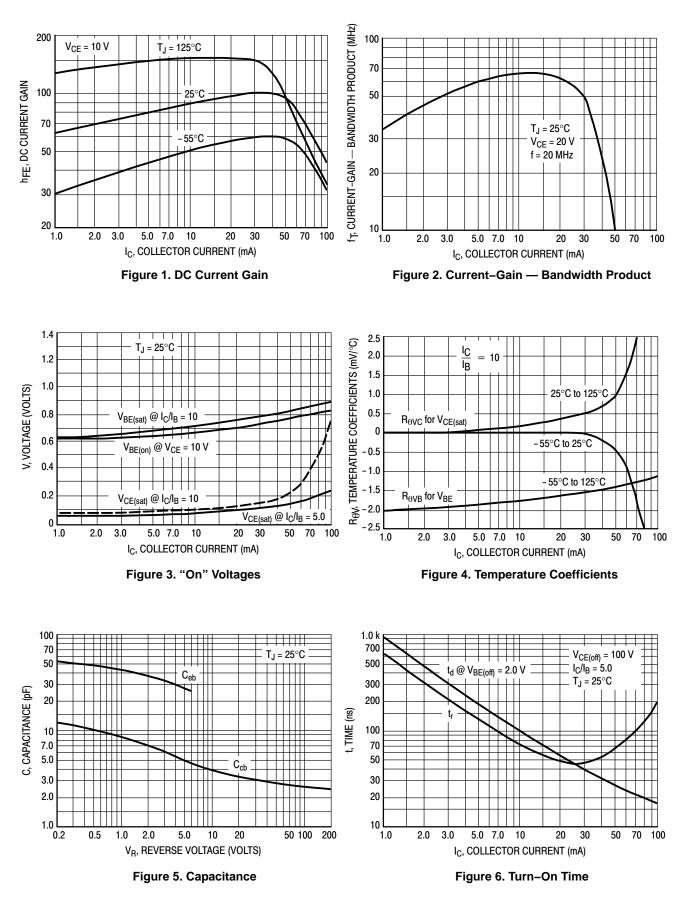
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage $(I_{C} = -1.0 \text{ mA})$	V _{(BR)CEO}	-350	-	Vdc
Collector–Base Breakdown Voltage $(I_C = -100 \ \mu A)$	V _(BR) CBO	-350	-	Vdc
Emitter–Base Breakdown Voltage (I _E = −10 μA)	V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current ($V_{CB} = -250 \text{ V}$)	I _{CBO}	-	-50	nA
Emitter Cutoff Current ($V_{EB} = -4.0 V$)	I _{EBO}	-	-50	nA
ON CHARACTERISTICS		•		
$ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = -1.0 \text{ mA}, \text{V}_{CE} = -10 \text{ V}) \\ (I_{C} = -10 \text{ mA}, \text{V}_{CE} = -10 \text{ V}) \\ (I_{C} = -30 \text{ mA}, \text{V}_{CE} = -10 \text{ V}) \\ (I_{C} = -50 \text{ mA}, \text{V}_{CE} = -10 \text{ V}) \\ (I_{C} = -100 \text{ mA}, \text{V}_{CE} = -10 \text{ V}) \end{array} $	h _{FE}	20 30 30 20 15	_ 200 	_
Collector-Emitter Saturation Voltage $(I_{C} = -10 \text{ mA}, I_{B} = -1.0 \text{ mA})$ $(I_{C} = -20 \text{ mA}, I_{B} = -2.0 \text{ mA})$ $(I_{C} = -30 \text{ mA}, I_{B} = -3.0 \text{ mA})$ $(I_{C} = -50 \text{ mA}, I_{B} = -5.0 \text{ mA})$	V _{CE(sat)}	_ _ _ _	-0.30 -0.35 -0.50 -1.0	Vdc
$\begin{array}{l} \text{Base-Emitter Saturation Voltage} \\ (I_{C} = -10 \text{ mA}, I_{B} = -1.0 \text{ mA}) \\ (I_{C} = -20 \text{ mA}, I_{B} = -2.0 \text{ mA}) \\ (I_{C} = -30 \text{ mA}, I_{B} = -3.0 \text{ mA}) \end{array}$	V _{BE(sat)}	_ _ _	-0.75 -0.85 -0.90	Vdc
Base–Emitter On Voltage ($I_C = -100$ mA, $V_{CE} = -10$ V)	V _{BE(on)}	_	-2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS	1	1		
Current–Gain – Bandwidth Product	f _T	40	200	MHz

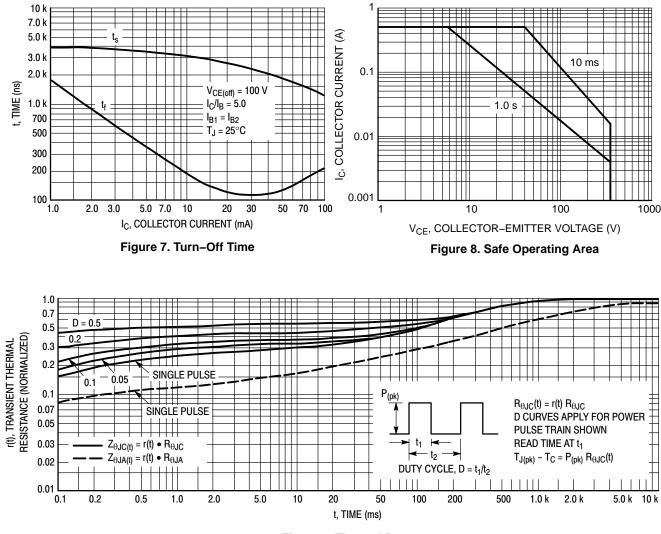
$(I_{C} = -10 \text{ mA}, V_{CE} = -20 \text{ V}, f = 20 \text{ MHz})$.1			
Collector–Base Capacitance (V_{CB} = -20 V, f = 1.0 MHz)	C _{cb}	-	6.0	pF
Emitter–Base Capacitance (V_{EB} = -0.5 V, f = 1.0 MHz)	C _{eb}	-	100	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

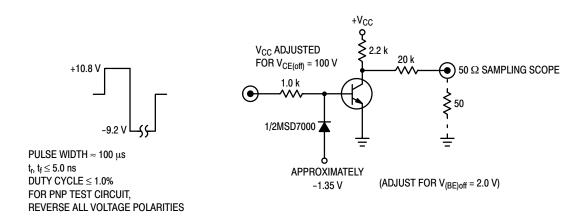
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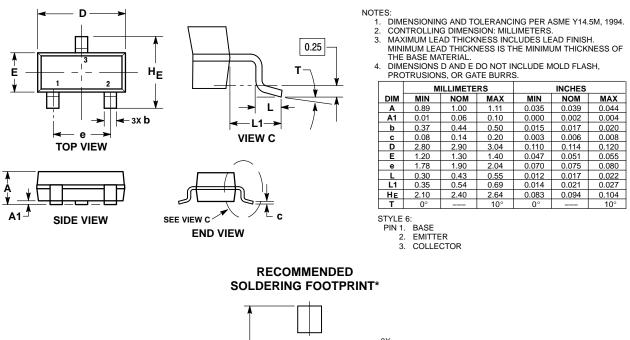


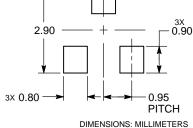




PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AR





*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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