

# SBL6030PT - SBL6060PT

## **60A SCHOTTKY BARRIER RECTIFIER**

### **Features**

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Lead Free Finish, RoHS Compliant (Note 3)

### **Mechanical Data**

Case: TO-3P

Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

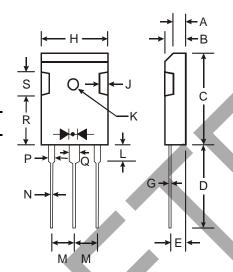
Terminals: Finish - Bright Tin. Solderable per MIL-STD-202, Method 208

Polarity: As Marked on Body

Ordering Information: See Last Page

Marking: Type Number

Weight: 5.6 grams (approximate)



TO-3P					
Dim	Min	Max			
Α	1.88	2.08			
В	4.87	5.13			
С	21.25	21.75			
D	19.60	20.10			
Ε	2.10	2.40			
G	0.51	0.76			
Н	15.75	16.25			
J	1.93	2.18			
K	2.90Ø	3.20Ø			
L	3.78	4.38			
М	5.20	5.70			
N	1.12	1.22			
Р	1.90	2.16			
Q	2.93	3.22			
R	11.70	12.80			
S	4.40 Typical				
All Dimensions in mm					

#### Maximum Ratings and Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SBL 6030PT	SBL 6040PT	SBL 6050PT	SBL 6060PT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	40	50	60	>
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	28	35	42	V
Average Rectified Output Current @ T <sub>C</sub> = 100°C (Note 1)	lo		6	60		Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>		5	00		А
Forward Voltage Drop @ I <sub>F</sub> = 30A, T <sub>C</sub> = 25°C	$V_{FM}$	0.	55	0.	70	V
Peak Reverse Current @ $T_C = 25^{\circ}$ C at Rated DC Blocking Voltage @ $T_C = 100^{\circ}$ C	I <sub>RM</sub>	20 200		mA		
Typical Total Capacitance (Note 2)	C <sub>T</sub>		15	500		pF
Typical Thermal Resistance Junction to Case (Note 1)	$R_{ hetaJc}$	0.5		°C/W		
Operating and Storage Temperature Range	$T_{j}$ , $T_{STG}$	-65 to +150			°C	

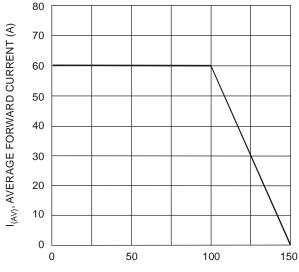
Notes:

Thermal resistance junction to case mounted on heatsink. Measured at 1.0MHz and Applied Reverse Voltage of 4.0V DC.

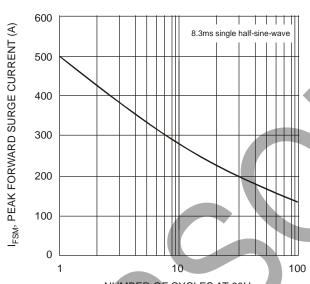
RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Notes 5 and 7.



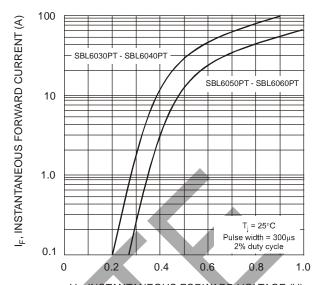
# PART OBSOLETE – NO ALTERNATE PART



 $T_{C}$ , CASE TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve



NUMBER OF CYCLES AT 60Hz
Fig. 3 Max Non-Repetitive Forward Surge Current



V<sub>F</sub>, INSTANTANEOUS FORWARD VOLTAGE (V)
Fig. 2 Typical Forward Characteristics

10000

1000

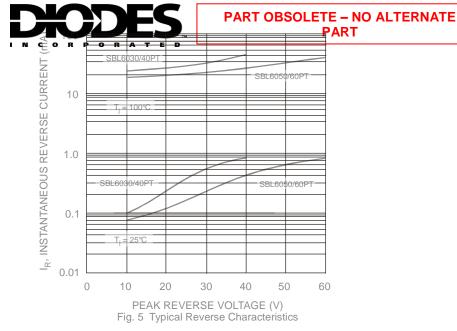
1000

0.1

1.0

100

V<sub>R</sub>, REVERSE VOLTAGE (V)
Fig. 4 Typical Total Capacitance per Element



# **Ordering Information**

Device	Packaging	Shipping
SBL6030PT	TO-3P	30/Tube
SBL6040PT	TO-3P	30/Tube
SBL6050PT	TO-3P	30/Tube
SBL6060PT	TO-3P	30/Tube

4. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02008.pdf.

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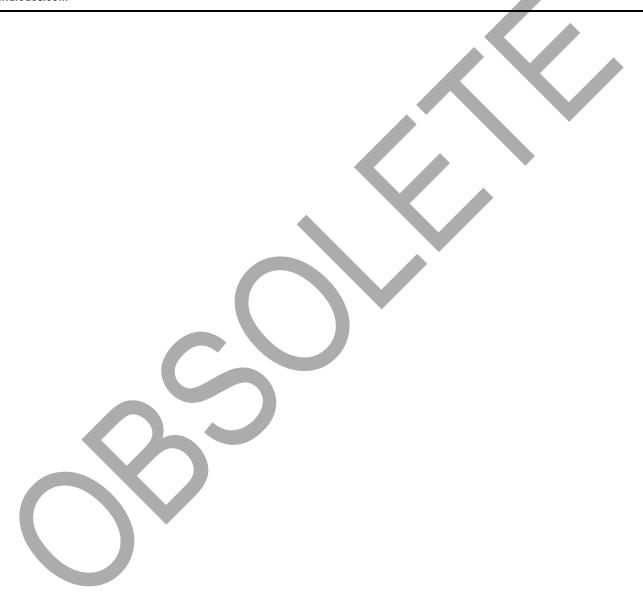
# PART OBSOLETE – NO ALTERNATE

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