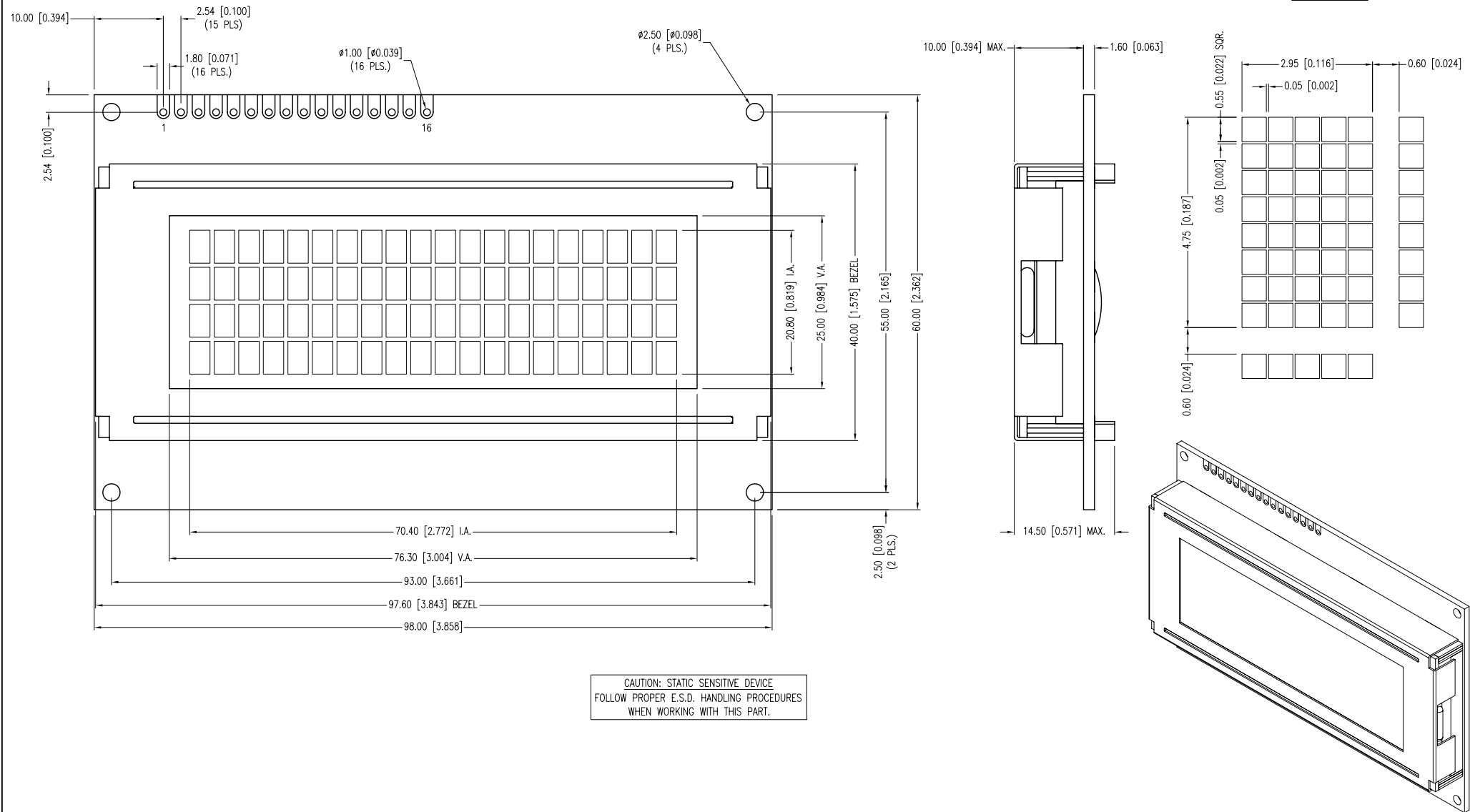


PART NUMBER	LCR-H02004DLF/FYWC	REV.	A
DATE	E.C.N. NUMBER AND REVISION COMMENTS		REV.
11.16.11	E.C.N. #11767/#10BRDR. & REDRAWN.		A



*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN.= +DECIMAL PRECISION MAX.= +DECIMAL PRECISION -0.00

UNCONTROLLED DOCUMENT



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20 x 4 CHARACTER LCD MODULE, STN YELLOW, TRANSFLECTIVE, WHITE LED BACKLIGHT, 6:00 VIEW, 3.3V.

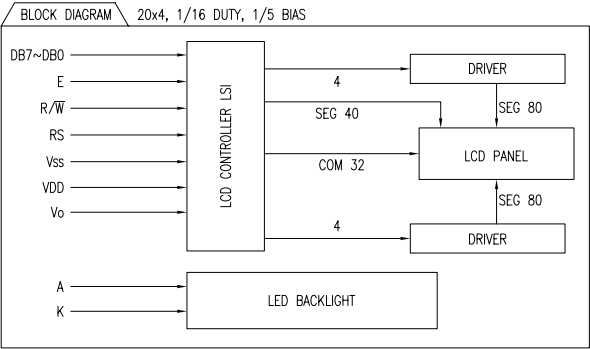
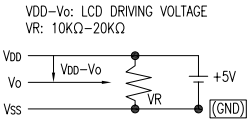
THE SPECIFICATIONS MAY CHANGE AT ANY TIME WITHOUT NOTICE DUE TO NEW MATERIALS OR PRODUCT IMPROVEMENT.

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DATE:	11.16.11	DRAWN BY:	AB
PAGE:	1 OF 2	CHKD BY:	SS
SCALE:	NTS	APRVD BY:	JD
UNIT:	mm [INCH]		(Pc)

PART NUMBER	LCR-H02004DLF/FYWC	REV.	A
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PIN CONFIGURATION			
PIN NO.	SYMBOL	LEVEL	FUNCTION
1	Vss	—	POWER SUPPLY
2	Vdd	—	
3	Vo	—	
4	RS	H/L	REGISTER SELECT SIGNAL H: DATA INPUT L: INSTRUCTION INPUT
5	R W	H/L	H: DATA READ (MODULE-->MPU) L: DATA WRITE (MODULE<--MPU)
6	E	H,H->L	ENABLE
7~14	DB0~DB7	H/L	DATA BUS—SOFTWARE SELECTABLE 4 OR 8 BIT MODE.
15	A	—	LED BACKLIGHT J2=J4=OPEN
16	K	—	LED BACKLIGHT J3=J5=SHORT



ELECTRICAL CHARACTERISTICS		V _{DD} =2.7V to 4.5V, T _a =25°C					
ITEM		SYMBOL	CONDITION	STANDARD VALUE			UNIT
				MIN	TYP	MAX	
SUPPLY VOLTAGE FOR LOGIC		V _{DD} -V _{SS}	—	—	3.3	—	V
SUPPLY CURRENT FOR LOGIC		I _{DD}	V _{DD} =5V	—	0.10	0.25	mA
INPUT VOLTAGE	HIGH	V _{IH}	—	2.2	—	V _{DD}	V
	LOW	V _{IL}	—	0	—	0.6	V
OUTPUT VOLTAGE	HIGH	V _{OH}	I _{OH} =-0.205mA	2.4	—	—	V
	LOW	V _{OL}	I _{OL} =1.6mA	—	—	0.4	V
LED BACKLIGHT	VOLTAGE	V _f	I _f =30mA	—	3.3	—	V
	CURRENT	I _f	—	—	30	40	mA
	POWER CONSUMPTION	P _D	—	—	100	—	mW
	LUMINOUS	L	I _f =30mA	—	200	—	cd/m ²
	COLOR	WHITE	X	0.26	—	0.33	nm
			Y	0.26	—	0.33	nm

ABSOLUTE MAXIMUM RATINGS					
ITEM	SYMBOL	TEST CONDITION	STANDARD VALUE		UNIT
SUPPLY VOLTAGE FOR LOGIC	Vdd-Vss	Ta=25°C	-0.3	7.0	V
SUPPLY VOLTAGE FOR LCD DRIVE	Vdd-Vo	—	-0.3@50°C	10@0°C	V
INPUT VOLTAGE	Vi	Ta=25°C	Vss	Vdd	V
OPERATING TEMPERATURE	Topr	—	-20	+70	°C
STORAGE TEMPERATURE	Tstg	—	-30	+80	°C

- NOTES:**
- J1 AND J7 ARE NOT OCCUPIED. ONE 0Ω RESISTOR CONNECTS THE NON-GROUND SIDE OF J1 AND J7. THIS IS DONE TO CONNECT THE BEZEL TO THE PLATED THROUGH HOLES, BUT ISOLATE THEM FROM DIGITAL GROUND.
 - THE USER MUST PROVIDE A PATH FROM THE BEZEL/HOLES TO GROUND TO CREATE AN ESD PATH TO GROUND.
 - PROTECTIVE TAPE TO BE SUPPLIED WITH BLUE GRIPPER TAB.

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