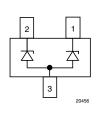


### **Two-Line ESD Protection in SOT-23**





#### **MARKING** (example only)



YYY = type code (see table below) XX = date code

#### **DESIGN SUPPORT TOOLS** click logo to get started



#### **FEATURES**

- Two-line ESD protection device
- ESD immunity acc. IEC 61000-4-2
  - ± 30 kV contact discharge
  - ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- Space saving SOT-23 package
- e3 Sn
- AEC-Q101 qualified available







ROHS COMPLIANT HALOGEN FREE

**GREEN** (5-2008)

ORDERIN	IG INFORM	MATION					
	ENVIR	ONMENTAL AN	ID QUALITY CO	DDE	PACKAG	ING CODE	
PART NUMBER (EXAMPLE)	AEC-Q101	Q101 LEAD (Pb)-FREE TIN (8 mm		3K PER 7" REEL (8 mm TAPE),	10K PER 13" REEL (8 mm TAPE),	ORDERING CODE (EXAMPLE)	
(L)OAM LL)	STANDARD STANDARD	GREEN	PLATED	15K/BOX = MOQ	10K/BOX = MOQ		
GSOT05C-		E		3	-08		GSOT05C-E3-08
GSOT05C-			G	3	-08		GSOT05C-G3-08
GSOT05C-	Н	Е		3	-08		GSOT05C-HE3-08
GSOT05C-	Н		G	3	-08		GSOT05C-HG3-08
GSOT05C-		Е		3		-18	GSOT05C-E3-18
GSOT05C-			G	3		-18	GSOT05C-G3-18
GSOT05C-	Н	E		3		-18	GSOT05C-HE3-18
GSOT05C-	Н		G	3		-18	GSOT05C-HG3-18

PACKA	GE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	ENVIRONMENTAL STATUS	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
GSOT03C	SOT-23	03C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
4001000	001 20	C1G	Green	8.1 mg	020470	(according J-STD-020)	200 0, 10 3 at terrimas	
GSOT04C	SOT-23	04C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
4001040	001 20	C8G	Green 8.1 mg		02 34 7 0	(according J-STD-020)	200 0/103 at terrilinais	
GSOT05C	SOT-23	05C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
G30103C	301-23	C2G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 C/10's at terminals	
GSOT08C	SOT-23	08C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
G30106C	301-23	C3G	Green	8.1 mg	] OL 94 V-0	(according J-STD-020)	200 C/10's at terminals	
GSOT12C	SOT-23	12C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
G301120	301-23	C4G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 C/10's at terminals	
GSOT15C	SOT-23	15C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 a at tarminala	
GSUTISC	301-23	C5G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	260 °C/10 s at terminals	
CCOT24C	SOT 22	24C	Standard	8.8 mg	LIL OA V. C	MSL level 1	260 °C/10 a at tarminals	
GSOT24C	SOT-23	C6G	Green	8.1 mg	UL 94 V-0	(according J-STD-020)	260 °C/10 s at terminals	
GSOT36C	SOT-23	36C	Standard	8.8 mg	UL 94 V-0	MSL level 1	260 °C/10 s at terminals	
G301360	301-23	C7G	Green	8.1 mg	OL 94 V-0	(according J-STD-020)	200 C/10 s at terminals	

Rev. 2.6, 02-May-17 **1** Document Number: 85824



ABSOLUTE MAXIMUM	RATINGS GSOT03C			
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	<b>I</b>	30	А
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ІРРМ		Α
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 µs; single shot	D	369	W
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 µs; single shot	P <sub>PP</sub>	504	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV
ESD IIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
Operating temperature	Junction temperature	$T_J$	-55 to +150	°C
Storage temperature		T <sub>STG</sub>	-55 to +150	°C

ABSOLUTE MAXIMUM RATINGS GSOT04C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	l	30 30 429	А	
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	Іррм		Α	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	564	W	
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР		W	
CCD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
Operating temperature	Junction temperature	T <sub>J</sub>	-55 to +150	°C	
Storage temperature		T <sub>STG</sub>	-55 to +150	°C	

ABSOLUTE MAXIMU	ABSOLUTE MAXIMUM RATINGS GSOT05C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	I	30	А		
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	Іррм	30	Α		
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	$P_PP$	480	W		
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	грр		W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	W	± 30	kV		
ESD IIIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	°C		



ABSOLUTE MAXIMUM RATINGS GSOT08C						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	I	18	А		
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	I <sub>PPM</sub>	18	А		
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	345 400	W		
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 µs; single shot	Грр		W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV		
ESD IIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 30	kV		
Operating temperature	Junction temperature	T <sub>J</sub>	-55 to +150	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS GSOT12C						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Bulling	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	<b>I</b>	12	А		
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	5, t <sub>p</sub> = 8/20 μs; single shot	А			
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs; single shot	P <sub>PP</sub>	312	W		
reak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p$ = 8/20 $\mu$ s; single shot	ГРР	337	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV		
ESD initiditity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV		
Operating temperature	Junction temperature	$T_J$	-55 to +150	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	°C		

ABSOLUTE MAXIMUM RATINGS GSOT15C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Post of the country	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	l	8	Α	
Peak pulse current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p$ = 8/20 $\mu$ s; single shot	ІРРМ	8	Α	
Dook pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	D	345 400	W	
Peak pulse power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>		W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD initiduity	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
Operating temperature	Junction temperature	TJ	-55 to +150	°C	
Storage temperature		T <sub>STG</sub>	-55 to +150	°C	



ABSOLUTE MAXIMUM RATINGS GSOT24C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	l	5	А	
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ІРРМ	5 235	Α	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	· P <sub>PP</sub>	240	W	
reak puise power	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ГРР		W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
L3D initiditity	Air discharge acc. IEC 61000-4-2; 10 pulses	VESD .	± 30	kV	
Operating temperature	Junction temperature	TJ	-55 to +150	°C	
Storage temperature		T <sub>STG</sub>	-55 to +150	°C	

ABSOLUTE MAXIMUM RATINGS GSOT36C					
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT	
Peak pulse current	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	1	3.5 3.5 248	А	
reak puise current	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	ІРРМ		А	
Peak pulse power	Pin 1 to 3 or pin 2 to 3 acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	$P_PP$	252	W	
reak puise powei	Pin 1 to 2 or pin 2 to 1; pin 3 not connected acc. IEC 61000-4-5, $t_p = 8/20 \mu s$ ; single shot	грр		W	
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 30	kV	
ESD IIIIIIuility	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV	
Operating temperature	Junction temperature	T <sub>J</sub>	-55 to +150	°C	
Storage temperature		T <sub>STG</sub>	-55 to +150	°C	



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### Vishay Semiconductors

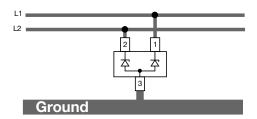
#### **BIAs-MODE** (2-line Bidirectional Asymmetrical protection mode)

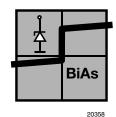
With the GSOTxxC two signal- or data-lines (L1, L2) can be protected against voltage transients. With pin 3 connected to ground and pin 1 and pin 2 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified Maximum Reverse Working Voltage (V<sub>RWM</sub>) the protection diode between pin 2 and pin 3 and between pin 1 and pin 3 offers a high isolation to the ground line. The protection device behaves like an open switch.

As soon as any positive transient voltage signal exceeds the breakdown voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The Clamping Voltage (V<sub>C</sub>) is defined by the breakdown voltage (V<sub>BR</sub>) level plus the voltage drop at the series impedance (resistance and inductance) of the protection diode.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction through the protection diode. The low Forward Voltage ( $V_F$ ) clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the GSOTxxC clamping behavior is Bidirectional and Asymmetrical (BiAs).

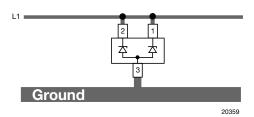




If a higher surge current or peak pulse current (IPP) is needed, both protection diodes in the GSOTxxC can also be used in parallel in order to "double" the performance.

#### This offers:

- double surge power = double peak pulse current (2 x I<sub>PPM</sub>)
- half of the line inductance = reduced clamping voltage
- half of the line resistance = reduced clamping voltage
- double line capacitance (2 x C<sub>D</sub>)
- double reverse leakage current (2 x I<sub>R</sub>)



<b>ELECTRICAL CHARACTERISTICS GSOT03C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	3.3	V	
Reverse voltage	at I <sub>R</sub> = 100 μA	$V_R$	3.3	-	-	V	
Reverse current	at V <sub>R</sub> = 3.3 V	I <sub>R</sub>	-	-	100	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	4.0	4.6	5.5	V	
Deverse elemning veltage	at I <sub>PP</sub> = 1 A	V	-	5.7	7.5	V	
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	4.0 4.6 5.5	V		
Forward alamping valtage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V	
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>F</sub>	-	4.5	-	V	
Canaditanas	at V <sub>R</sub> = 0 V; f = 1 MHz	-	-	420	600	pF	
Capacitance	at V <sub>R</sub> = 1.6 V; f = 1 MHz	C <sub>D</sub>	-	260	-	pF	



<b>ELECTRICAL CHARACTERISTICS GSOT04C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines	
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	=.	-	4	V	
Reverse voltage	at I <sub>R</sub> = 20 μA	$V_R$	4	-	=	V	
Reverse current	at V <sub>R</sub> = 4 V	I <sub>R</sub>	-	-	20	μΑ	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	5	6.1	7	V	
Deverse elemning veltage	at I <sub>PP</sub> = 1 A	V	-	7.5	9	V	
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	11.2	14.3	V	
Famound alamania a valta sa	at I <sub>PP</sub> = 1 A		-	1	1.2	V	
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>F</sub>	-	4.5	-	V	
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz		-	310	450	pF	
Capacitance	at V <sub>R</sub> = 2 V; f = 1 MHz	C <sub>D</sub>	-	200	-	pF	

<b>ELECTRICAL CHARACTERISTICS GSOT05C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	5	V		
Reverse voltage	at I <sub>R</sub> = 10 μA	$V_R$	5	-	-	V		
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6	6.8	8	V		
Payeres alamning voltage	at I <sub>PP</sub> = 1 A	V	-	7	8.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	12	16	V		
Famusard alamaning valtage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>F</sub>	-	4.5	-	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz		-	260	350	pF		
Capacitance	at V <sub>R</sub> = 2.5 V; f = 1 MHz	- C <sub>D</sub>	-	150	-	pF		

<b>ELECTRICAL CHARACTERISTICS GSOT08C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	=	-	8	V		
Reverse voltage	at I <sub>R</sub> = 5 μA	$V_R$	8	-	-	V		
Reverse current	at V <sub>R</sub> = 8 V	I <sub>R</sub>	=	-	5	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	9	10	11	V		
Deverse elemening velters	at I <sub>PP</sub> = 1 A	V	=	10.7	13	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	V <sub>C</sub>	=	15.2	19.2	V		
Forward alamaning valtage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	V <sub>F</sub>	-	3	-	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz		-	160	250	pF		
Capacitance	at V <sub>R</sub> = 4 V; f = 1 MHz	C <sub>D</sub>	ı	80	-	pF		



<b>ELECTRICAL CHARACTERISTICS GSOT12C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	12	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	12	-	-	V		
Reverse current	at V <sub>R</sub> = 12 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.5	15	16.5	V		
Poverse elemning voltage	at I <sub>PP</sub> = 1 A	V	-	15.4	18.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>C</sub>	-	21.2	26	V		
Famusard alamaning valtage	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>F</sub>	-	2.2	-	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	115	150	pF		
Capacitance	at V <sub>R</sub> = 6 V; f = 1 MHz		-	50	-	pF		

<b>ELECTRICAL CHARACTERISTICS GSOT15C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	15	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	15	-	-	V		
Reverse current	at V <sub>R</sub> = 15 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	16.5	18	20	V		
Deverse elements veltage	at I <sub>PP</sub> = 1 A	V	-	19.4	23.5	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	V <sub>C</sub>	-	24.8	28.8	V		
Forward clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>F</sub>	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	VF	-	1.8	-	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz		-	90	120	pF		
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz	C <sub>D</sub>	-	35	-	pF		

<b>ELECTRICAL CHARACTERISTICS GSOT24C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	24	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	24	-	-	V		
Reverse current	at V <sub>R</sub> = 24 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	27	30	33	V		
Deverse elemning veltage	at I <sub>PP</sub> = 1 A	V	-	34	41	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>C</sub>	-	41	47	V		
Farmer department and the second	at I <sub>PP</sub> = 1 A	V	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>F</sub>	-	1.4	-	V		
Oit	at V <sub>R</sub> = 0 V; f = 1 MHz	_	-	65	80	pF		
Capacitance	at V <sub>R</sub> = 12 V; f = 1 MHz	$C_D$	-	20	-	pF		

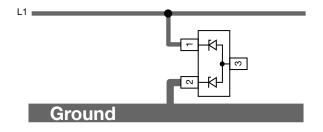


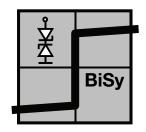
<b>ELECTRICAL CHARACTERISTICS GSOT36C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 3 or pin 2 to pin 3								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	36	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_{R}$	36	-	-	V		
Reverse current	at V <sub>R</sub> = 36 V	I <sub>R</sub>	-	-	1	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	39	43	47	V		
Deverse elemning veltage	at I <sub>PP</sub> = 1 A	V	-	49	60	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 3.5 A	V <sub>C</sub>	-	59	71	V		
Famous description with the	at I <sub>PP</sub> = 1 A	.,,	-	1	1.2	V		
Forward clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 3.5 A	V <sub>F</sub>	-	1.3	-	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	52	65	pF		
Capacitance	at V <sub>R</sub> = 18 V; f = 1 MHz		-	12	-	pF		

#### **BISy-MODE** (1-line bidirectional symmetrical protection mode)

If a bipolar symmetrical protection device is needed the GSOTxxC can also be used as a single line protection device. Therefore pin 1 has to be connected to the signal- or data-line (L1) and pin 2 to ground (or vice versa). Pin 3 must not be connected. Positive and negative voltage transients will be clamped in the same way. The clamping current through the GSOTxxC passes one diode in forward direction and the other one in reverse direction. The clamping voltage (V<sub>C</sub>) is defined by the breakthrough voltage (V<sub>BR</sub>) level of one diode plus the forward voltage of the other diode plus the voltage drop at the series impedances (resistances and inductances) of the protection device.

Due to the same clamping levels in positive and negative direction the GSOTxxC voltage clamping behaviour is bidirectional and symmetrical (BiSy).





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<b>ELECTRICAL CHARACTERISTICS GSOT03C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	3.8	V		
Reverse voltage	at I <sub>R</sub> = 100 μA	$V_R$	3.8	-	-	V		
Reverse current	at V <sub>R</sub> = 3.8 V	I <sub>R</sub>	-	-	100	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	4.5	5.3	6.2	V		
Payaraa alamaina valtaga	at I <sub>PP</sub> = 1 A	W	-	7	8.4	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	14	16.8	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	210	300	pF		
Capacitance	at V <sub>R</sub> = 1.6 V; f = 1 MHz		-	190	-	pF		



<b>ELECTRICAL CHARACTERISTICS GSOT04C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	4.5	V		
Reverse voltage	at I <sub>R</sub> = 20 μA	$V_R$	4.5	-	-	V		
Reverse current	at V <sub>R</sub> = 4.5 V	I <sub>R</sub>	-	-	20	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	5.5	6.8	7.7	V		
Payaraa alamaina valtaga	at I <sub>PP</sub> = 1 A	V	-	7.5	9	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 30 A	V <sub>C</sub>	-	15.7	18.8	V		
Capacitance $ at V_R = 0 V; f = 1 MHz $ $ at V_R = 2 V; f = 1 MHz $	at V <sub>R</sub> = 0 V; f = 1 MHz	0	-	155	225	pF		
	C <sub>D</sub>	ı	135	-	pF			

<b>ELECTRICAL CHARACTERISTICS GSOT05C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	ı	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	ı	-	5.5	V		
Reverse voltage	at I <sub>R</sub> = 10 μA	$V_R$	5.5	-	-	V		
Reverse current	at V <sub>R</sub> = 5.5 V	I <sub>R</sub>	ı	-	10	μΑ		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6.5	7.5	8.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	8.1	9.7	V		
neverse clamping voltage	at $I_{PP} = I_{PPM} = 30 \text{ A}$	v <sub>C</sub>	ı	17	20.4	٧		
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$		ı	130	175	pF		
Capacitance	$at V_R = 4 V; f = 1 MHz$	-	100	-	pF			

<b>ELECTRICAL CHARACTERISTICS GSOT08C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	ı	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	ı	1	8.5	V		
Reverse voltage	at I <sub>R</sub> = 5 μA	$V_R$	8.5	ı	-	V		
Reverse current	at V <sub>R</sub> = 8.5 V	I <sub>R</sub>	ı	-	5	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	9.5	10.7	11.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	ı	11.7	14	V		
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 18 A	v <sub>C</sub>	ı	18.5	22.2	V		
0	at $V_R = 0 V$ ; $f = 1 MHz$	0	-	80	125	pF		
Capacitance	at $V_R = 4 V$ ; $f = 1 MHz$	C <sub>D</sub>	- 1	60	-	pF		

<b>ELECTRICAL CHARACTERISTICS GSOT12C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	12.5	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	12.5	-	-	V		
Reverse current	at V <sub>R</sub> = 12.5 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	13.5	15.7	16.5	V		
Payaraa alamaina valtaga	at I <sub>PP</sub> = 1 A	W	-	16.4	19.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 12 A	V <sub>C</sub>	-	23.4	28.1	V		
Canacitanas	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	58	75	pF		
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz		-	36	-	pF		



<b>ELECTRICAL CHARACTERISTICS GSOT15C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	15.5	V		
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	15.5	-	-	V		
Reverse current	at V <sub>R</sub> = 15.5 V	I <sub>R</sub>	-	-	1	μA		
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	17	18.7	20.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	W	-	20.4	24.5	V		
neverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 8 A	V <sub>C</sub>	-	26.6	30.6	V		
On a sufficient	at V <sub>R</sub> = 0 V; f = 1 MHz	_	-	45	60	pF		
Capacitance	at V <sub>R</sub> = 7.5 V; f = 1 MHz	C <sub>D</sub>	-	25	-	pF		

<b>ELECTRICAL CHARACTERISTICS GSOT24C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	24.5	V			
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	24.5	-	-	V			
Reverse current	at V <sub>R</sub> = 24.5 V	I <sub>R</sub>	-	-	1	μΑ			
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	27.5	30.7	33.7	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	- V <sub>C</sub>	-	34	41	V			
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A		-	40	48	V			
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	- C <sub>D</sub>	-	33	40	pF			
	at V <sub>R</sub> = 12 V; f = 1 MHz		-	18	-	pF			

<b>ELECTRICAL CHARACTERISTICS GSOT36C</b> (T <sub>amb</sub> = 25 °C unless otherwise specified) between pin 1 to pin 2 or pin 2 to pin1; pin 3 not connected										
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT				
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines				
Reverse stand-off voltage	Max. reverse working voltage	$V_{RWM}$	-	-	36.5	V				
Reverse voltage	at I <sub>R</sub> = 1 μA	$V_R$	36.5	1	-	V				
Reverse current	at V <sub>R</sub> = 36.5 V	I <sub>R</sub>	-	-	1	μΑ				
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	39.5	43.7	47.7	V				
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	50	60	V				
	at $I_{PP} = I_{PPM} = 3.5 \text{ A}$		ı	60	72	V				
Capacitance	at $V_R = 0 V$ ; $f = 1 MHz$	C <sub>D</sub>	-	26	33	pF				
	at V <sub>R</sub> = 18 V; f = 1 MHz		-	10	-	pF				



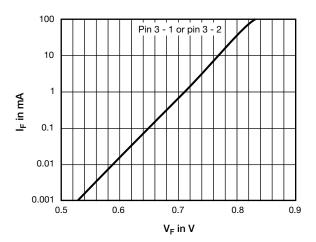


Fig. 1 - Typical Forward Current  $I_F$  vs. Forward Voltage  $V_F$ 

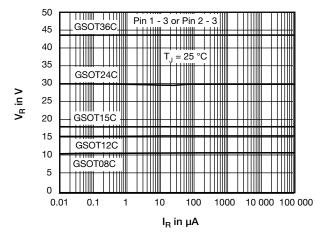


Fig. 2 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

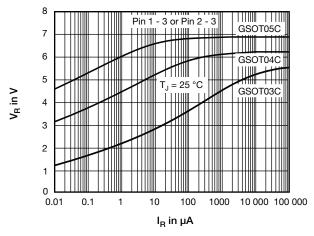
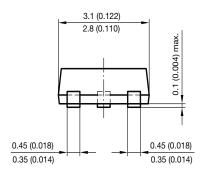
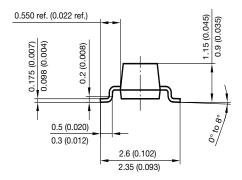
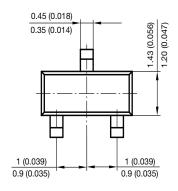


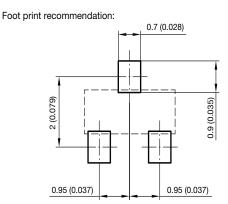
Fig. 3 - Typical Reverse Voltage  $V_{\text{R}}$  vs. Reverse Current  $I_{\text{R}}$ 

#### PACKAGE DIMENSIONS in millimeters (inches): SOT-23

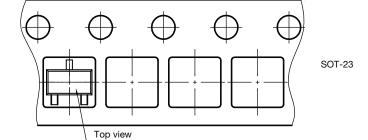








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Orientation in carrier tape SOT-23 S8-V-3929.01-006 (4) 04.02.2010 22607

Unreeling direction



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GSOT08C-HE3-08 GSOT15C-HE3-08 GSOT15C-E3-08 GSOT24C-E3-08 GSOT08C-E3-08 GSOT15C-HG3-08
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