



DMP3017SFG

30V P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
201/	10mΩ @ V _{GS} = -10V	-11.5A
-30V	$18m\Omega$ @ $V_{GS} = -4.5V$	-8.7A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low R_{DS(ON)} Ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMP3017SFGQ</u>)

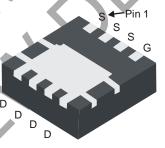
Mechanical Data

- Case: PowerDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
 Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 [©]3
- Weight: 0.072 grams (Approximate)



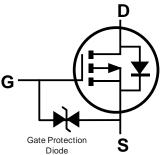






Bottom View





Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3017SFG-7	PowerDI3333-8	2,000/Tape & Reel
DMP3017SFG-13	PowerDI3333-8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



P17 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 = 2017) WW = Week Code (01 to 53)



DMP3017SFG

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Prain Current (Alata C) V 40V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-11.5 -9.4	А
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-15.2 -12.1	А
Maximum Continuous Body Diode Forward Current	Is	-3.0	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	-80	Α		
Avalanche Current (Note 7) L = 1mH	I _{AR}	-14	Α		
Repetitive Avalanche Energy (Note 7) L = 1mH	E _{AR}	104	mJ		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	P _D	0.94	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	137	°C/W	
Themai resistance, canonem to runisione (rete o)	t < 10s	TOJA	82	°C/W	
Total Power Dissipation (Note 6)	$T_A = +25$ °C		2.2	W	
Total I owel bissipation (Note o)	$T_A = +70^{\circ}C$	PD	1.3	٧٧	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	60	°C/W	
Thermal Resistance, suriction to Ambient (Note o)	t < 10s	NejA	36	°C/W	
Thermal Resistance, Junction to Case (Note 6)		Reuc	3.0	°C/W	
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C		

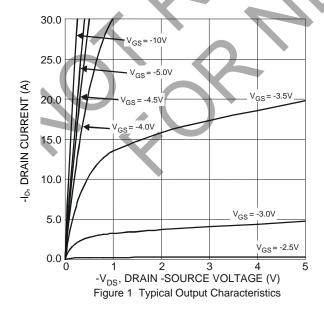


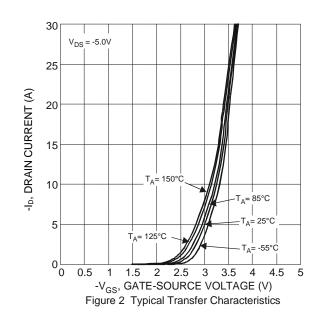
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage		-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current		_	_	-1	μA	V _{DS} = -24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 25V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	_	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
Static Drain-Source On-Resistance	D		8.5	10	mΩ	$V_{GS} = -10V, I_D = -11.5A$
Static Dialif-Source Off-Nesistance	R _{DS(ON)}	-	15	18		$V_{GS} = -4.5V$, $I_D = -8.5A$
Forward Transfer Admittance	Y _{fs}		24		S	$V_{DS} = -5V$, $I_D = -11.5A$
DYNAMIC CHARACTERISTICS (Note 9)	_					
Input Capacitance	C _{iss}		2246	_	pF	45/4 // 0/4
Output Capacitance	Coss		352		pF	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		294	1-	pF	1 = 1.0WHZ
Gate Resistance	Rg	1	5.1	12	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
Total Gate Charge (V _{GS} = -5V)	Q_g	1	20.5	7	nC	
Total Gate Charge (V _{GS} = -10V)		_	41	_	nC	V _{DS} = -15V, I _D = -11.5A
Gate-Source Charge		-	7.6	<u> </u>	nC	V _{DS} = -15V, I _D = -11.5A
Gate-Drain Charge	Q_{gd}		8.0	_	nC	
Turn-On Delay Time	t _{D(ON)}	6	7.5	-(ns	
Turn-On Rise Time	t _R	-	15.4		ns	V _{DD} = -15V, V _{GS} = -10V,
Turn-Off Delay Time	t _{D(OFF)}	1-1	45.6	*	ns	$R_G = 6\Omega$, $I_D = -11.5A$
Turn-Off Fall Time	tF	7 -	36.8	V - /	ns]
BODY DIODE CHARACTERISTICS						
Diode Forward Voltage	V_{SD}		-0.7) –	V	$V_{GS} = 0V, I_{S} = -1A$
Reverse Recovery Time (Note 9)		4	20	_	ns	1- 44 EA dI/dt 400A/:
Reverse Recovery Charge (Note 9)	Q _{RR}	1-0	9.5	_	nC	I _S = -11.5A, dI/dt = 100A/μs

Notes:

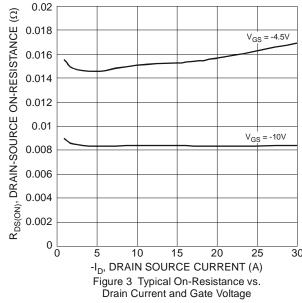
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- Better finding drift a state of coding 202 copper, with find square copper.
 I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

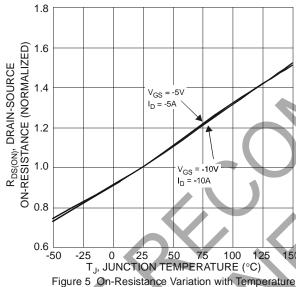


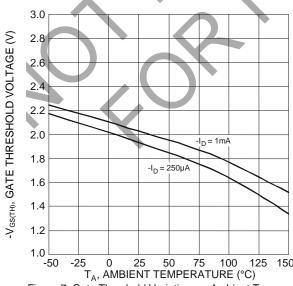


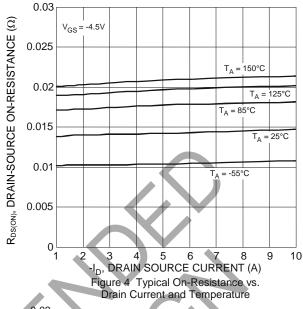


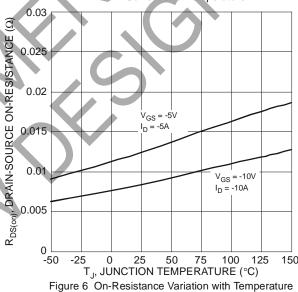
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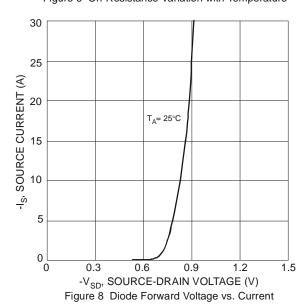






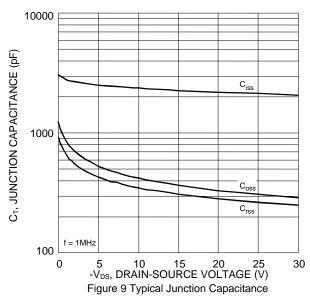


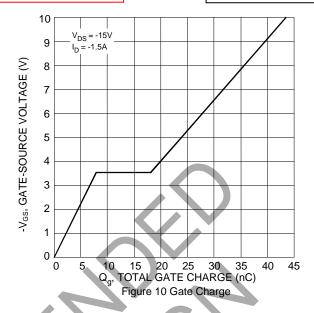


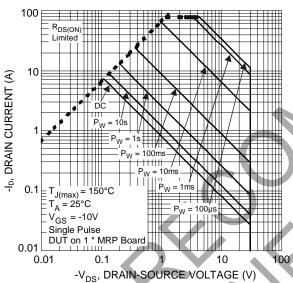


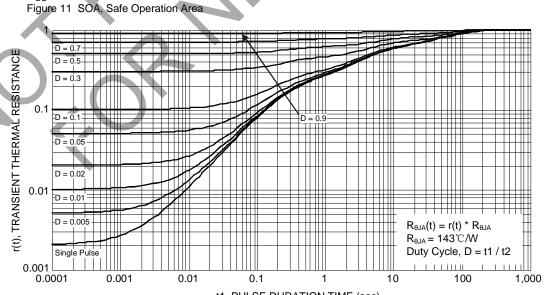


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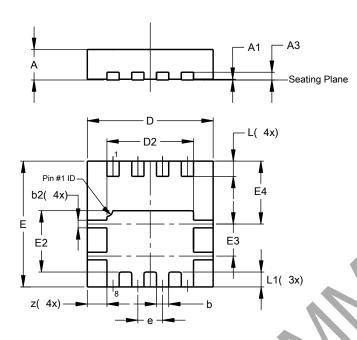




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8

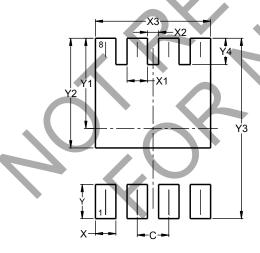


PowerDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	_	_	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
ш	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
е	_	_	0.65		
L	0.35	0.45	0.40		
L1	_	_	0.39		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)		
С	0.650		
Х	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



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