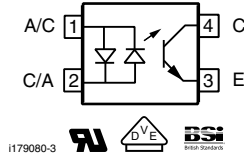
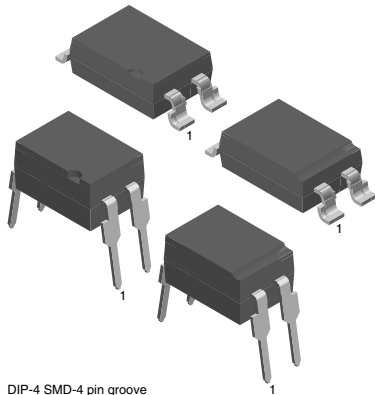




## Optocoupler, Phototransistor Output, AC Input, Low Input Current



1179080-3



DIP-4 SMD-4 pin groove

### DESCRIPTION

The SFH628A (DIP) and SFH6286 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared emitting diode, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm are achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400 V<sub>RMS</sub> or DC.

### FEATURES

- High common mode interference immunity
- Isolation test voltage, 5300 V<sub>RMS</sub>
- Low coupling capacitance
- Good CTR linearity depending on forward current
- Low CTR degradation
- High collector emitter voltage, V<sub>CEO</sub> = 55 V
- Material categorization: For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



RoHS COMPLIANT

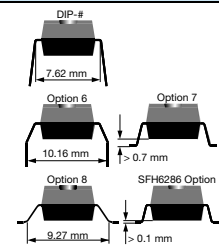
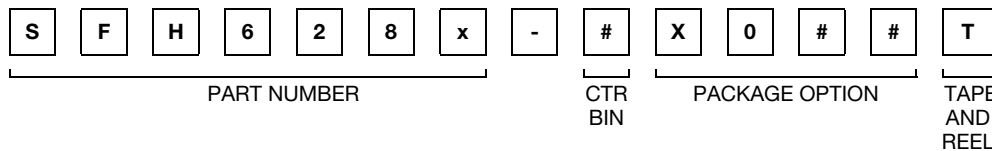
### APPLICATIONS

- Telecom
- Industrial controls
- Battery powered equipment
- Office machines

### AGENCY APPROVALS

- UL1577, file no. E52744 system code H
- DIN EN 60747-5-5 (VDE0884-5), available with option 1
- BSI IEC 60950; IEC 60065

### ORDERING INFORMATION



AGENCY CERTIFIED/ PACKAGE	CTR (%)					
	SFH628A			SFH6286		
<b>UL, BSI</b>	<b>63 to 200</b>	<b>100 to 320</b>	<b>160 to 500</b>	<b>63 to 200</b>	<b>100 to 320</b>	<b>160 to 500</b>
DIP-4	SFH628A-2T <sup>(1)</sup>	SFH628A-3	SFH628A-4	-	-	-
SFH6286 option	-	-	-	SFH6286-2T <sup>(1)</sup>	SFH6286-3T <sup>(1)</sup>	SFH6286-4T <sup>(1)</sup>
<b>VDE, UL, BSI</b>	<b>63 to 200</b>	<b>100 to 320</b>	<b>160 to 500</b>	<b>63 to 200</b>	<b>100 to 320</b>	<b>160 to 500</b>
DIP-4	-	SFH628A-3X001	-	-	-	-
DIP-4, 400 mil, option 6	SFH628A-2X016	SFH628A-3X016	SFH628A-4X016	-	-	-
SMD-4, option 7	-	SFH628A-3X017T	-	-	-	-
SMD-4, option 8	SFH628A-2X018	-	-	-	-	-
SFH6286 option	-	-	-	SFH6286-2X001T	SFH6286-3X001T	SFH6286-4X001T <sup>(1)</sup>

### Notes

- Additional options may be possible, please contact sales office.
- <sup>(1)</sup> Also available in tubes; do not add T to end.



ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
<b>INPUT</b>				
DC forward current		I <sub>F</sub>	± 50	mA
Surge forward current	t ≤ 10 μs	I <sub>FSM</sub>	± 2.5	A
Power dissipation		P <sub>diss</sub>	76	mW
<b>OUTPUT</b>				
Collector emitter voltage		V <sub>CEO</sub>	55	V
Emitter collector voltage		V <sub>ECO</sub>	7	V
Collector current		I <sub>C</sub>	50	mA
	t <sub>p</sub> ≤ 1 ms	I <sub>C</sub>	100	mA
Power dissipation		P <sub>diss</sub>	150	mW
<b>COUPLER</b>				
Isolation test voltage		V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Creepage distance			≥ 7	mm
Clearance distance			≥ 7	mm
Insulation thickness between			≥ 0.4	mm
Comparative tracking index per DIN IEC112/			175	
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C
Ambient temperature range		T <sub>amb</sub>	-55 to +100	°C
Soldering temperature <sup>(1)</sup>	max. 10 s, dip soldering distance	T <sub>slid</sub>	260	°C

**Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- <sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

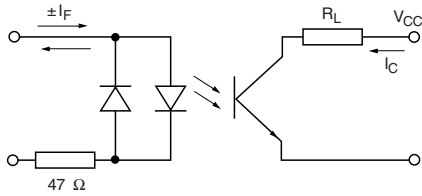
ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>							
Forward voltage	I <sub>F</sub> = ± 5 mA		V <sub>F</sub>		1.1	1.5	V
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>O</sub>		45		pF
Thermal resistance			R <sub>thja</sub>		1070		K/W
<b>OUTPUT</b>							
Collector emitter leakage current	V <sub>CE</sub> = 10 V		I <sub>CEO</sub>		10	200	nA
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>CE</sub>		7		pF
Thermal resistance			R <sub>thja</sub>		500		K/W
<b>COUPLER</b>							
Collector emitter saturation voltage	I <sub>F</sub> = ± 1 mA, I <sub>C</sub> = 0.5 mA	SFH628A-2	V <sub>CEsat</sub>		0.25	0.4	V
		SFH6286-2	V <sub>CEsat</sub>		0.25	0.4	V
	I <sub>F</sub> = ± 1 mA, I <sub>C</sub> = 0.8 mA	SFH628A-3	V <sub>CEsat</sub>		0.25	0.4	V
		SFH6286-3	V <sub>CEsat</sub>		0.25	0.4	V
	I <sub>F</sub> = ± 1 mA, I <sub>C</sub> = 1.25 mA	SFH628A-4	V <sub>CEsat</sub>		0.25	0.4	V
		SFH6286-4	V <sub>CEsat</sub>		0.25	0.4	V

**Note**

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

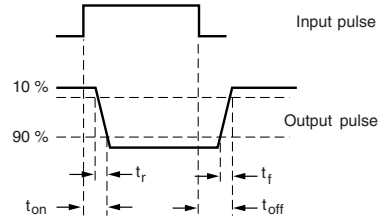
CURRENT TRANSFER RATIO ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
$I_C/I_F$	$I_F = \pm 1\text{ mA}$ , $V_{CE} = 0.5\text{ V}$	SFH628A-2	CTR	63		200	%
		SFH6286-2	CTR	63		200	%
	$I_F = \pm 0.5\text{ mA}$ , $V_{CE} = 1.5\text{ V}$	SFH628A-2	CTR	32	100		%
		SFH6286-2	CTR	32	100		%
	$I_F = \pm 1\text{ mA}$ , $V_{CE} = 0.5\text{ V}$	SFH628A-3	CTR	100		320	%
		SFH6286-3	CTR	100		320	%
	$I_F = \pm 0.5\text{ mA}$ , $V_{CE} = 1.5\text{ V}$	SFH628A-3	CTR	50	160		%
		SFH6286-3	CTR	50	160		%
	$I_F = \pm 1\text{ mA}$ , $V_{CE} = 0.5\text{ V}$	SFH628A-4	CTR	160		500	%
		SFH6286-4	CTR	160		500	%
	$I_F = \pm 0.5\text{ mA}$ , $V_{CE} = 1.5\text{ V}$	SFH628A-4	CTR	80	250		%
		SFH6286-4	CTR	80	250		%

SWITCHING CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn-on time	$V_{CC} = 5\text{ V}$ , $I_C = 2\text{ mA}$ , $R_L = 100\text{ }\Omega$	$t_{on}$		6		$\mu\text{s}$	
Rise time	$V_{CC} = 5\text{ V}$ , $I_C = 2\text{ mA}$ , $R_L = 100\text{ }\Omega$	$t_r$		3.5		$\mu\text{s}$	
Turn-off time	$V_{CC} = 5\text{ V}$ , $I_C = 2\text{ mA}$ , $R_L = 100\text{ }\Omega$	$t_{off}$		5.5		$\mu\text{s}$	
Fall time	$V_{CC} = 5\text{ V}$ , $I_C = 2\text{ mA}$ , $R_L = 100\text{ }\Omega$	$t_f$		5		$\mu\text{s}$	



isfh618a\_11

Fig. 1 - Test Circuit



isfh618a\_12

Fig. 2 - Test Circuit and Waveforms

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification	according to IEC 68 part 1			55/100/21			
Comparative tracking index		CTI	175		399		
$V_{IOTM}$			10000			V	
$V_{IORM}$			890			V	
$P_{SO}$					400	mW	
$I_{SI}$					275	mA	
$T_{SI}$					175	$^{\circ}\text{C}$	
Creepage distance	Standard DIP-8		7			mm	
Clearance distance	Standard DIP-8		7			mm	
Creepage distance	400 mil DIP-8		8			mm	
Clearance distance	400 mil DIP-8		8			mm	
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.4			mm	

**Note**

- As per IEC 60747-5-5, § 7.4.3.8.1, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

**TYPICAL CHARACTERISTICS** ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

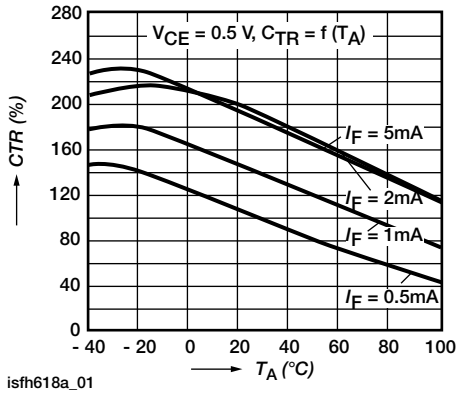


Fig. 3 - Current Transfer Ratio (typ.)

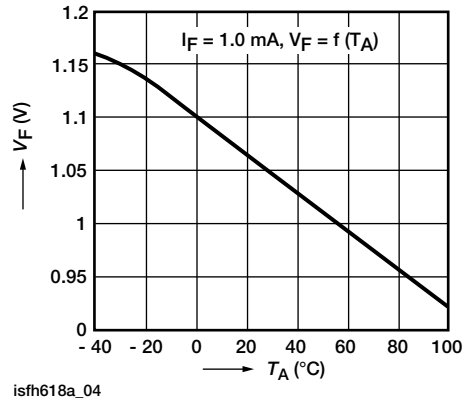


Fig. 6 - Diode Forward Voltage (typ.)

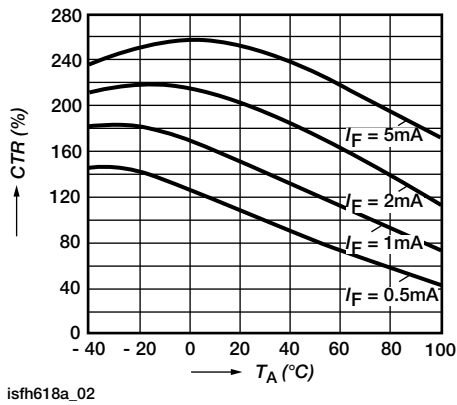


Fig. 4 - Current Transfer Ratio (typ.)

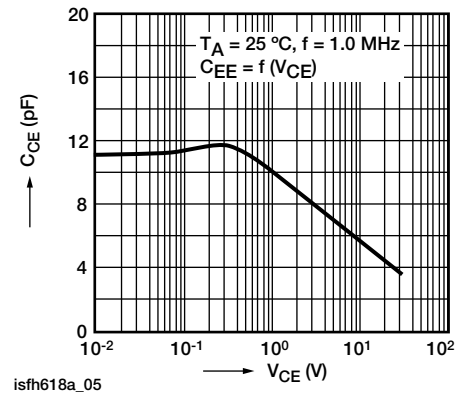


Fig. 7 - Transistor Capacitance

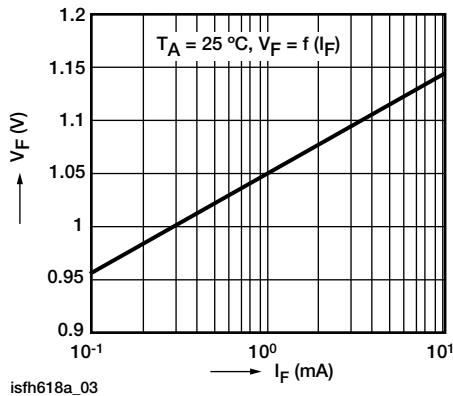


Fig. 5 - Diode Forward Voltage (typ.)

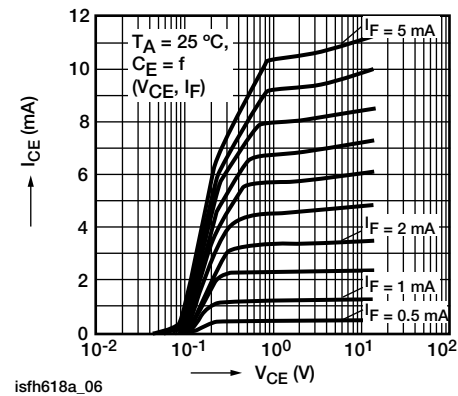


Fig. 8 - Output Characteristics

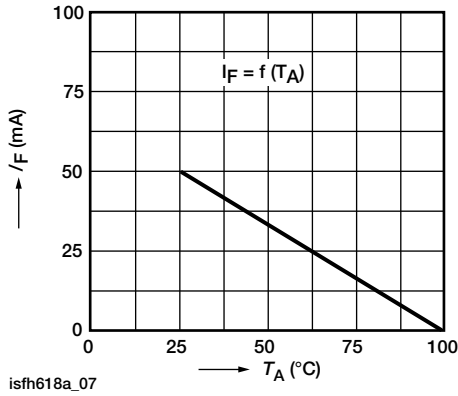


Fig. 9 - Permissible Forward Current Diode

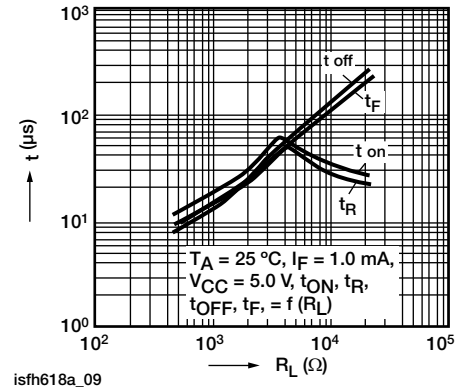


Fig. 11 - Switching Times (Typ.)

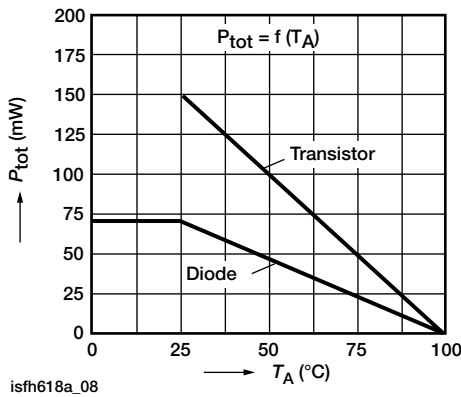
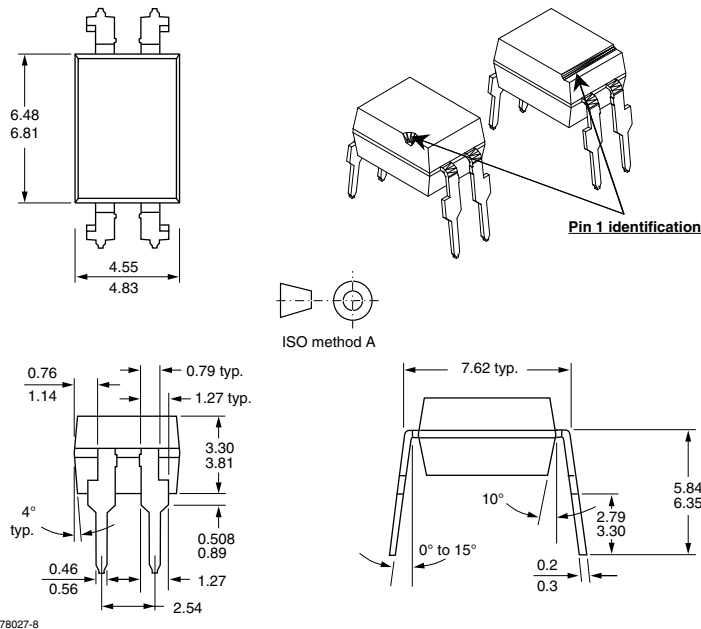
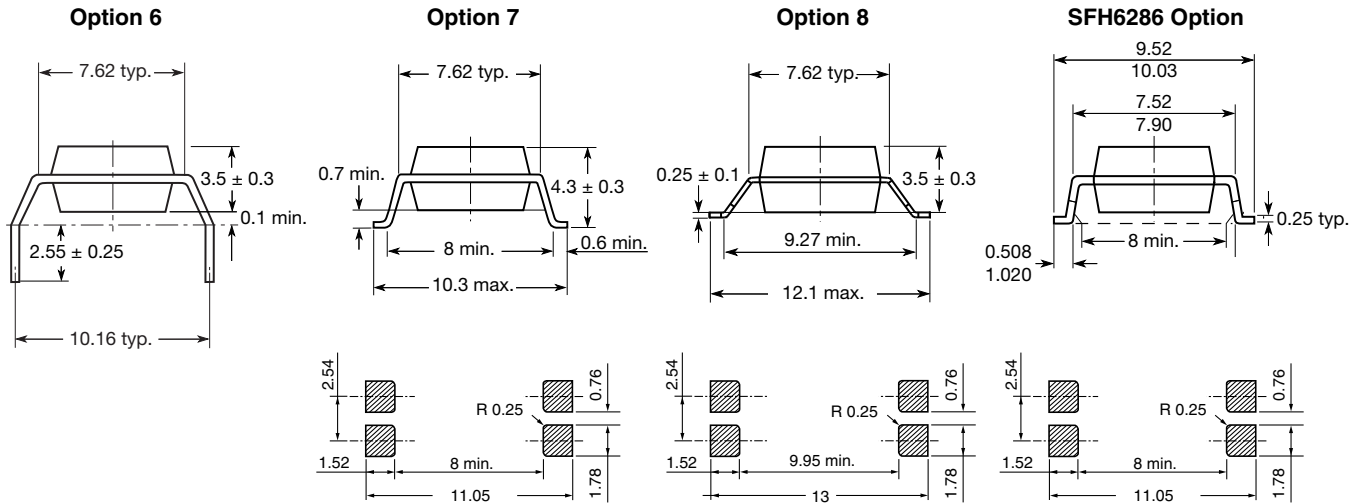


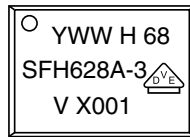
Fig. 10 - Permissible Power Dissipation

## PACKAGE DIMENSIONS in millimeters





### PACKAGE MARKING (Example)



### Notes

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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