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November 2015

## **FDPF190N15A**

# N-Channel PowerTrench<sup>®</sup> MOSFET 150 V, 27.4 A, 19 m $\Omega$

#### **Features**

- $R_{DS(on)}$  = 14.7 m $\Omega$  (Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 27.4 A
- Low Gate Charge, Q<sub>G</sub> = 31 nC (Typ.)
- Low C<sub>rss</sub> (Typ. 56 pF)
- · Fast Switching Speed
- · Improved dv/dt Capability
- · RoHS Compliant

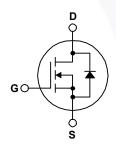
#### Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

#### **Applications**

- · Consumer Appliances
- LED TV
- · Synchronous Rectification for ATX / Sever / Telecom PSU
- · Uninterruptible Power Supply
- · Micro Solar Inverter





#### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

| Symbol                            |                            | Parameter                             |            | FDPF190N15A | Unit |
|-----------------------------------|----------------------------|---------------------------------------|------------|-------------|------|
| $V_{DSS}$                         | Drain to Source Voltage    |                                       |            | 150         | V    |
| V                                 | Onto to Onione Walterin    | - DC                                  |            | ±20         | V    |
| $V_{GSS}$                         | Gate to Source Voltage     | - AC                                  | (f > 1 Hz) | ±30         | V    |
|                                   | Drain Current              | - Continuous (T <sub>C</sub> = 25°C)  |            | 27.4        | Α    |
| ID                                | Drain Current              | - Continuous (T <sub>C</sub> = 100°C) |            | 17.4        | _ A  |
| I <sub>DM</sub>                   | Drain Current              | - Pulsed                              | (Note 1)   | 110         | Α    |
| E <sub>AS</sub>                   | Single Pulsed Avalanche En | ergy                                  | (Note 2)   | 261         | mJ   |
| dv/dt                             | Peak Diode Recovery dv/dt  |                                       | (Note 3)   | 6.0         | V/ns |
| D                                 | Power Dissipation          | (T <sub>C</sub> = 25°C)               |            | 33          | W    |
| $P_{D}$                           | Power Dissipation          | - Derate Above 25°C                   |            | 0.26        | W/°C |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temp | perature Range                        |            | -55 to +150 | °C   |
| TL                                | Maximum Lead Temperature   | for Soldering, 1/8" from Case for 5 S | Seconds    | 300         | °C   |

#### **Thermal Characteristics**

| Symbol          | Parameter                                     | FDPF190N15A | Unit  |
|-----------------|-----------------------------------------------|-------------|-------|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case, Max.    | 3.3         | °C/W  |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient, Max. | 62.5        | *C/VV |

## **Package Marking and Ordering Information**

| Part Number | Top Mark    | Package | Packing Method | Reel Size | Tape Width | Quantity |
|-------------|-------------|---------|----------------|-----------|------------|----------|
| FDPF190N15A | FDPF190N15A | TO-220F | Tube           | N/A       | N/A        | 50 units |

## **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

| Symbol                                  | Parameter                                    | Test Conditions                                       | Min. | Тур. | Max. | Unit |
|-----------------------------------------|----------------------------------------------|-------------------------------------------------------|------|------|------|------|
| Off Charac                              | cteristics                                   |                                                       |      |      |      |      |
| BV <sub>DSS</sub>                       | Drain to Source Breakdown Voltage            | $I_D = 250 \mu A, V_{GS} = 0 V$                       | 150  | -    | -    | V    |
| ΔBV <sub>DSS</sub><br>/ ΔT <sub>J</sub> | Breakdown Voltage Temperature<br>Coefficient | $I_D$ = 250 μA, Referenced to 25°C                    | -    | 0.14 | -    | V/°C |
| I                                       |                                              | V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V        | -    | -    | 1    | μА   |
| Zero Gate Voltage Dra                   | Zero Gate Voltage Drain Current              | $V_{DS} = 120 \text{ V}, T_{C} = 150^{\circ}\text{C}$ | -    | -    | 500  | μΑ   |
| I <sub>GSS</sub>                        | Gate to Body Leakage Current                 | V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V        | -    | -    | ±100 | nA   |

#### On Characteristics

| V <sub>GS(th)</sub> | Gate Threshold Voltage               | $V_{GS} = V_{DS}, I_{D} = 250 \mu A$            | 2.0 | -    | 4.0  | V  |
|---------------------|--------------------------------------|-------------------------------------------------|-----|------|------|----|
| R <sub>DS(on)</sub> | Static Drain to Source On Resistance | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 27.4 A | -   | 14.7 | 19.0 | mΩ |
| g <sub>FS</sub>     | Forward Transconductance             | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 27.4 A | -   | 64   | -    | S  |

#### **Dynamic Characteristics**

| C <sub>iss</sub>     | Input Capacitance                  | V 05.V.V 0.V                                                       | - | 2020 | 2685 | pF |
|----------------------|------------------------------------|--------------------------------------------------------------------|---|------|------|----|
| Coss                 | Output Capacitance                 | $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$ | - | 700  | 930  | pF |
| C <sub>rss</sub>     | Reverse Transfer Capacitance       | 1 - 1 1011 12                                                      | - | 56   | 85   | pF |
| C <sub>oss(er)</sub> | Energy Related Output Capacitance  | V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V                      | - | 252  | -    | pF |
| Q <sub>g(tot)</sub>  | Total Gate Charge at 10V           | V <sub>DS</sub> = 120 V, I <sub>D</sub> = 27.4 A,                  | - | 30   | 39   | nC |
| Q <sub>gs</sub>      | Gate to Source Gate Charge         | V <sub>GS</sub> = 10 V                                             | - | 8.8  | -    | nC |
| $Q_{gd}$             | Gate to Drain "Miller" Charge      | (Note 4)                                                           | - | 7.3  | -    | nC |
| ESR                  | Equivalent Series Resistance (G-S) | f = 1 MHz                                                          | - | 1.5  | -    | Ω  |

#### **Switching Characteristics**

| t <sub>d(on)</sub>  | Turn-On Delay Time  |                                                |          | -   | 18 | 46 | ns |
|---------------------|---------------------|------------------------------------------------|----------|-----|----|----|----|
| t <sub>r</sub>      | Turn-On Rise Time   | $V_{DD} = 75 \text{ V}, I_D = 27.4 \text{ A},$ |          | - / | 16 | 42 | ns |
| t <sub>d(off)</sub> | Turn-Off Delay Time | $V_{GS} = 10 \text{ V}, R_G = 4.7 \Omega$      |          | -/  | 32 | 74 | ns |
| t <sub>f</sub>      | Turn-Off Fall Time  |                                                | (Note 4) | -   | 8  | 26 | ns |

#### **Drain-Source Diode Characteristics**

| I <sub>S</sub>  | Maximum Continuous Drain to Source Diod              | Maximum Continuous Drain to Source Diode Forward Current      |   | -    | 27.4 | Α  |
|-----------------|------------------------------------------------------|---------------------------------------------------------------|---|------|------|----|
| I <sub>SM</sub> | Maximum Pulsed Drain to Source Diode Forward Current |                                                               | - | -    | 110  | Α  |
| $V_{SD}$        | Drain to Source Diode Forward Voltage                | V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 27.4 A               | - | -    | 1.3  | V  |
| t <sub>rr</sub> | Reverse Recovery Time                                | V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 27.4 A,              | - | 76   | -    | ns |
| Q <sub>rr</sub> | Reverse Recovery Charge                              | $dI_F/dt = 100 \text{ A/}\mu\text{s}, V_{DD} = 120 \text{ V}$ | - | 0.18 | -    | μС |

#### Notes

- 1. Repetitive rating: pulse-width limited by maximum junction temperature.
- 2. L = 0.33 mH, I<sub>AS</sub> = 29 A, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C.
- 3. I<sub>SD</sub>  $\leq$  27.4 A, di/dt  $\leq$  200 A/ $\mu$ s, V<sub>DD</sub>  $\leq$  BV<sub>DSS</sub>, starting T<sub>J</sub> = 25°C.
- 4. Essentially independent of operating temperature typical characteristics.

## **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

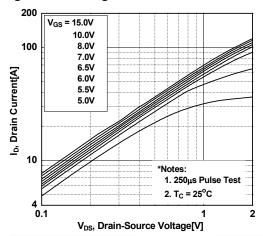


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

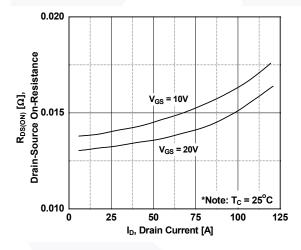


Figure 5. Capacitance Characteristics

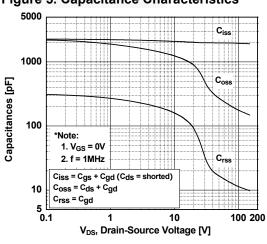


Figure 2. Transfer Characteristics

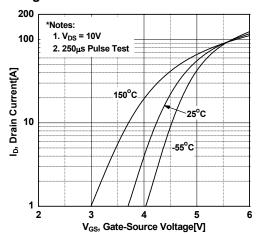


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

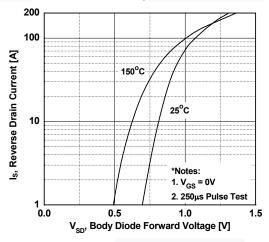
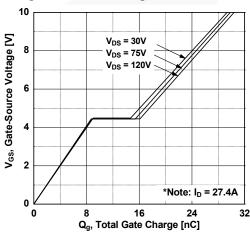


Figure 6. Gate Charge Characteristics



### **Typical Performance Characteristics** (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

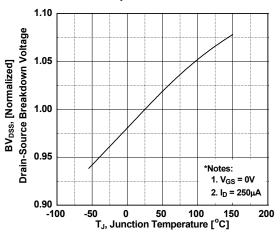


Figure 8. On-Resistance Variation vs. Temperature

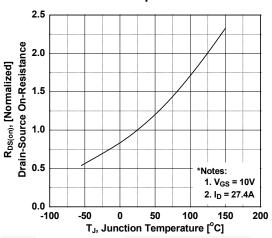


Figure 9. Maximum Safe Operating Area

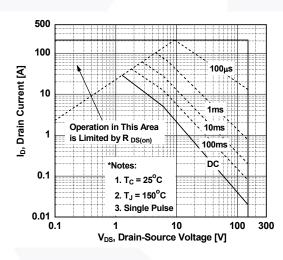


Figure 10. Maximum Drain Current vs. Case Temperature

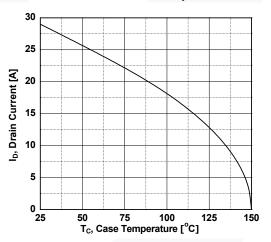
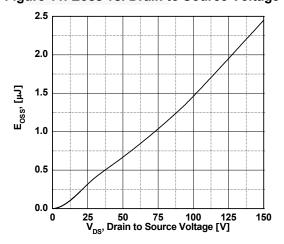
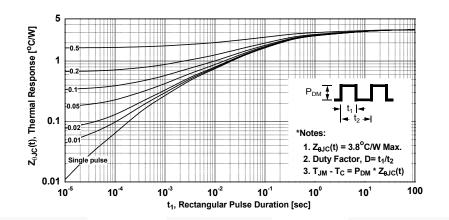


Figure 11. Eoss vs. Drain to Source Voltage



## **Typical Performance Characteristics** (Continued)





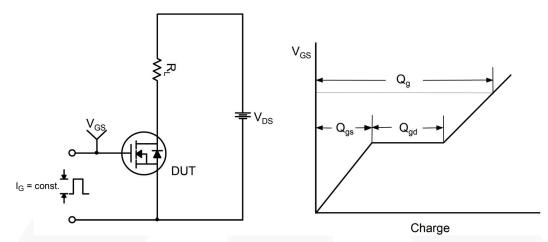


Figure 13. Gate Charge Test Circuit & Waveform

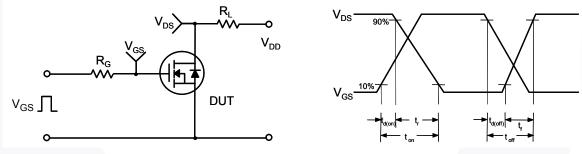


Figure 14. Resistive Switching Test Circuit & Waveforms

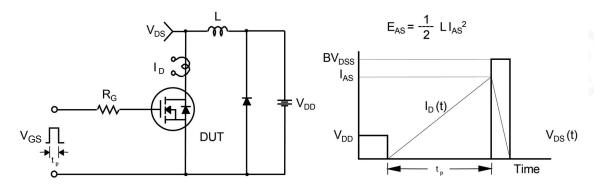


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

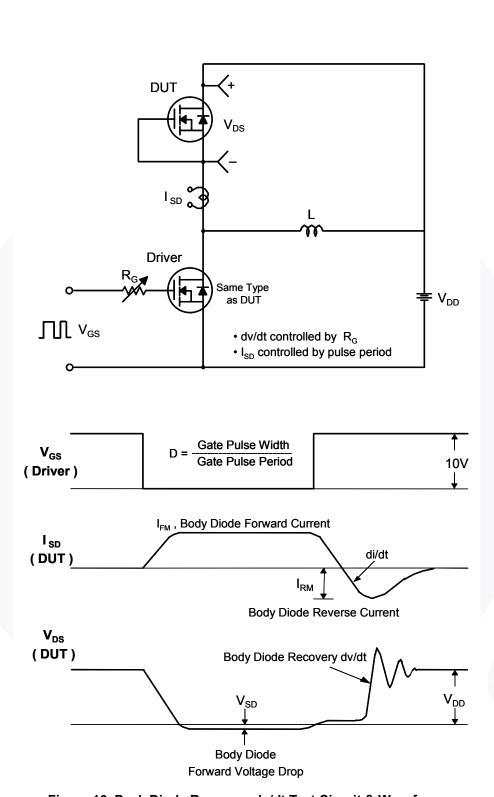
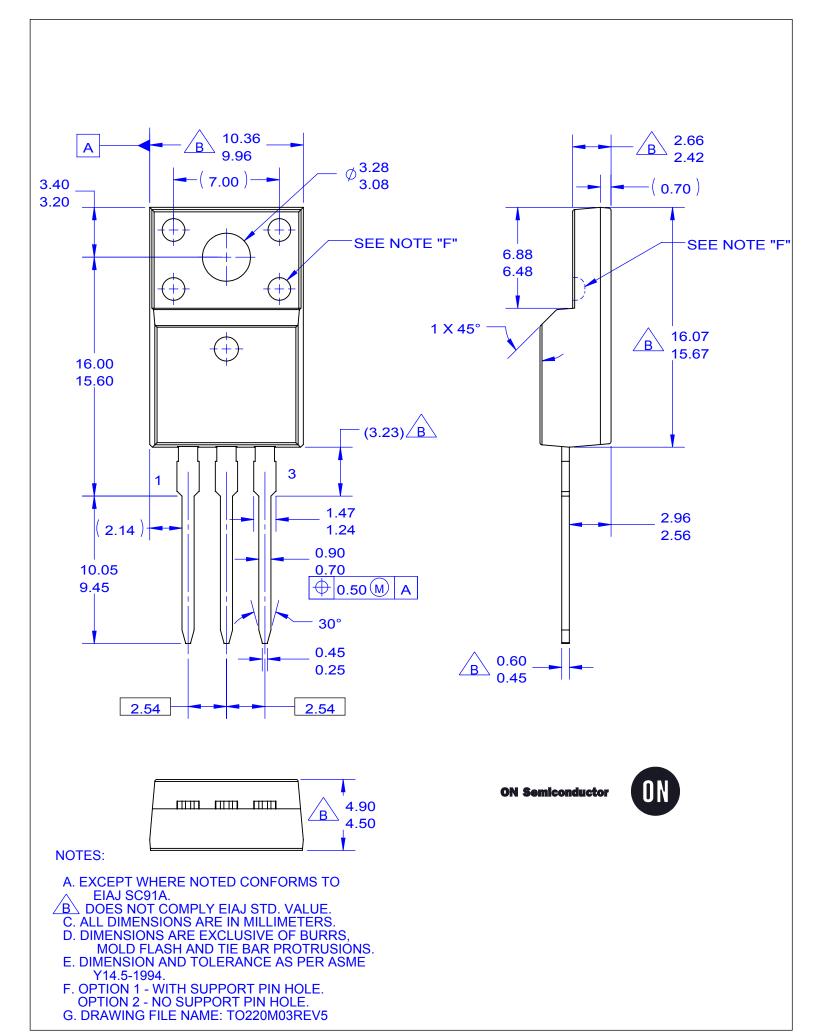


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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