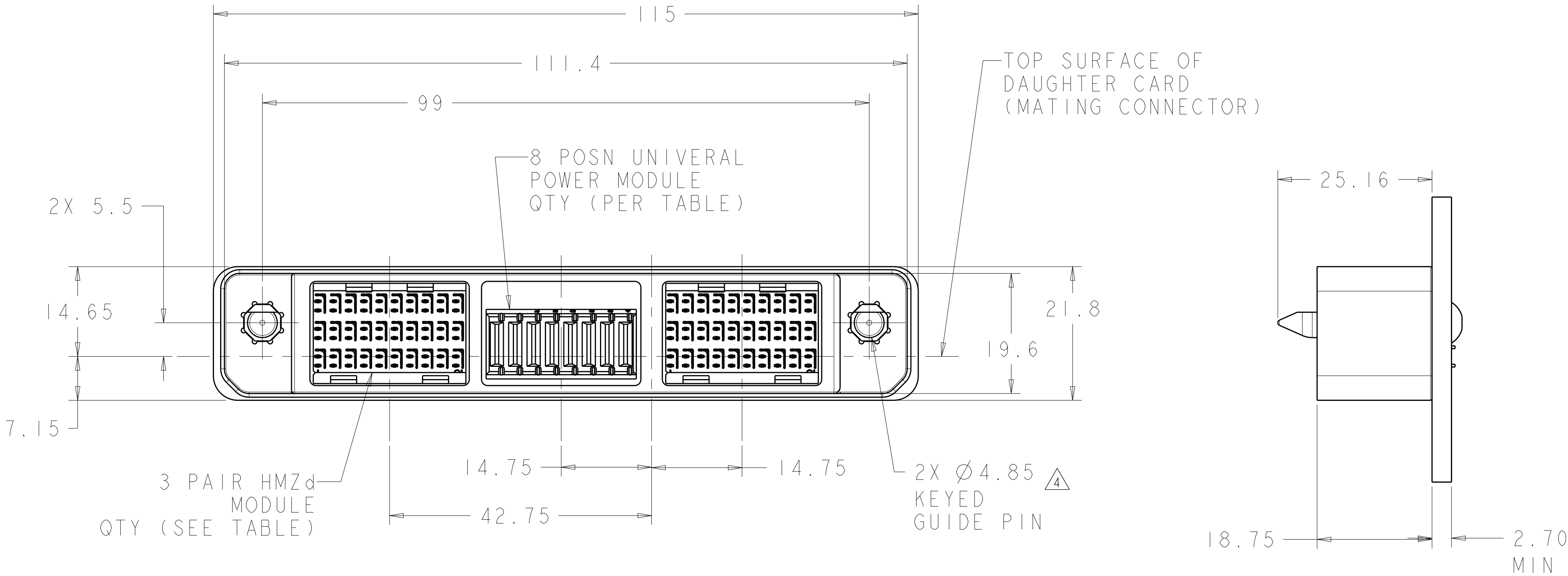


LOC		DIST		REVISIONS					
-	-	P	LTR	DESCRIPTION		DATE	DWN	APVD	
			G	REV PER ECO 15-010033		7-10-15	CT	DH	



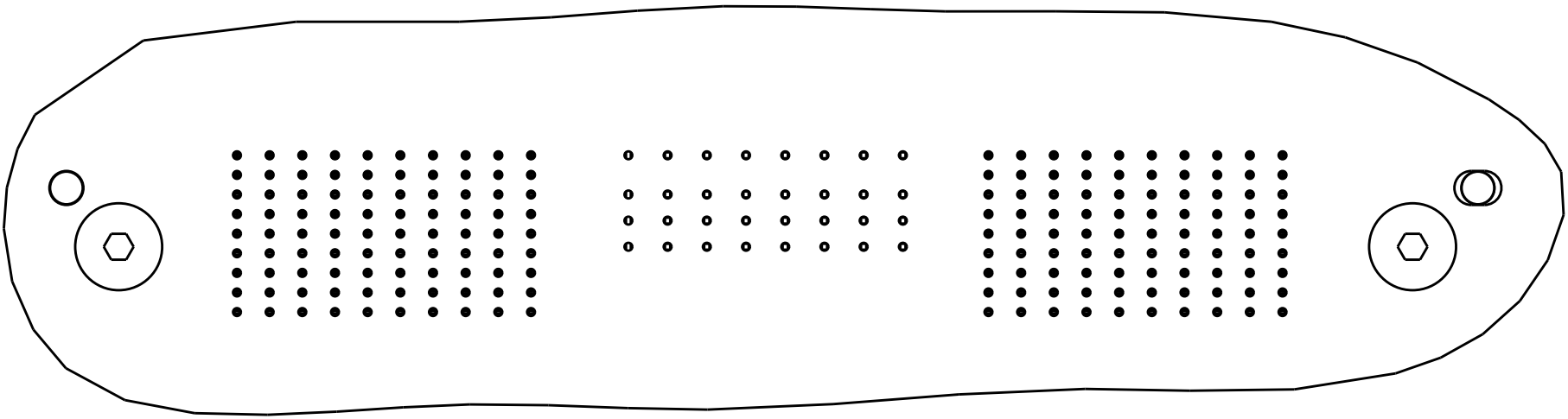
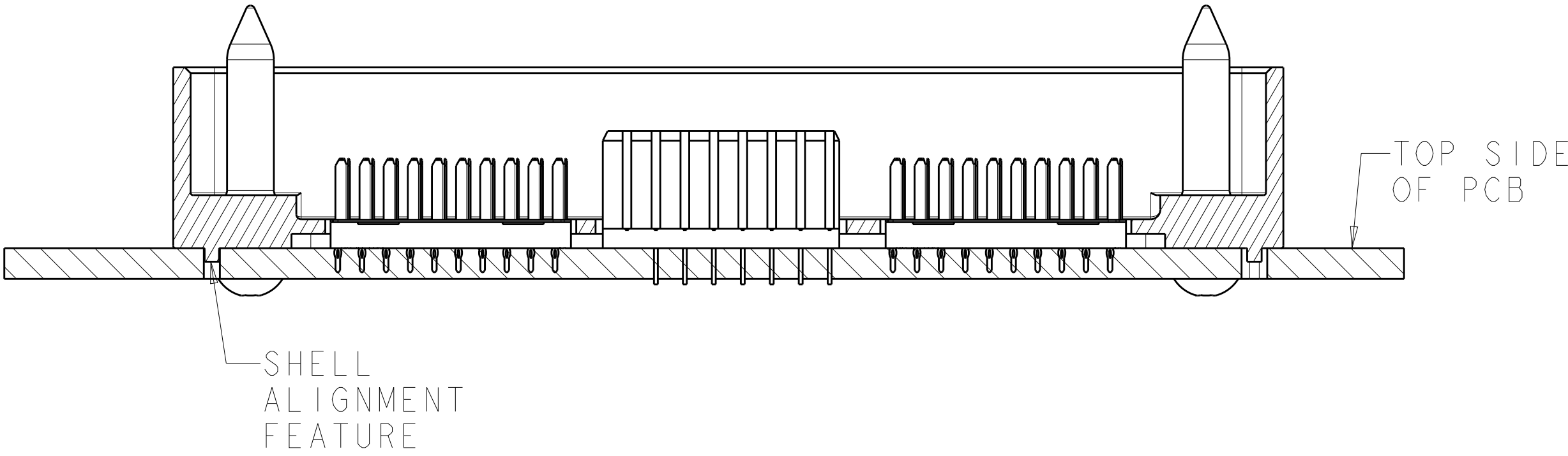
1. MATING CONNECTOR FOR THIS CONNECTOR IS 2000979.

2 MATERIAL- SHELL: ALUMINUM
HOUSING: GLASS FILLED POLYESTER, UL94VO
RETAINING RING: STAINLESS STEEL
KEYING GUIDE PIN: STAINLESS STEEL

3 PLATING- SHELL: ELECTROLESS NICKEL

4 KEYING GUIDE PIN PACKAGED FOR
ASSEMBLY BY CUSTOMER.

5 PLATING- CONTACT MATING AREA:
1.27µm MIN GOLD OVER 1.27µm MIN NICKEL
CONTACT PCB AREA: 0.5µm MIN TIN
OVER 1.27µm MIN NICKEL



OBSOLETE

NOT RELEASED FOR PRODUCTION

OBSOLETE

OBSOLETE

OBSOLETE

2	1	--	--	5-2000980-4
2	1	--	--	5-2000980-3
--	1	1	1	2000980-6
--	-	-	3	2000980-5
--	1	-	2	2000980-4
--	1	-	2	2000980-3
--	1	1	1	2000980-2
--	-	-	3	2000980-1
3 PAIR HMZd MODULE (RoHS)	8 POSN UPM MODULE	7 POSN RF COAX MODULE	3 PAIR HMZd MODULE	PART NUMBER
MODULE QTY PER ASSY				

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN Gent D. Yohm CHK CHONG YI APVD M. R. McALONIS	13JAN2010 13JAN2010 13JAN2010	TE Connectivity	
DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME	
mm		0 PLC ±0.2 1 PLC ±0.1 2 PLC ±0.05 3 PLC ±0.025 4 PLC ± ANGLES ±°		PRODUCT SPEC 108-2381	
MATERIAL		FINISH		APPLICATION SPEC	
-		-		WEIGHT	
-		-		CUSTOMER DRAWING	
-		-		SCALE 2:1	
-		-		SHEET 1 OF 3	
-		-		REV G	

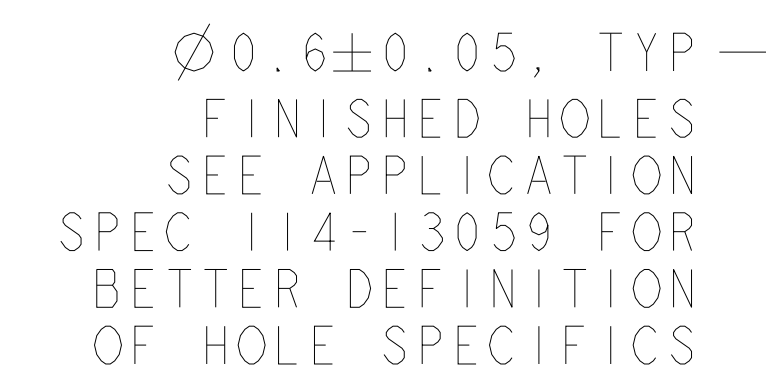
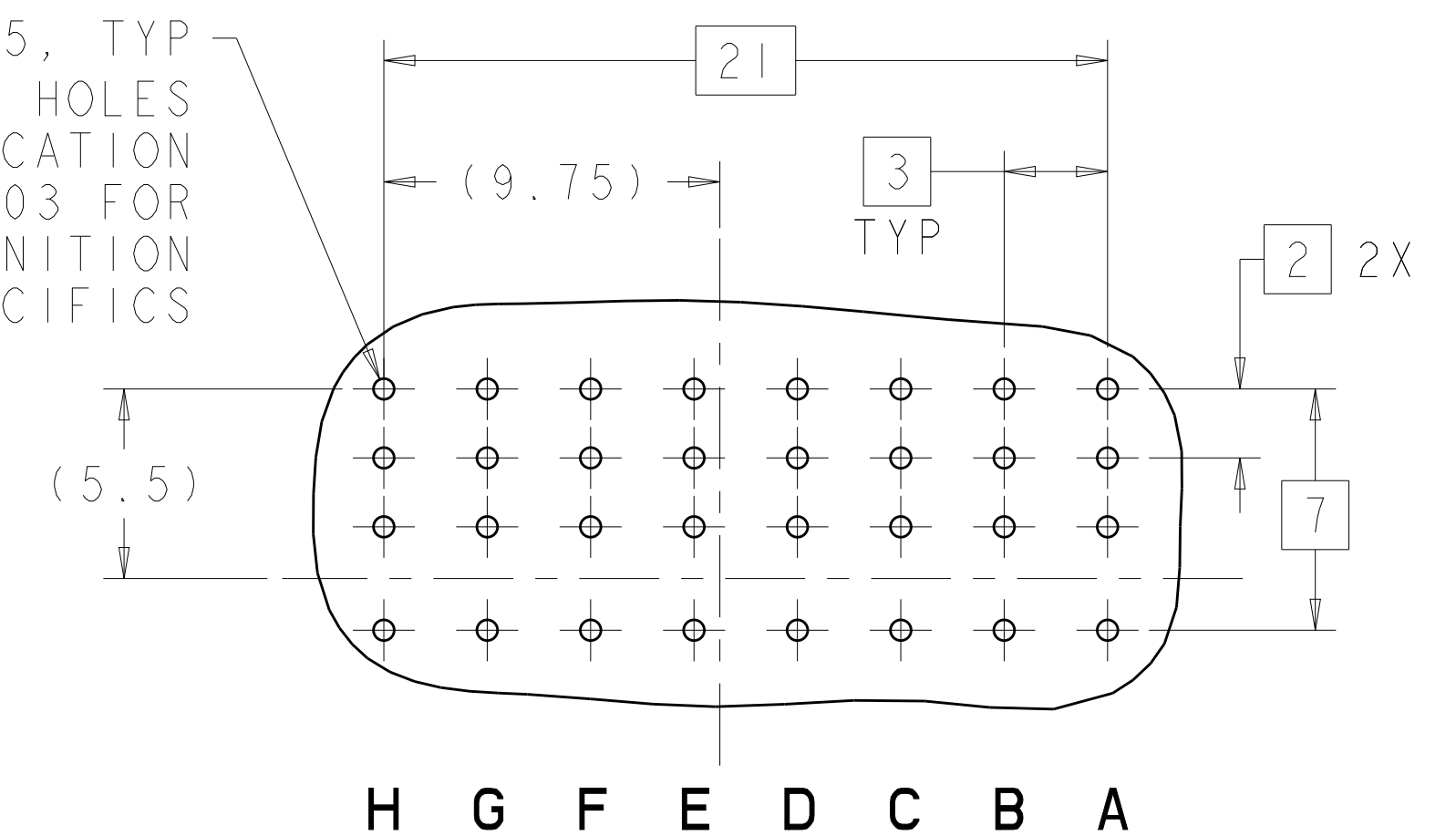
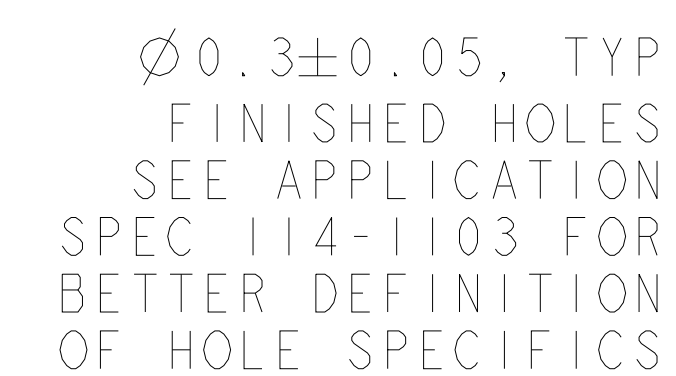


Diagram illustrating a 10x8 array of pins (80 pins total) in a 3-pin HMZd format. The array is defined by dimensions 22.5 (width) and 12 (height). The pin rows are labeled ROW FG (GND) through ROW A. The pin columns are numbered 1 through 10. The diagram shows a 10x8 grid of pins. Dimensions include 11.7 for the first 5 columns, 1.5 for the first column offset, and 1.5 for the last column offset. Notes specify 3 PAIR HMZd, TYP 2X FOR -4, and TYP 3X FOR -5.

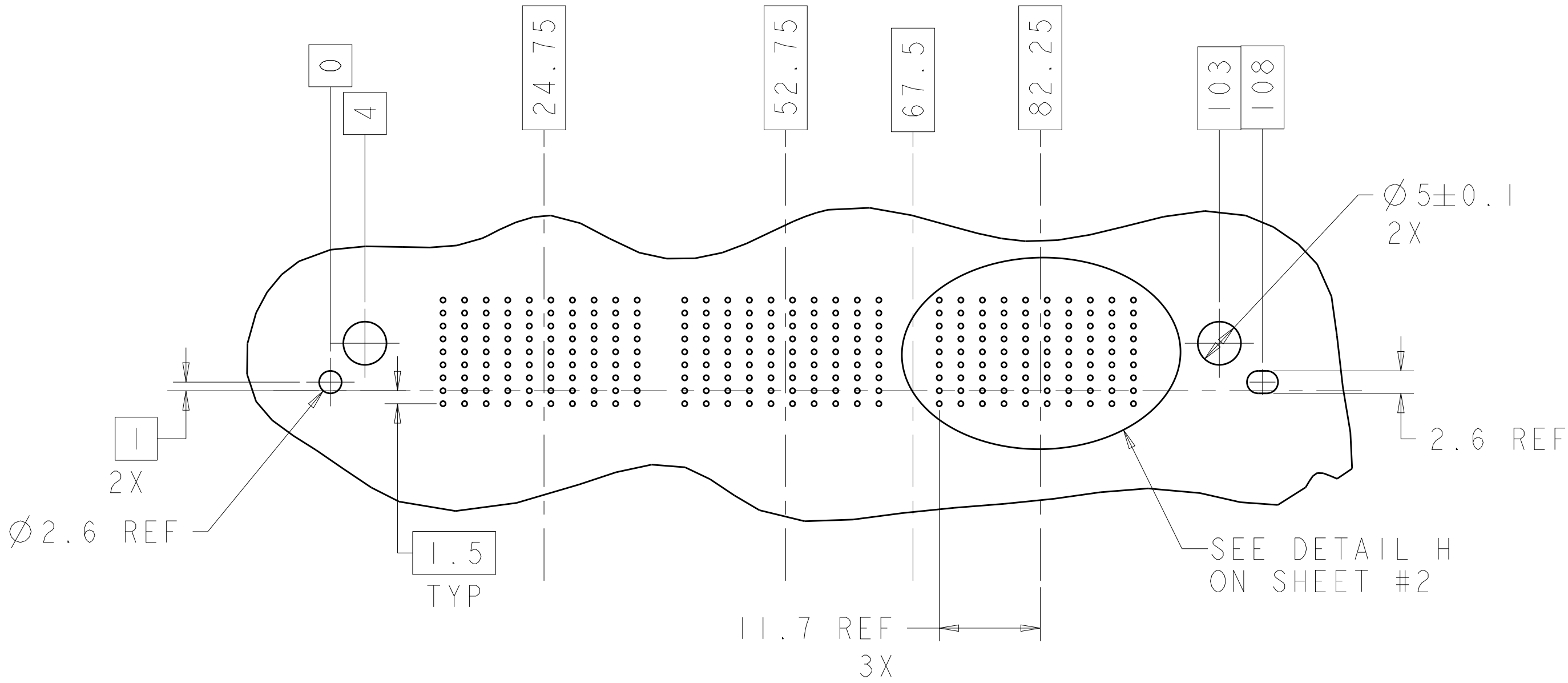
2000980-4 PCB
SCALE 2:1



8 POSITION
UNIVERSAL POWER MODULE
DETAIL K
SCALE 5:1

4805 (3/11)

LOC	DIST	REVISIONS				
		P	LTR	DESCRIPTION	DATE	DWN
-	-	-	-	SEE SHEET 1	-	-



RECOMMENED PC BOARD LAYOUT (MOTHERBOARD)
FOR 2000980-5
AS VIEWED FROM TOP OF PCB
(3 HM-Zd)

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN
Brent D. Yohm
13JAN2010

CHK
CHONG YI
13JAN2010

APVD
M. R. McALONIS
13JAN2010

NAME

MOTHER BOARD CONNECTOR,
HIGH SPEED RUGGEDIZED (HSR),
3 BAY, UNSEALED, VERTICAL

DIMENSIONS:

mm

TOLERANCES UNLESS
OTHERWISE SPECIFIED:

0 PLC ±0.2
1 PLC ±0.1
2 PLC ±0.05
3 PLC ±0.025
4 PLC ±
ANGLES ±
FINISH

MATERIAL

-

PRODUCT SPEC
108-2381

APPLICATION SPEC

WEIGHT

CUSTOMER DRAWING

SIZE
A1

CAGE CODE
00779

DRAWING NO
C=2000980

RESTRICTED TO

-

SCALE
1:1

SHEET
3

OF
3

REV
G

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[TE Connectivity:](#)

[2000980-4](#)