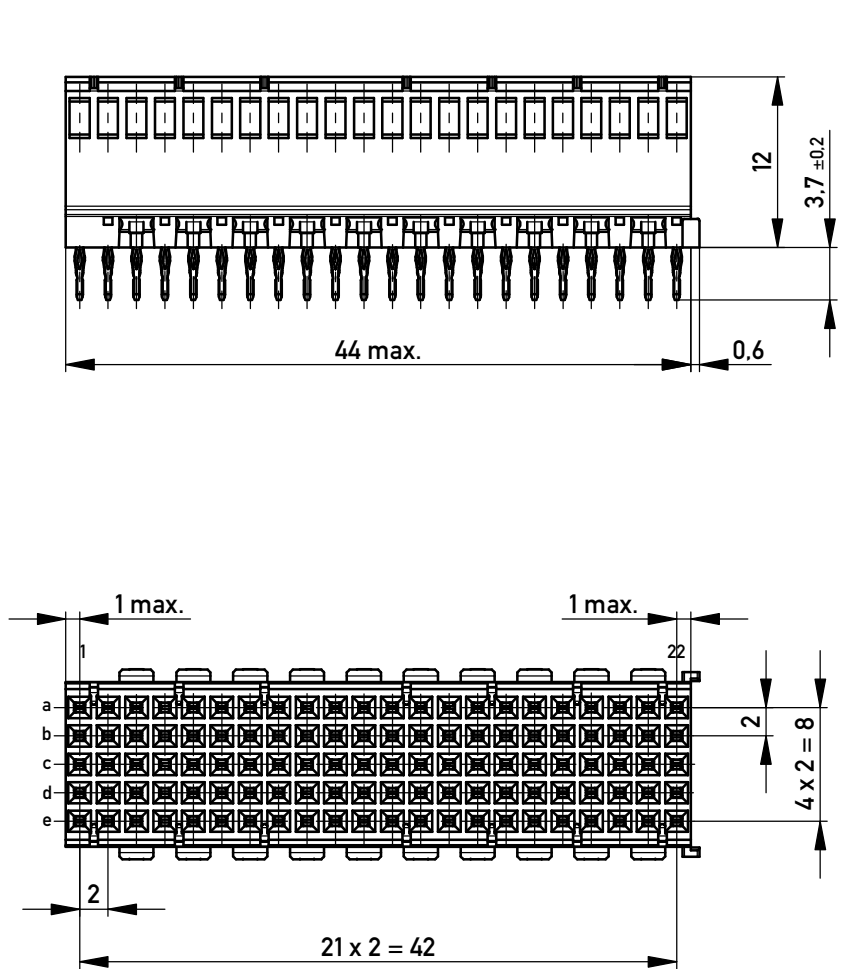
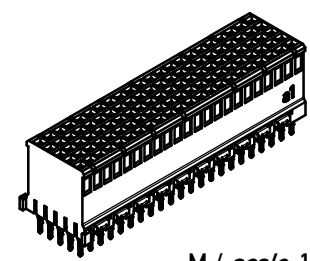
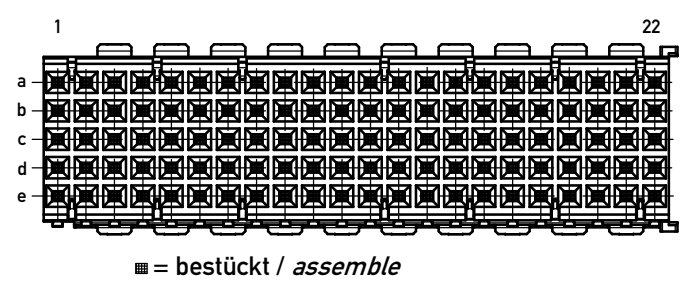


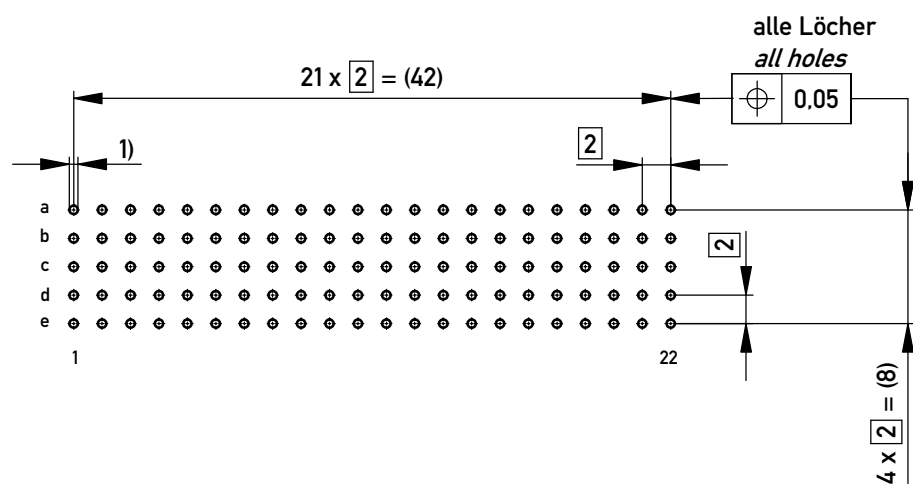
Bestückungsplan / *contact layout*



M / *scale* 1:1

Lochbild für Leiterplatte
(Bestückungsseite)

board hole pattern
(*component mounting side*)

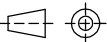


- 1) $\phi 0,6 \pm 0,05$ Durchmesser des metallisierten Loches
 $\phi 0,6 \pm 0,05$ Diameter of finished plated-through hole

Schichtaufbau im metallisierten Loch siehe Zeichnung 164062 Nr.1
Metal plating of plating-through hole according to 164062 No.1

Fehlende Maße und Angaben nach IEC 61076-4-101
Missing information and dimensions per 61076-4-101

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Information:			Tolerances		 All Dimensions in mm		Scale		2:1		
Anforderungsstufe 2 / class 2											
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e		01.02.2018						Class		ERMB22	
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