Product Overview

MC74LVXC3245: Configurable Dual Supply Octal Transceiver

For complete documentation, see the data sheet.

The 74LVXC3245 is a 24-pin dual-supply, octal configurable voltage interface transceiver especially well suited for PCMCIA and other real time configurable I/O applications. The V_{CCA} pin accepts a 3V supply level; the A port is a dedicated 3V port. The V_{CCB} pin accepts a 3V-to-5V supply level. The B port is configured to track the V_{CCB} supply level. A 5V level on the V_{CCB} pin will configure the I/O pins at a 5V level and a 3V V_{CCB} will configure the I/O pins at a 3V level. The A port interfaces with a 3V host system and the B port to the card slots. This device will allow the V_{CCB} voltage source pin and I/O pins on the B port to float when OE is High. This feature is necessary to buffer data to and from a PCMCIA socket that permits PCMCIA cards to be inserted and removed during normal operation. The Transmit/Receive (T/R) input determines the direction of data flow. Transmit (active-High) enables data from the A port to B port. Receive (active-Low) enables data from the B port to the A port.

Features

- Bidirectional Interface Between 3V and 3V/5V Buses
- Control Inputs Compatible with TTL Level
- Outputs Source/Sink Up to 24mA
- Guaranteed Simultaneous Switching Noise Level and Dynamic Threshold Performance
- Available in SOIC and TSSOP Packages
- Flexible V_{CCB} Operating Range
- Allows B Port and V_{CCB} to Float Simultaneously When OE Is High
- Functionally Compatible with the 74 Series 245 PIN Pins OE T/R A0-A7 B0-B7
- Pb-Free Packages are Available*

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
MC74LVXC3245DTG	Pb-free / Halide free	Active	8	3-State	2.3	3.6	9	24	TSSOP-24
MC74LVXC3245DTRG	Pb-free / Halide free	Active	8	3-State	2.3	3.6	9	24	TSSOP-24
MC74LVXC3245DWRG	Pb-free / Halide free	Active	8	3-State	2.3	3.6	9	24	SOIC-24

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