



ZXTP720MA

40V PNP LOW SATURATION SWITCHING TRANSISTOR

Features and Benefits

- $BV_{CEO} > -40V$
- I_C = -3A Continuous Collector Current .
- Low Saturation Voltage (-220mV max @ -1A)
- $R_{SAT} = 104 \text{ m}\Omega$ for a low equivalent On-Resistance
- h_{FE} specified up to -3A for high current gain hold up •
- Low profile 0.6mm high package for thin applications
- $R_{\theta JA}$ efficient, 60% lower than SOT23
- 4mm² footprint, 50% smaller than SOT23
- Lead-Free, RoHS Compliant (Note 1) •
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: DFN2020B-3 •
- Case material: Molded Plastic. "Green" Molding Compound.
- Terminals: Pre-Plated NiPdAu leadframe.
- Nominal package height: 0.6mm
- UL Flammability Rating 94V-0 •
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.01 grams (approximate)

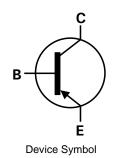
Applications

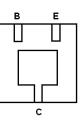
- **MOSFET Gate Driving**
- **DC-DC Converters**
- **Charging Circuits**
- Power switches
- Motor control



Top View

Bottom View





Bottom View Pin-Out

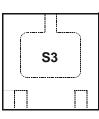
Ordering Information (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTP720MATA	S3	7	8	3000

1. No purposefully added lead. Notes:

Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com
For Packaging Details, go to our website at http://www.diodes.com.

Marking Information



S3 = Product Type Marking code

Top View



ZXTP720MA

Maximum Ratings @T_A = 25°C unless otherwise specified

Parameter		Symbol	Limit	Unit
Collector-Base Voltage		V _{CBO}	-50	
Collector-Emitter Voltage		V _{CEO}	-40	V
Emitter-Base Voltage		V _{EBO}	-7	
Peak Pulse Current		ICM	-4	
Continuous Collector Current	(Note 4)	· ·	-3	^
	(Note 5)	IC	-3.3	A
Base Current		IB	-1	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 4)		1.5 12 2.45 m 19.6		
Linear Derating Factor	(Note 5)	PD -			
Thermal Desistance, lunction to Archient	(Note 4)	P	83		
Thermal Resistance, Junction to Ambient	(Note 5)	R _{θJA}	51	°C/W	
Thermal Resistance, Junction to Lead	(Note 6)	$R_{ ext{ heta}JL}$	16.8		
Operating and Storage Temperature Range		T _{J.} T _{STG}	-55 to +150	°C	

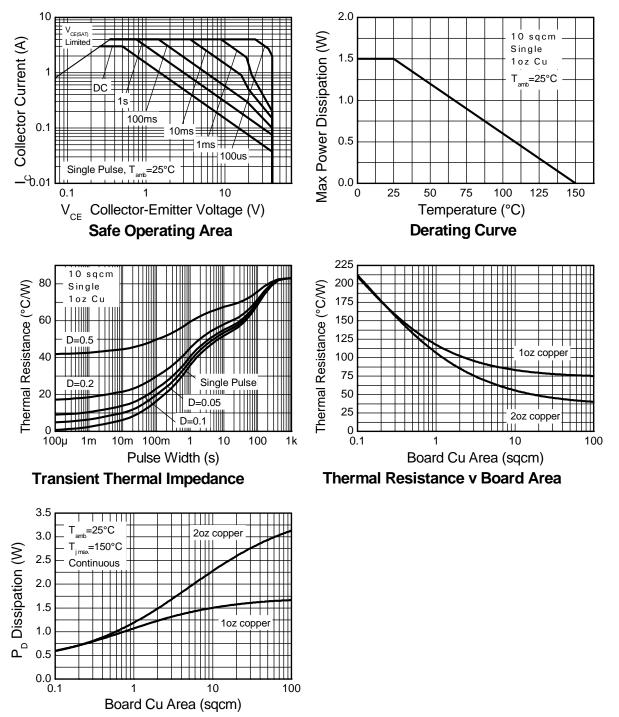
 For a device surface mounted on 31mm x 31mm (10cm²) FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition. The entire exposed collector pad is attached to the heatsink.
Same as note (4), except the device is measured at t ≤ 5 sec. Notes:

6. For a single device, thermal resistance from junction to solder-point (at the end of the drain lead).





Thermal Characteristics



Power Dissipation v Board Area





Electrical Characteristics @T_A = 25°C unless otherwise specified

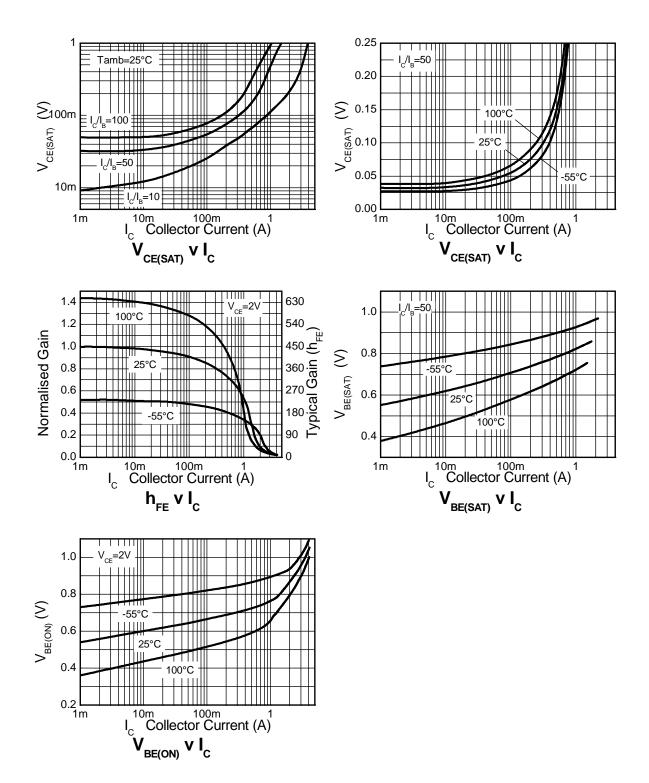
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-50	-80	-	V	I _C = -100μA
Collector-Emitter Breakdown Voltage (Note 7)	BV _{CEO}	-40	-70	-	V	I _C = -10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8.5	-	V	I _E = -100μA
Collector Cutoff Current	I _{CBO}	-	-	-100	nA	$V_{CB} = -40V$
Emitter Cutoff Current	I _{EBO}	-	-	-100	_ nA	$V_{EB} = -6V$
Collector Emitter Cutoff Current	I _{CES}	-	-	-100	nA	$V_{CES} = -32V$
Static Forward Current Transfer Ratio (Note 7)	h _{FE}	300 300 180 60 12	480 450 290 130 22		-	$\begin{split} & I_{C} = -10 \text{mA}, \ V_{CE} = -2 \text{V} \\ & I_{C} = -100 \text{mA}, \ V_{CE} = -2 \text{V} \\ & I_{C} = -1 \text{A}, \ V_{CE} = -2 \text{V} \\ & I_{C} = -1.5 \text{A}, \ V_{CE} = -2 \text{V} \\ & I_{C} = -3 \text{A}, \ V_{CE} = -2 \text{V} \end{split}$
Collector-Emitter Saturation Voltage (Note 7)	V _{CE(sat)}		-25 -150 -195 -210 -260	-40 -220 -300 -300 -370	mV	$\begin{split} I_C =& - 0.1A, \ I_B = -10 mA \\ I_C =& -1A, \ I_B = -50 mA \\ I_C =& -1.5A, \ I_B = -100 mA \\ I_C =& -2A, \ I_B = -200 mA \\ I_C =& -2.5A, \ I_B = -250 mA \end{split}$
Base-Emitter Turn-On Voltage (Note 7)	V _{BE(on)}	-	-0.89	-0.95	V	I _C = -2.5A, V _{CE} = -2V
Base-Emitter Saturation Voltage (Note 7)	V _{BE(sat)}	-	-0.97	-1.05	V	I _C = -2.5A, I _B = -250mA
Output Capacitance	C _{obo}	-	19	25	pF	V _{CB} = -10V. f = 1MHz
Transition Frequency	f _T	150	190	-	MHz	$V_{CE} = -10V, I_C = -50mA,$ f = 100MHz
Turn-On Time	t _{on}	-	40	-	ns	V _{CC} = -15V, I _C = -0.75A
Turn-Off Time	t _{off}	-	435	-	ns	$I_{B1} = I_{B2} = -15mA$

Notes: 7. Measured under pulsed conditions. Pulse width \leq 300 µs. Duty cycle \leq 2%.





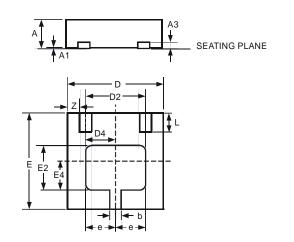
Typical Electrical Characteristics





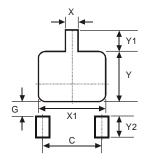


Package Outline Dimensions



DFN2020B-3					
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0	0.05	0.02		
A3			0.152		
b	0.20	0.30	0.25		
D	1.95	2.075	2.00		
D2	1.22	1.42	1.32		
D4	0.56	0.76	0.66		
e			0.65		
ш	1.95	2.075	2.00		
E2	0.79	0.99	0.89		
E4	0.48	0.68	0.58		
L	0.25	0.35	0.30		
Z			0.225		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
С	1.30
G	0.24
Х	0.35
X1	1.52
Y	1.09
Y1	0.47
Y2	0.50



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