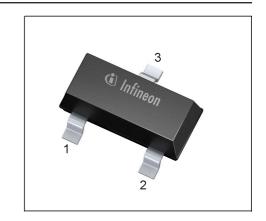


Low Noise Silicon Bipolar RF Transistor

- For low distortion broadband amplifiers and oscillators up to 2GHz at collector currents from 0.5mA to 20 mA
- Pb-free (RoHS compliant) package
- Qualification report according to AEC-Q101 available





ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package
BFR35AP	GEs	1 = B	2 = E	3 = C	SOT23

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	15	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2.5	
Collector current	I _C	45	mA
Base current	I _B	4	
Total power dissipation ¹⁾	P _{tot}	280	mW
_ <i>T</i> _S ≤ 93 °C			
Junction temperature	T_{J}	150	°C
Storage temperature	T_{Stg}	-55 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	205	K/W

1

 $^{{}^1}T_{\rm S}$ is measured on the collector lead at the soldering point to the pcb

 $^{^2}$ For calculation of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)



Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
DC Characteristics	,				•
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	15	-	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0	, ,				
Collector-emitter cutoff current	I _{CES}	-	-	10	μA
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\text{CB}} = 10 \text{ V}, I_{\text{E}} = 0$					
Emitter-base cutoff current	<i>I</i> _{EBO}	-	_	100	μA
$V_{\rm EB} = 2.5 \rm V, I_{\rm C} = 0$					
DC current gain	h _{FE}	70	100	140	-
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, pulse measured					



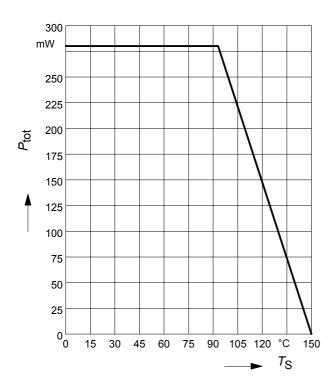
Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random samplin	g)				•
Transition frequency	f _T	3.5	5	-	GHz
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, f = 500 MHz					
Collector-base capacitance	C _{cb}	-	0.39	0.55	pF
$V_{\text{CB}} = 10 \text{ V}, f = 1 \text{ MHz}, V_{\text{BE}} = 0$,					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.23	-	
$V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	0.64	-	
$V_{\text{EB}} = 0.5 \text{ V}, f = 1 \text{ MHz}, V_{\text{CB}} = 0$,					
collector grounded					
Minimum noise figure	NF _{min}				dB
$I_{\rm C}$ = 2 mA, $V_{\rm CE}$ = 6 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 900 MHz		-	1.4	-	
f = 1.8 GHz		-	2	-	
Power gain, maximum available ¹⁾	G _{ma}				
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
$Z_{L} = Z_{Lopt}, f = 900 \text{ MHz}$		-	16	-	
f = 1.8 GHz		-	10.5	-	
Transducer gain	S _{21e} ²				dB
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 900 MHz		-	13	-	
f = 1.8 GHz		_	7.5	_	

 $^{{}^{1}}G_{ma} = |S_{21}/S_{12}| (k-(k^{2}-1)^{1/2})$



Total power dissipation $P_{tot} = f(T_S)$

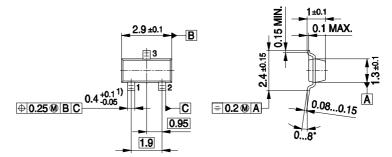


4



Package Outline

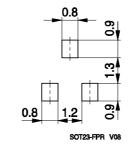




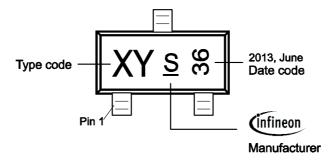
1) Lead width can be 0.6 max. in dambar area

SOT23-PO V08

Foot Print

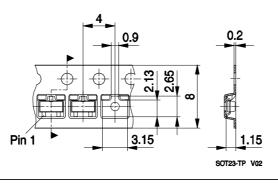


Marking Layout



Standard Packing

Reel o 180 mm: 3.000 Pieces / Reel Reel o 330 mm = 10.000 Pieces / Reel





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6

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