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## **FDPF14N30 N-Channel UniFET<sup>TM</sup> MOSFET** 300 V, 14 A, 290 mΩ

## Features

- $R_{DS(on)}$  = 290 m $\Omega$  (Max.) @  $V_{GS}$  = 10 V, I<sub>D</sub> = 7 A
- Low Gate Charge (Typ. 18 nC)
- Low C<sub>rss</sub> (Typ. 17 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability

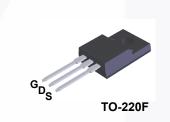
## Applications

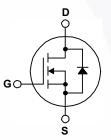
- PDP TV
- Uninterruptible Power Supply



## Description

UniFET<sup>TM</sup> MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





### Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FDPF14N30	Unit
V <sub>DSS</sub>	Drain-Source Voltage	<u>;</u>	300	V
ID	Drain Current	- Continuous (T <sub>C</sub> = 25°C) - Continuous (T <sub>C</sub> = 100°C)	14 * 8.4 *	A A
I <sub>DM</sub>	Drain Current	- Pulsed (Note	1) 56 *	Α
V <sub>GSS</sub>	Gate-Source voltage		±30	V
E <sub>AS</sub>	Single Pulsed Avalan	nche Energy (Note	2) 330	mJ
I <sub>AR</sub>	Avalanche Current	(Note	1) 14	Α
E <sub>AR</sub>	Repetitive Avalanche Energy (*		1) 14	mJ
dv/dt	Peak Diode Recovery dv/dt		3) 4.5	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25°C) - Derate above 25°C	35 0.28	W W/°C
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storag	ge Temperature Range	-55 to +150	°C
TL	Maximum Lead Temp	perature for Soldering, 1/8" from Case for 5 Seconds	300	°C

\*Drain current limited by maximum junction temperature.

## **Thermal Characteristics**

Symbol	Parameter	FDPF14N30	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	3.56	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	C/W

DPF14N30
- N-Channel
UniFET <sup>TM</sup> I
MOSFET

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Part Number Top Mark P		Package	Packing Method	Reel Size	E Ta	ape Width	ı Qu	antity	
FDPF14N30 FDPF14N30 T		TO-220F	O-220F Tube N/A			N/A	50	50 units	
Electric	al Char	racteristics T <sub>C</sub> = 25°	C unless ot	herwise noted.					
Symbol		Parameter		Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristics								1
BV <sub>DSS</sub>	Drain-Sou	rce Breakdown Voltage	V <sub>GS</sub> = (	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		300			V
$\Delta BV_{DSS}$ / $\Delta T_{J}$	Breakdow Coefficien	n Voltage Temperature t	I <sub>D</sub> = 25	$I_D = 250 \ \mu$ A, Referenced to 25°C			0.3		V/∘C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current			$V_{DS} = 300 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 240 \text{ V}, T_{C} = 125^{\circ}\text{C}$				1 10	μΑ μΑ
I <sub>GSSF</sub>	Gate-Body	y Leakage Current, Forward	-	30 V, V <sub>DS</sub> = 0 V				100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse		e V <sub>GS</sub> = -	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$				-100	nA
On Charac	teristics								
V <sub>GS(th)</sub>	Gate Thre	shold Voltage	V <sub>DS</sub> = \	/ <sub>GS</sub> , I <sub>D</sub> = 250 μA		3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance		V <sub>GS</sub> = 2	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7 A			0.24	0.29	Ω
9 <sub>FS</sub>	Forward Transconductance		V <sub>DS</sub> = 4	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 7 A			10.5		S
Dynamic (	Characterist	tics							
C <sub>iss</sub>	Input Capa	acitance		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,			815	1060	pF
C <sub>oss</sub>	Output Capacitance		f = 1.0 l	f = 1.0 MHz			150	195	pF
C <sub>rss</sub>	Reverse T	ransfer Capacitance				1		25	pF
Switching	Characteris	stics							
t <sub>d(on)</sub>	Turn-On Delay Time			V <sub>DD</sub> = 150 V, I <sub>D</sub> = 14 A,			20	50	ns
t <sub>r</sub>	Turn-On R	Rise Time	V <sub>GS</sub> = 1	$V_{\rm GS}$ = 10 V, R <sub>G</sub> = 25 Ω			105	120	ns
t <sub>d(off)</sub>	Turn-Off D	elay Time					30	70	ns
t <sub>f</sub>	Turn-Off F	all Time			(Note 4)		75	160	ns
Qg	Total Gate	Charge		$V_{DS} = 240 \text{ V}, \text{ I}_{D} = 14 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 4)			18	25	nC
Q <sub>gs</sub>	Gate-Sour	rce Charge	V <sub>GS</sub> = 1				4.5		nC
Q <sub>gd</sub>	Gate-Drain	n Charge					8		nC
Drain-Sou	rce Diode C	haracteristics and Maxim	um Rating	6					
I <sub>S</sub>	Maximum	Continuous Drain-Source	iode Forwa	rd Current				14	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode F		Forward C	orward Current				56	Α
V <sub>SD</sub>	Drain-Sou	rce Diode Forward Voltage	V <sub>GS</sub> = (	) V, I <sub>S</sub> = 14 A				1.4	V
t <sub>rr</sub>	Reverse R	Recovery Time		) V, I <sub>S</sub> = 14 A,			235		ns
Q <sub>rr</sub>	Reverse R	Recovery Charge	$dl_F/dt = 100 \text{ A}/\mu\text{s}$				1.6		μC

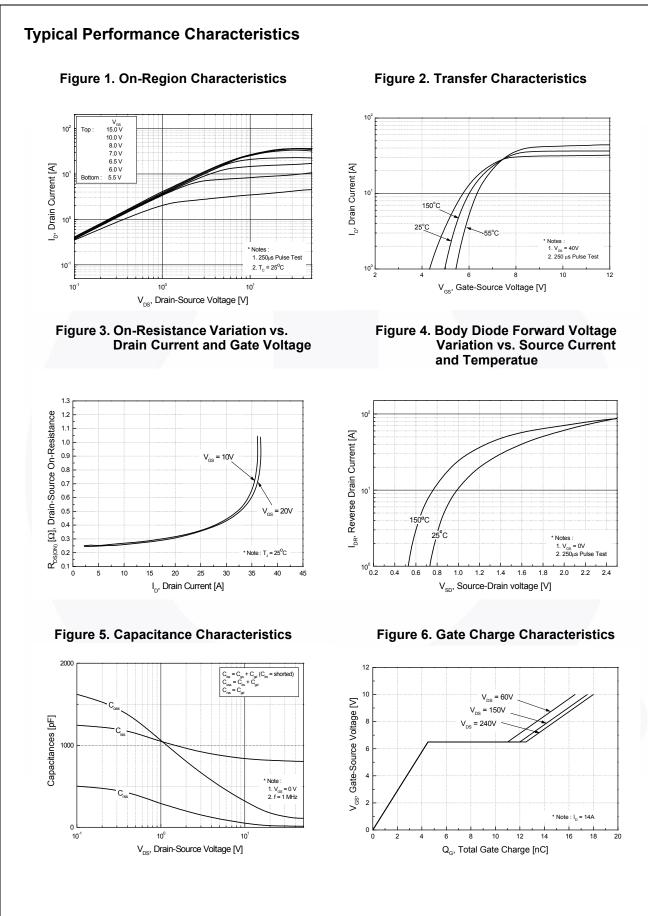
#### Notes:

1. Repetitive rating: pulse-width limited by maximum junction temperature.

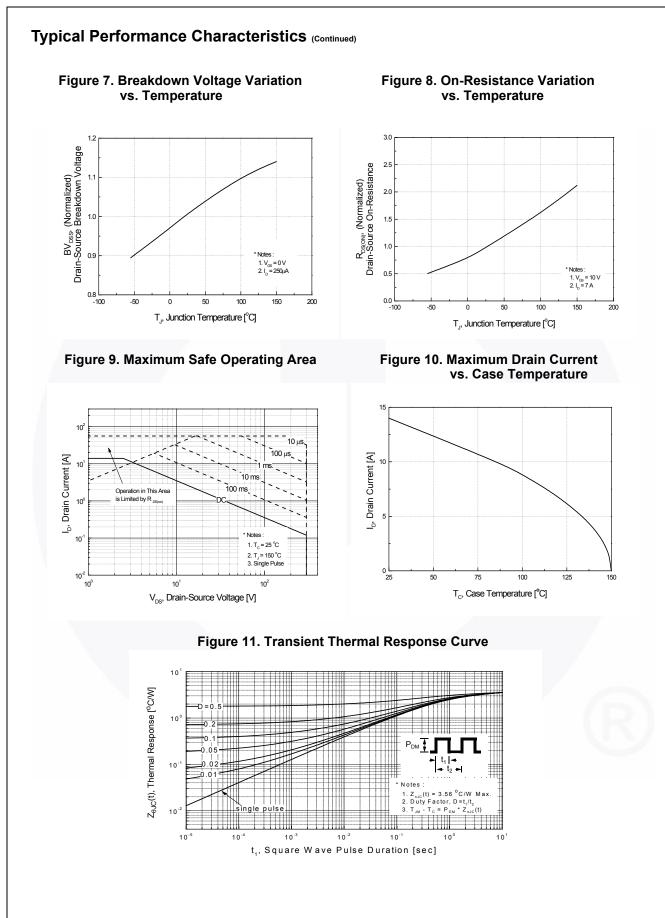
2. L = 2.8 mH, I<sub>AS</sub> = 14 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , starting T<sub>J</sub> = 25°C.

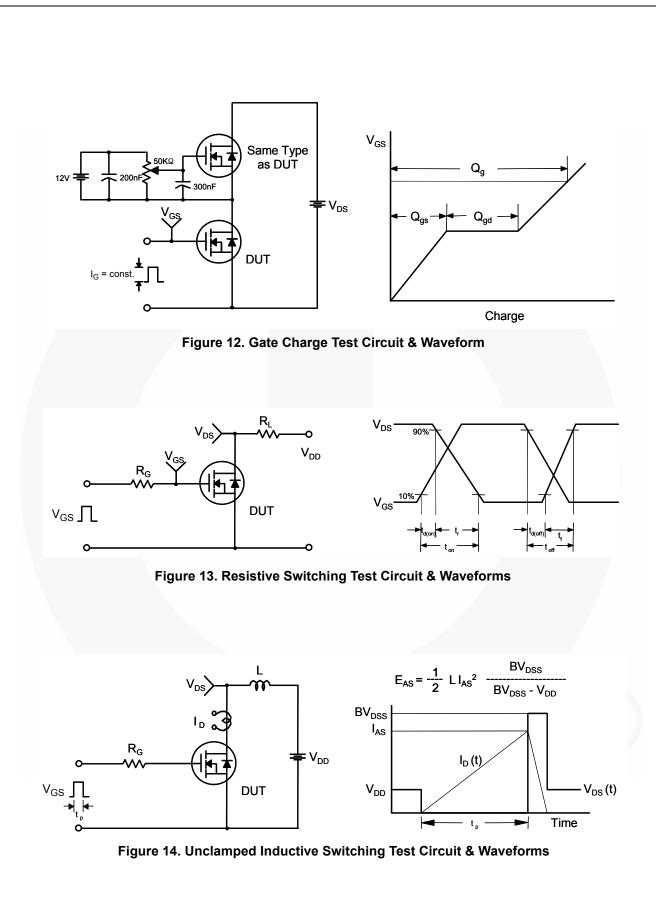
3. I\_{SD}  $\leq$  14 A, di/dt  $\leq$  200 A/µs, V\_{DD}  $\leq$  BV\_{DSS}, starting T\_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.



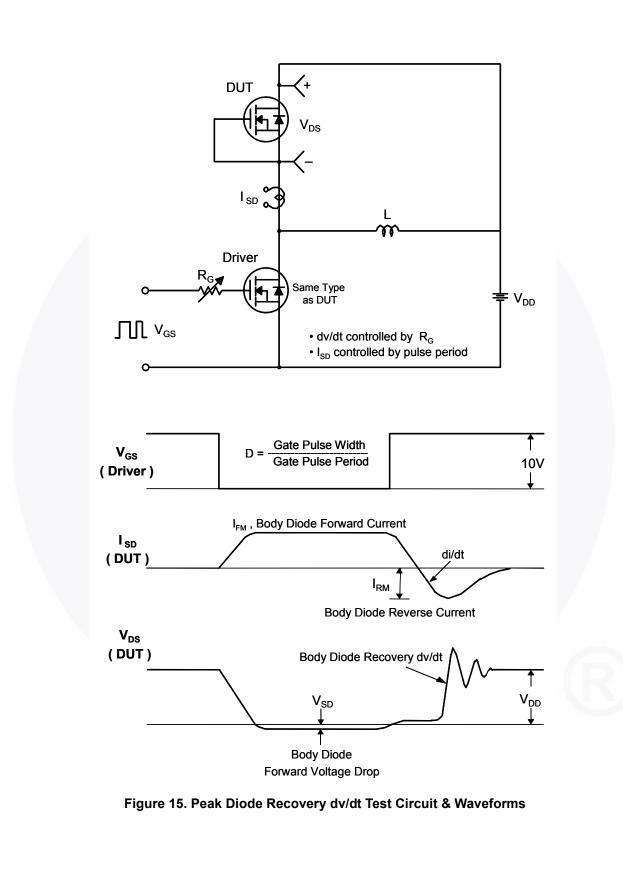
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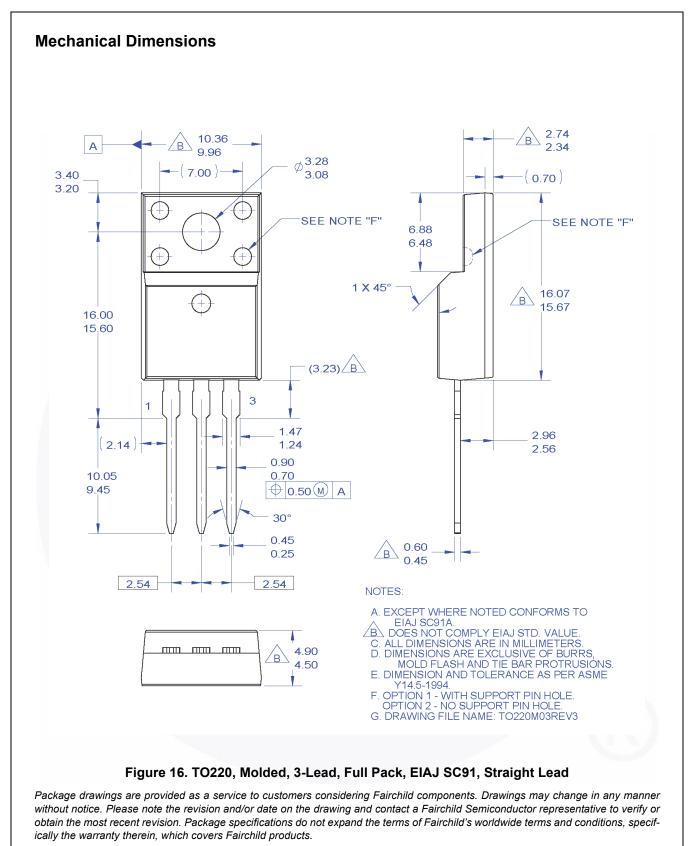




FDPF14N30 — N-Channel UniFET<sup>TM</sup> MOSFET

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