

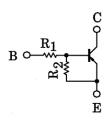
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

RN2314, RN2315, RN2316, RN2317, RN2318

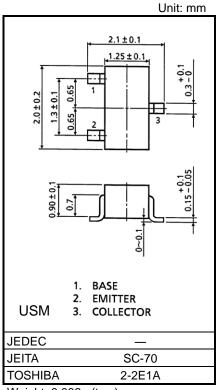
Switching, Inverter Circuit, Interface Circuit and Driver Circuit

- With built-in bias resistors.
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process and miniaturize equipment.
- Various resistance values are available to suit various circuit designs.
- Complementary to RN1314 to RN1318

Equivalent Circuit and Bias Resistor Values



Type No.	R ₁ (kΩ)	R ₂ (kΩ)
RN2314	1	10
RN2315	2.2	10
RN2316	4.7	10
RN2317	10	4.7
RN2318	47	10



Weight: 0.006g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristi	Symbol	Rating	Unit		
Collector-base voltage	RN2314 to 2318	V _{CBO}	-50	V	
Collector-emitter voltage	RIN2314 10 2316	VCEO	-50	V	
Emitter-base voltage	RN2314		-5		
	RN2315		-6	V	
	RN2316	V _{EBO}	-7		
	RN2317		-15		
	RN2318		-25		
Collector current		Ic	-100	mA	
Collector power dissipation	RN2314 to 2318	Pc	100	mW	
Junction temperature	KIN2314 (0 2316	Tj	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

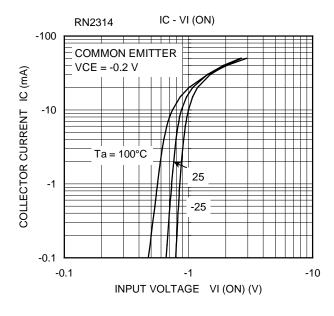
Start of commercial production 1999-01

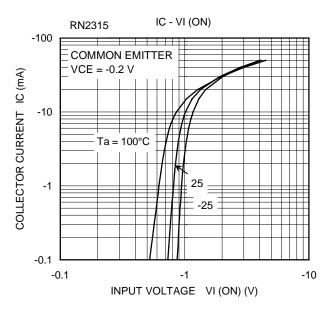


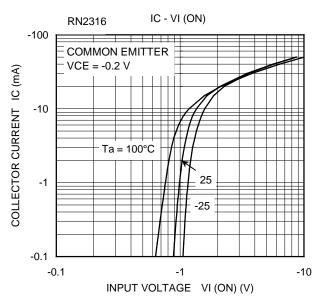
Electrical Characteristics (Ta = 25°C)

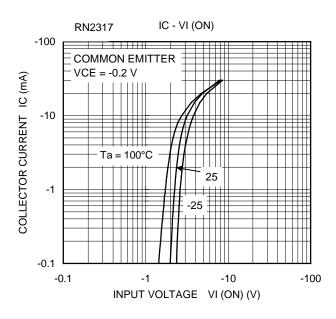
Characteristic		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector out off ourrent	RN2314 to 2318	I _{CBO}	_	V _{CB} =-50 V, I _E = 0 mA	_	_	-100	nA
Collector cut-off current	RN2314 to 2318	ICEO	_	$V_{CE} = -50 \text{ V}, I_B = 0 \text{ mA}$	_	_	-500	nA
Emitter cut-off current	RN2314	I _{EBO}	_	V _{EB} = −5 V, I _C = 0 mA	-0.35	_	-0.65	mA
	RN2315		_	$V_{EB} = -6 \text{ V}, I_{C} = 0 \text{ mA}$	-0.37	_	-0.71	
	RN2316		_	V _{EB} = -7 V, I _C = 0 mA	-0.36	_	-0.68	
	RN2317		_	V _{EB} = −15 V, I _C = 0 mA	-0.78	_	-1.46	
	RN2318		_	V _{EB} = −25 V, I _C = 0 mA	-0.33	_	-0.63	
DC current gain	RN2314 to 16, RN2318	hFE	_	V _{CE} = -5 V, I _C = -10 mA	50	_	_	_
	RN2317		_		30	_	1	
Collector-emitter saturation voltage	RN2314 to 2318	VCE (sat)	_	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	٧
	RN2314	Vi (ON)	_		-0.5	1	-2.0	V
	RN2315		_		-0.6	_	-2.5	
Input voltage (ON)	RN2316		_	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-0.7	_	-2.5	
	RN2317		_		-1.5	_	-3.5	
	RN2318		_		-2.5	_	-10.0	
Input voltage (OFF)	RN2314	VI (OFF)	_	V _{CE} = −5 V, I _C = −0.1 mA	-0.3	_	-0.9	V
	RN2315		_		-0.3	-	-1.0	
	RN2316		_		-0.3	-	-1.1	
	RN2317		_		-0.3	_	-3.0	
	RN2318		_		-0.5	_	-5.7	
Translation frequency	RN2314 to 2318	f⊤	_	V _{CE} = −10 V, I _C = −5 mA	_	200	_	MHz
Collector output capacitance	RN2314 to 2318	C _{ob}	_	V _{CB} = −10 V, I _E = 0 mA, f = 1 MHz	_	3.0	6.0	pF
	RN2314	R ₁	_	_	0.7	1.0	1.3	kΩ
	RN2315		_		1.54	2.2	2.86	
Input resistor	RN2316		_		3.29	4.7	6.11	
	RN2317		_		7.0	10.0	13.0	
	RN2318		_		32.9	47.0	61.1	
Resistor ratio	RN2314	R ₁ /R ₂	_		_	0.1	_	
	RN2315		_	_	_	0.22	_	_
	RN2316		_		_	0.47	_	
	RN2317		_		_	2.13	_	
	RN2318		_		_	4.7	_	

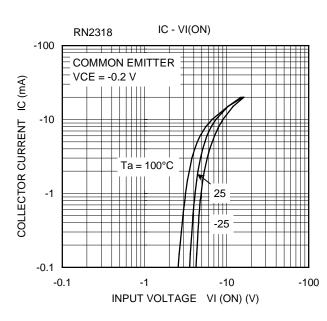




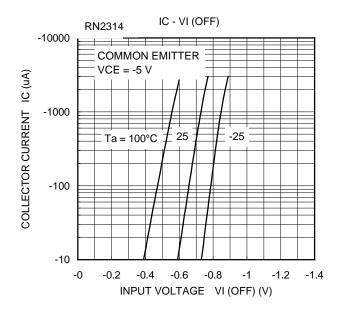


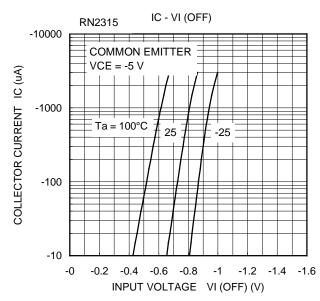


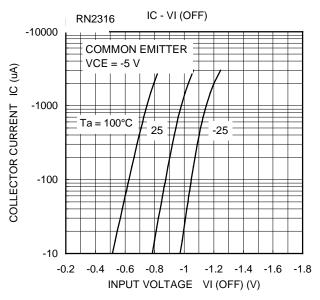


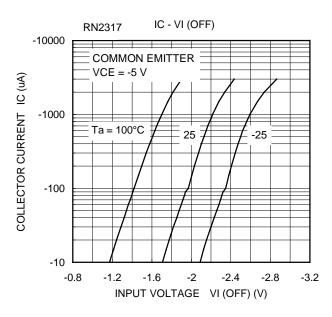


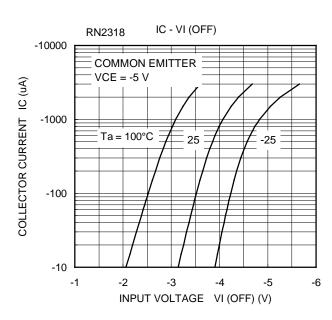




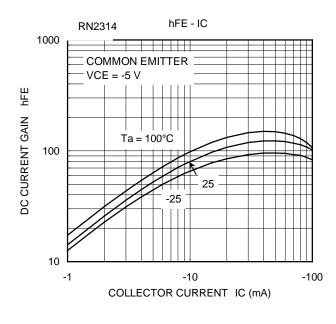


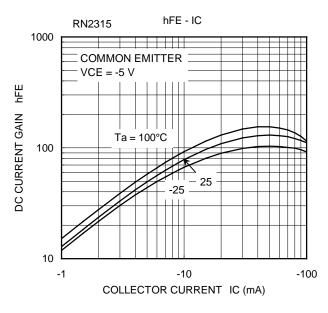


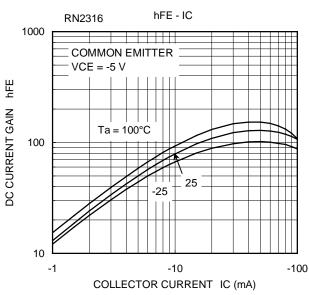


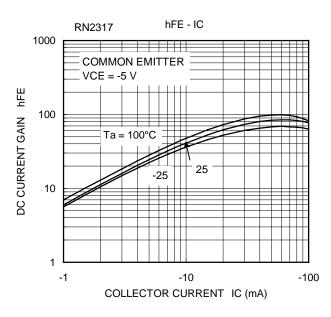


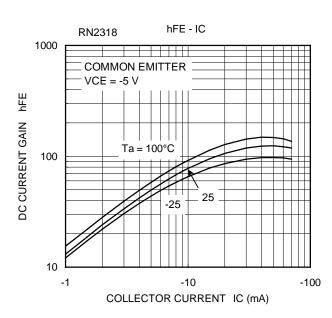




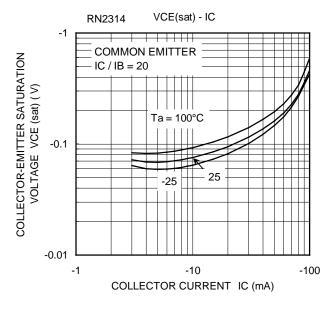


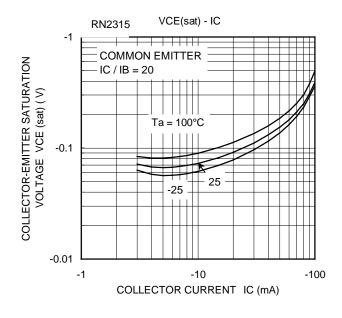


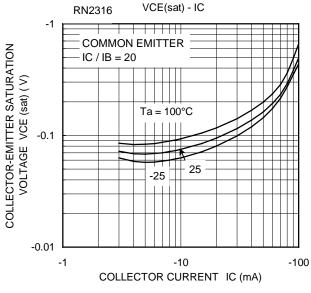


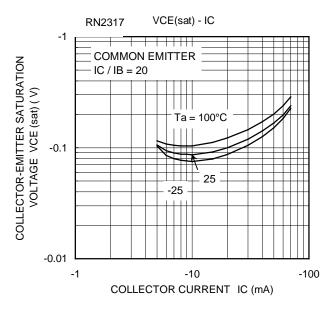


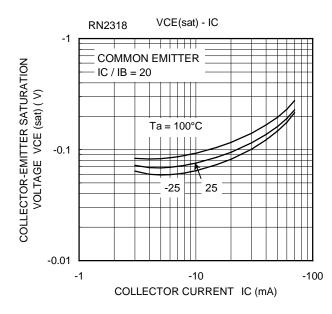






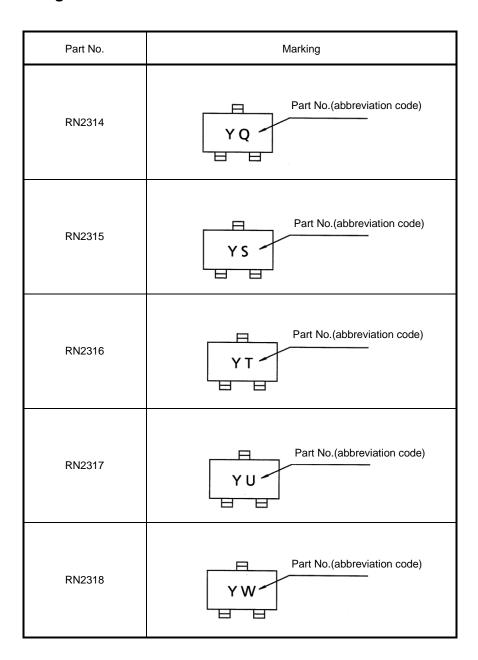








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