

5 V AGC AMPLIFIER + VIDEO AMPLIFIER

UPC3219GV

FEATURES

- ON-CHIP LOW DISTORTION AMPLIFIER: IIP3 = -1 dBm at minimuim gain
- WIDE AGC DYNAMIC RANGE: GCR = 42 dB TYP
- ON-CHIP VIDEO AMPLIFIER: VOUT = 1.0 VP-P at single-ended output
- SUPPLY VOLTAGE: Vcc = 5 V
- PACKAGED IN 8 PIN SSOP SUITABLE FOR SURFACE MOUNTING

APPLICATIONS

- Digital CATV
- · Cable modem receivers
- IP Telephony receivers

DESCRIPTION

NEC's UPC3219GV is a Silicon Monolithic IC designed for use as an AGC Amplifier for digital CATV, cable modem and IP telephony systems. This IC consists of a two stage gain control amplifier and a fixed gain video amplifier. The device provides a differential input and differential output for noise performance, which eliminates shielding requirements.

The package is 8-pin SSOP (Shrink Small Outline Package) suitable for surface mount.

This IC is manufactured using NEC's 10 GHz fT NESAT[™]II AL silicon bipolar process. This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, this IC has excellent performance, uniformity and reliability.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

ELECTRICAL CHARACTERISTICS

 $(TA = 25^{\circ}C, Vcc = 5 V, Zs = 1K\Omega, ZL = 1K\Omega, fin = 45 MHz, single-ended output)$, unless otherwise noted

PART NUMBER PACKAGE OUTLINE			UPC3219GV S08		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	ТҮР	MAX
DC Characterist	ics				
Icc	Circuit Current (no input signal)	mA	28	35	42
RF Characterisit	ics				•
BW	Frequency Bandwidth, VAGC = 3 V ¹	MHz		100	
GMAX	Maximum Gain , VAGC = 4.5 V	dB	39	42	45
Gmin	Minimum Gain, VAGC = 0.5 V	dB	-4	0	4
GCR	Gain Control Range, VAGC = 0.5 to 4.5 V	dB	35	42	-
NFAGC	Noise Figure, VAGC = 4.5 V at MAX Gain	dB	-	9	10.5
Vout	Output Voltage, Single Ended Output	VP-P		1.0	
IМз	Third Order Intermodulation Distortion, $f_{IN1} = 44$ MHz, $f_{IN2} = 45$ MHz, $V_{IN} = 30$ dBmV per tone ²	dBc		55	

Note:

1. -3dB with respect to 10 MHz gain

2. VAGC is adjusted to establish VOUT = 1.0 VP-P per tone

VOLTAGE GAIN vs. AUTOMATIC GAIN CONTROL VOLTAGE



Automatic Gain Control Voltage, VAGC (V)

PACKAGE OUTLINE S08



All dimensions are typical unless specified otherwise.

ABSOLUTE MAXIMUM RATINGS¹

(TA = 25°C, unless otherwise specified)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	6.0
PD	Power Dissipation ² , TA = 85°C	mW	250
TOP1	Operating Ambient Temp.	°C	-40 to +85
Tstg	Storage Temperature	°C	-50 to +150

Notes:

1. Operation in excess of any one of these parameters may result

in permanent damage.

2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB, with copper

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	UNITS	MIN	ТҮР	МАХ
Vcc	Supply Voltage	V	4.5	5.0	5.5
TA	Operating Ambient Temp.1	°C	-40	+25	+85
VAGC	Gain Control Voltage Range	V	0.5	-	4.5
Vin	Video Input Signal Range	dBmV	9		30

Note:

1. Vcc = 4.5 to 5.5 V

ORDERING INFORMATION

PART NUMBER	QUANTITY
UPC3219GV-E1-A	1 kp/reel

Note:

Embossed tape 8 mm wide. Pin 1 indicates pull-out direction of tape.

TYPICAL PERFORMANCE CURVES (TA = 25°C, unless otherwise specified)



THIRD ORDER INTERMODULATION DISTORTION vs. INPUT VOLTAGE



Input Voltage, PIN (dBm per tone)

TYPICAL PERFORMANCE CURVES, cont. (TA = 25°C, unless otherwise specified)













VOLTAGE OUTPUT vs. FREQUENCY



NOISE FIGURE vs. FREQUENCY



INPUT THIRD ORDER INTERCEPT POINT vs. INPUT POWER

SYSTEM APPLICATION EXAMPLE



EVALUATION BOARD SCHEMATIC AND TEST



PIN EXPLANATIONS

Pin No.	Name	Applied Voltage (v)	Pin Voltage (v) ¹	Description	Internal Equivalent Circuit
1	Vcc	4.5 to 5.5		Power supply pin. This pin should be externally equipped with bypass capacitor to minimize ground impedance.	
2	INPUT1		1.45	Signal input pins of AGC amplifier.	
3	INPUT2		1.45		
4	VAGC	0 to Vcc		Gain control pin. This pin's bias govern the AGC output level. Minimuim Gain at VAGC = 0.5 V Maximum Gain at VAGC = 4.5 V Recommended to use by dividing AGC voltage with external resistor (ex. 100k)	C AGC Amp
5	GND 2	0		Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible.	
6	OUTPUT2		2.2	Signal output pins of video amplifier	
7	OUTPUT1		2.2		
8	GND 1	0		Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All ground pins must be connected together with wide ground pattern to decrease impedance difference.	

Note:

1. PIN is measured at Vcc = 5 V

OUTLINE DIMENSIONS (Units in mm)



All dimensions are typical unless specified otherwise.

EVALUATION BOARD ASSEMBLY

EVALUATION BOARD





Life Support Applications

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This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentratio in CEL o		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM Not Detected		tected	
PBDE	< 1000 PPM	Not Detected		

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