

PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-100mA
R ₁	100kΩ
R ₂	100kΩ

Features

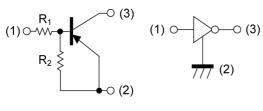
- 1) Built-In Biasing Resistors, $R_1 = R_2 = 100 \text{k}\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC115E series

Application

INVERTER, INTERFACE, DRIVER

•Inner circuit

DTA115EM/ DTA115EEB/ DTA115EUB

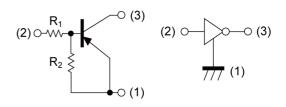


- (1) IN (BASE)
- (2) GND (+) (EMITTER)
- (3) OUT (COLLECTOR)

Outline

- Outilitie	
SOT-723	SOT-416FL
DTA115EM (VMT3)	DTA115EEB (EMT3F)
SOT-416	SOT-323FL
DTA115EE (EMT3)	DTA115EUB (UMT3F)
SOT-323	SOT-346
DTA115EUA (UMT3)	DTA115EKA (SMT3)

DTA115EE/ DTA115EUA/ DTA115EKA



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA115EM	SOT-723	1212	T2L	180	8	8000	19
DTA115EEB	SOT-416FL	1616	TL	180	8	3000	19
DTA115EE	SOT-416	1616	TL	180	8	3000	19
DTA115EUB	SOT-323FL	2021	TL	180	8	3000	19
DTA115EUA	SOT-323	2021	T106	180	8	3000	19
DTA115EKA	SOT-346	2928	T146	180	8	3000	19

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● **Absolute maximum ratings** (T_a = 25°C)

Par	ameter	Symbol	Values	Unit
Supply voltage		V _{CC}	-50	V
Input voltage		V _{IN}	-40 to 10	V
Output current		Io	-20	mA
Collector current			-100	mA
	DTA115EM		150	
		150		
Davis a dia sin ation	DTA115EE	P _D *2	150	mW
Power dissipation	DTA115EUB	_ P _D -	200	
	DTA115EUA		200	
	DTA115EKA		200	
Junction temperature		T _j	150	°C
Range of storage temperatu	re	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Daramatar	Symbol .	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uffil
Input voltage	$V_{l(off)}$	$V_{CC} = -5V, I_{O} = -100 \mu A$	-	-	-0.5	V
Input voltage	V _{I(on)}	$V_O = -0.3V$, $I_O = -1mA$	-3.0	-	-	V
Output voltage	V _{O(on)}	$I_O = -5mA$, $I_I = -0.25mA$	1	-100	-300	mV
Input current	I _I	V _I = -5V	1	-	-150	μA
Output current	I _{O(off)}	$V_{CC} = -50V, V_{I} = 0V$	1	-	-500	nA
DC current gain	G _I	$V_{O} = -5V, I_{O} = -5mA$	82	-	-	-
Input resistance	R ₁	-	70	100	130	kΩ
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land.

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

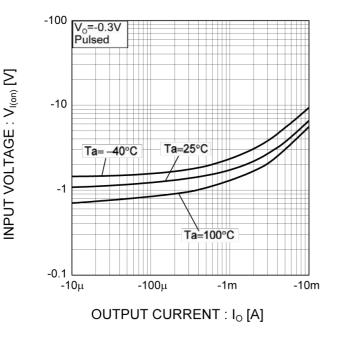


Fig.2 Output current vs. input voltage (OFF characteristics)

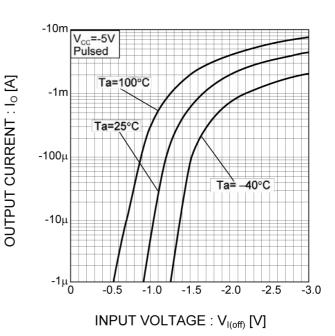


Fig.3 Output current vs. output voltage

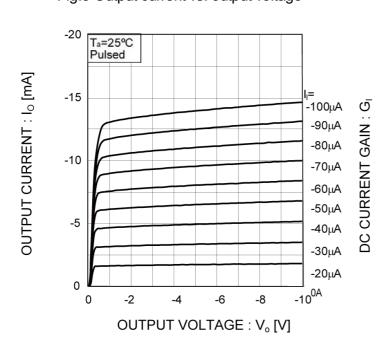
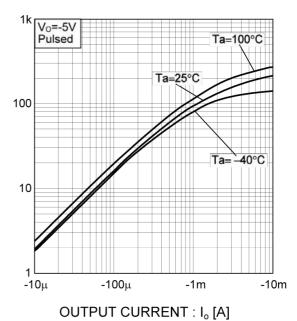
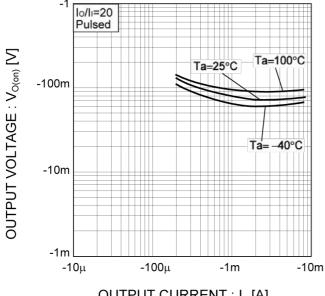


Fig.4 DC current gain vs. output current

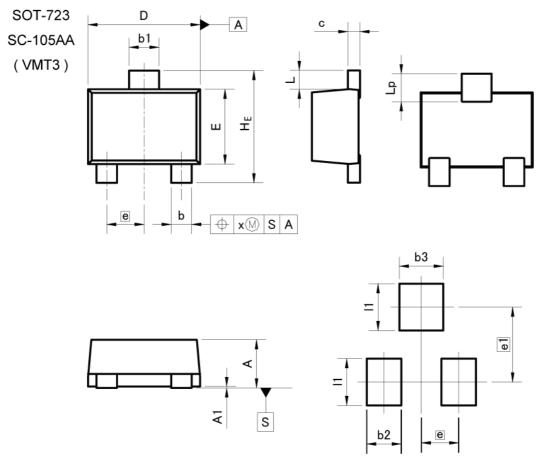


●Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]



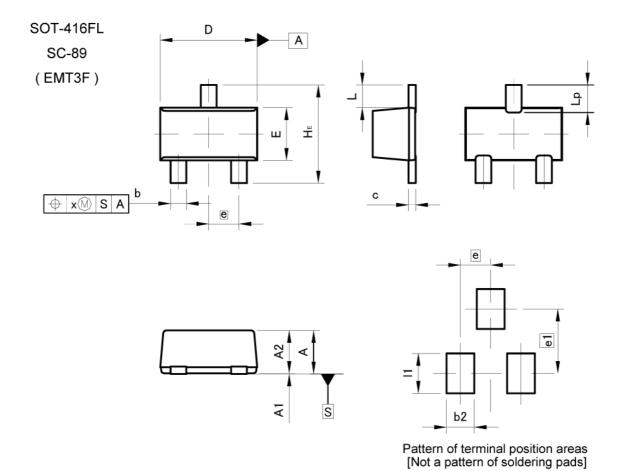
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIM	ETERS	INC	HES
DIM [MIN	MAX	MIN	MAX
Α	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
С	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
е	0.4	40	0.02	
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	0.012
Lp	0.20	0.40	0.008	0.016
х	#	0.10	<u> </u>	0.004

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	<u> </u>	0.37	544	0.015
b3	223	0.47	922	0.019
e1	0.	80	0.031	
11	5 98	0.50	250	0.020

Dimension in mm/inches



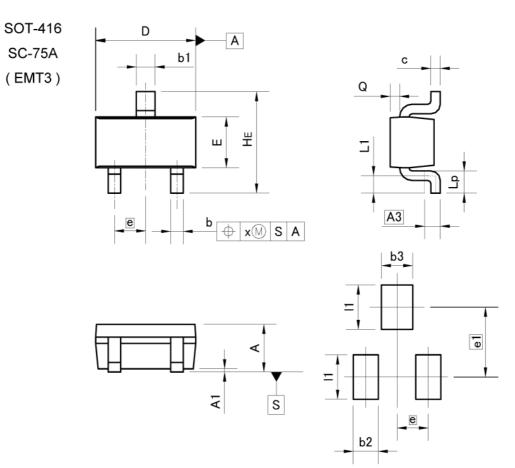


DIM -	MILIM	ETERS	INC	HES	
DIM L	MIN	MAX	MIN	MAX	
Α	0.65	0.85	0.026	0.033	
A1	0.00	0.10	0.000	0.004	
A2	0.60	0.80	0.024	0.031	
b	0.21	0.36	0.008	0.014	
С	0.08	0.18	0.003	0.007	
D	1.50	1.70	0.059	0.067	
E	0.76	0.96	0.030	0.038	
е	0.9	50	0.0	0.020	
HE	1.50	1.70	0.059	0.067	
L	0.3	37	0.0	15	
Lp	0.35	0.55	0.014	0.022	
х		0.10	-	0.004	

DIM -	MILIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b2	= 8	0.46	-	0.018
e1		1.05		0.041
11	#	0.65	=	0.026

Dimension in mm/inches





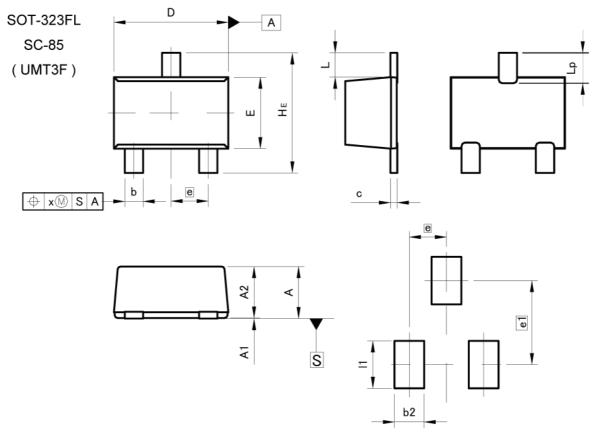
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM [MIN	MAX	MIN	MAX
Α	0.60	0.80	0.024	0.031
A1	0.00	0.10	0.000	0.004
A3	0.	25	0.0	10
b	0.15	0.30	0.006	0.012
b1	0.25	0.40	0.010	0.016
С	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.70	0.90	0.028	0.035
е	0.	50	0.0	20
HE	1.40	1.80	0.055	0.071
L1	0.10	<u>#</u> 3	0.004	÷
Lp	0.15		0.006	255
Q	0.05	0.25	0.002	0.010
х	2 7	0.10	_	0.004

DIM -	MILIMETERS		INCHES	
DIM L	MIN	MAX	MIN	MAX
b2	244	0.40	-	0.016
b3	10 48	0.50	; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; ; 	0.020
e1	1.	10	0.0	043
11		0.70	-	0.028

Dimension in mm/inches





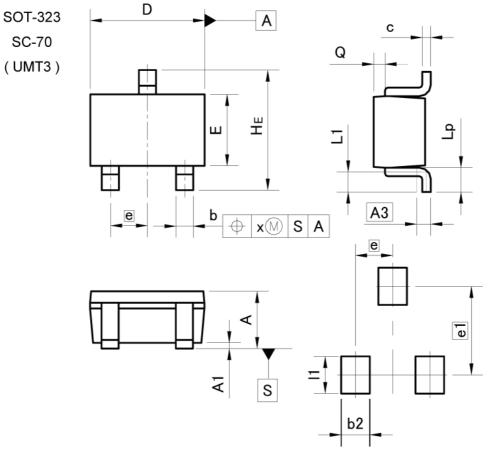
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.85	1.05	0.033	0.041	
A1	0.00	0.10	0.000	0.004	
A2	0.80	1.00	0.031	0.039	
b	0.27	0.42	0.011	0.017	
С	0.08	0.18	0.003	0.007	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.0	65	0.0	026	
HE	2.00	2.20	0.079	0.087	
L	0.4	25	0.0	17	
Lp	0.43	0.63	0.017	0.025	
х	==:	0.10	-	0.004	

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
b2	- s	0.52	-	0.020
e1	1.47		0.0	58
11		0.83	-	0.033

Dimension in mm/inches





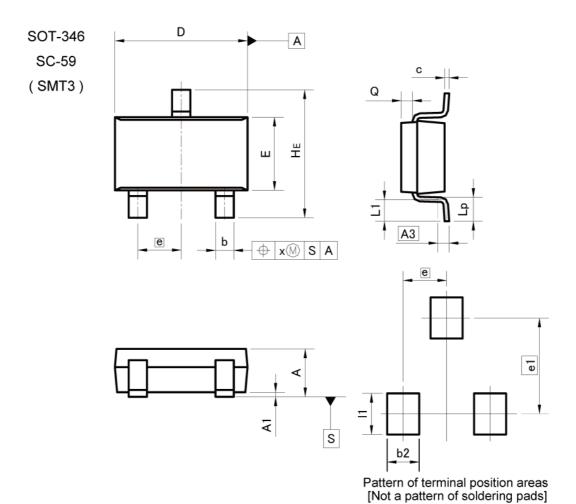
Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.25		0.010	
b	0.25	0.40	0.010	0.016
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.65		0.0	26
HE	2.00	2.20	0.079	0.087
L1	0.10	0.40	0.004	0.016
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х	=	0.10	-	0.004

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	<u>644</u> 9	0.50	~=	0.020	
e1	1.55		0.0	0.061	
11	-	0.65	1000	0.026	

Dimension in mm/inches





DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
Α	1.00	1.30	0.039	0.051
A1	0.00	0.10	0.000	0.004
A3	0.25		0.010	
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.95		0.0	37
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.20	0.30	0.008	0.012
х	-3	0.10	4 7	0.004
У	=:	0.10	97	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-2	0.60	×=	0.024
e1	2.10		0.083	
11	-8	0.90	(=	0.035

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	СГУССШ	CLASS II b	CLASSⅢ
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSIII

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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