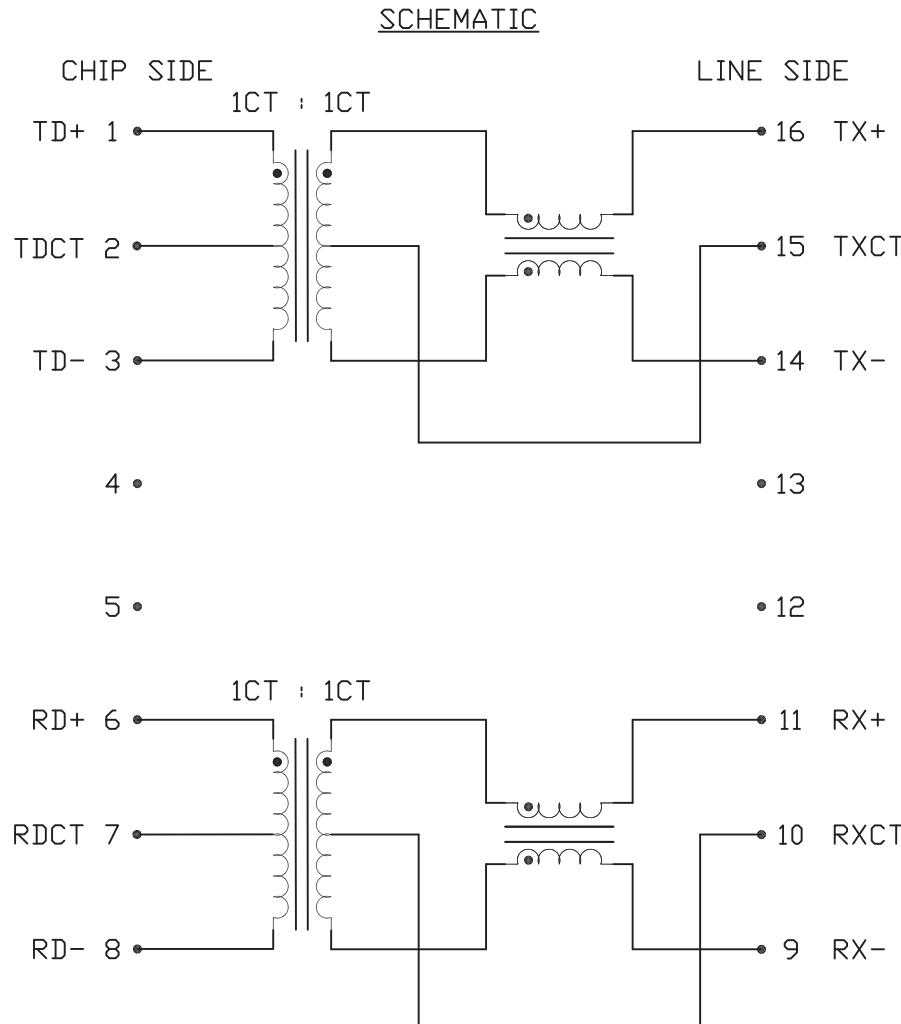


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NOTES :

THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS.
PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.

ELECTRICAL CHARACTERISTICS @25°C



TRANS RATIO

1-2-3 : 16-15-14
6-7-8 : 11-10-9

1CT : 1CT
1CT : 1CT

POLARITY

DCL

Tx PIN 1-3
Rx PIN 6-8

PER DOT CONVENTION
350μH MIN
@100kHz, 100mVrms
w/8mA DC BIAS

LEAKAGE INDUCTANCE, L_e
CHIP SIDE WITH LINE SIDE SHORT

0.3μH MAX
@1MHz, 20mVrms

INTERWINDING CAPACITANCE, $C_{w/w}$
CHIP SIDE TO LINE SIDE

30pF MAX
@1MHz, 20mVrms

DC RESISTANCE

PIN (1-3) = (6-8)

1.1Ωhm MAX

INSERTION LOSS
300kHz - 80MHz

1.0dB MAX

RETURN LOSS

300kHz - 30MHz
30MHz - 60MHz
60MHz - 80MHz

18dB MIN
18-20dB(G(F/30MHz) dB MIN
12dB MIN

COMMON TO COMMON MODE REJ
300kHz - 100MHz

40dB TYP

COMMON TO DIFFERENTIAL MODE REJ
300kHz - 100MHz

40dB MIN

CROSSTALK
300kHz - 100MHz

40dB MIN

HIPOT

1500Vrms

OPERATING TEMPERATURE -40°C TO +85°C

REV. : F PAGE : 2

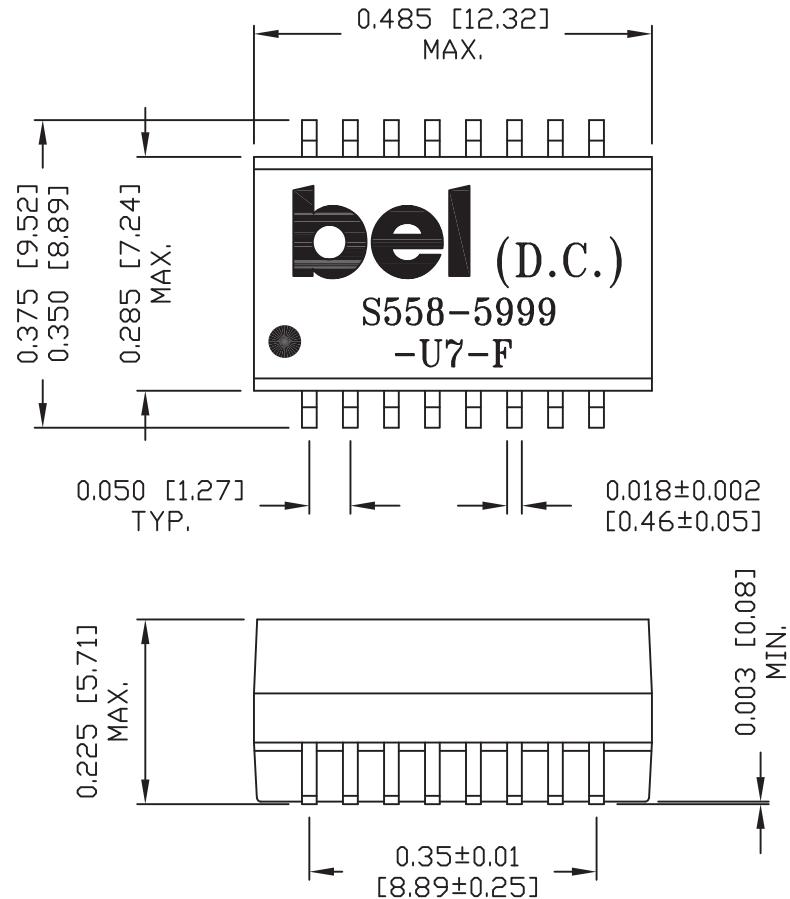
ORIGINATED BY	DATE	TITLE 10/100BT MAGNETICS MODULE S558-5999-U7-F	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REF.	UNIT : INCH [mm] SCALE : N/A	SIZE : A4
DRAWN BY	DATE		X5585999U7-F	TOL. IN INCH	.X		
ZC Guo	2015-07-16		FILE NAME	.XX	.XXX		
X5585999U7FF.DWG							



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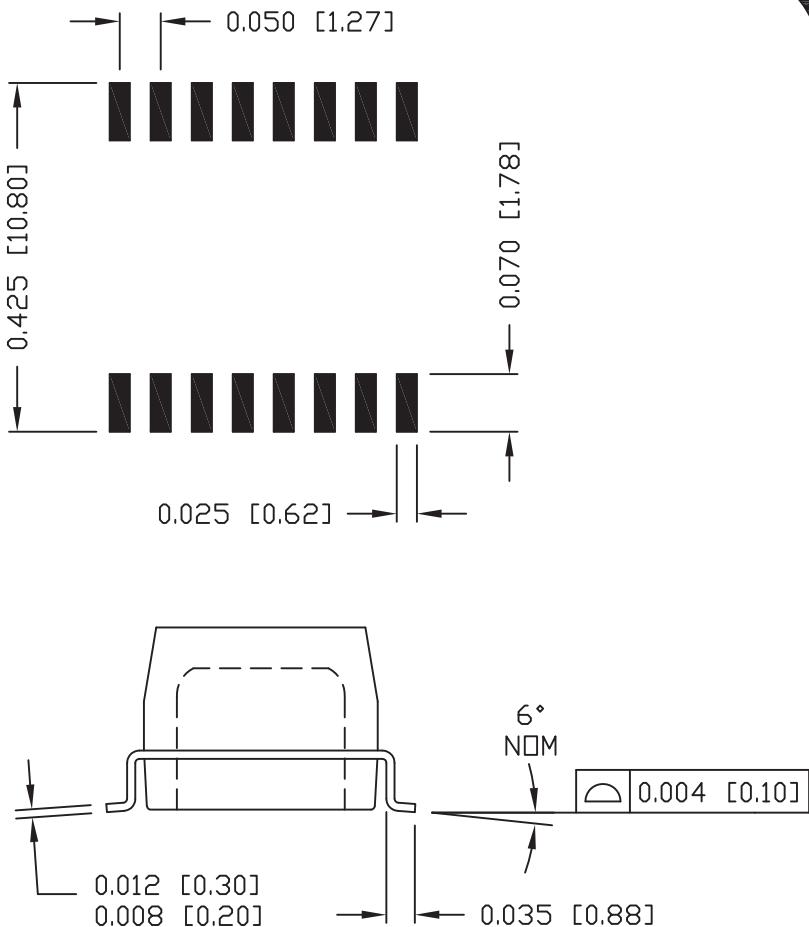


SUGGESTED PCB PAD LAYOUT



NOTES :

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "QBS001".
3. THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS.
PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.



REV. : F PAGE : 3



ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.		UNIT : INCH [mm]	SCALE : N/A	SIZE : A4
DRAWN BY	DATE		X5585999U7-F	TOL. IN INCH	.X			
Lawrence Tsang	2015-07-16	MECHANICAL DRAWING	.XX	±0.01				
ZC Guo	2015-07-16	S558-5999-U7-F	XXX	±0.005				

Mouser Electronics

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