N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ.)
30	0.050 at V_{GS} = 10 V	4.2	2.6
	0.079 at V_{GS} = 4.5 V	3.0	

FEATURES

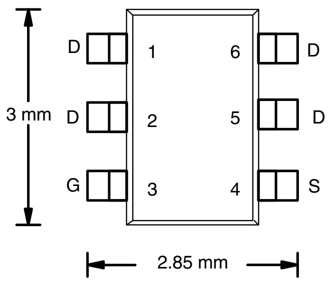
- **Halogen-free According to IEC 61249-2-21 Definition**
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

Pb-free
RoHS COMPLIANT
HALOGEN FREE Available

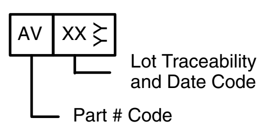
APPLICATIONS

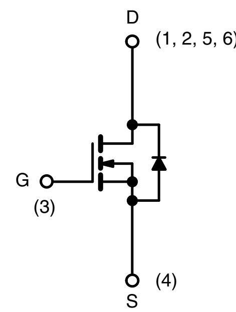
- Load Switch
  - Notebook PC

TSOP-6
Top View



Marking Code





N-Channel MOSFET

Ordering Information: Si3454CDV-T1-E3 (Lead (Pb)-free)
Si3454CDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	4.2	A
	T_C = 70 °C		3.3	
	T_A = 25 °C		3.8[b, c]	
	T_A = 70 °C		3.1[b, c]	
Pulsed Drain Current		I_{DM}	20	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	1.25	A
	T_A = 25 °C		1.04[b, c]	
Maximum Power Dissipation	T_C = 25 °C	P_D	1.5	W
	T_C = 70 °C		0.9	
	T_A = 25 °C		1.25[b, c]	
	T_A = 70 °C		0.8[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[b, d]	t ≤ 5 s	R_{thJA}	80	100	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	70	85	

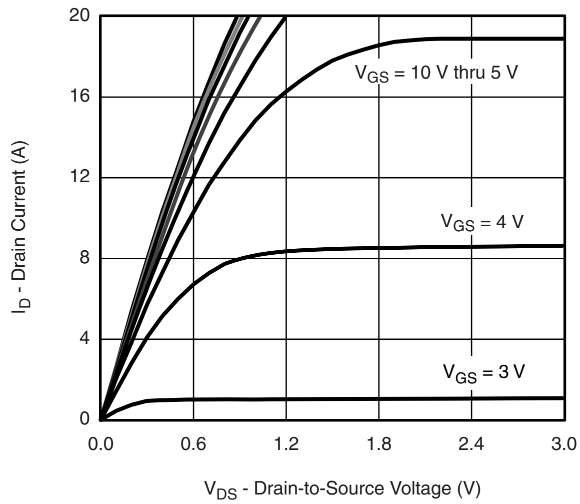
Notes:
a. Based on T_C = 25 °C.
b. Surface Mounted on 1" x 1" FR4 board.
c. t = 5 s.
d. Maximum under Steady State conditions is 145 °C/W.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		27.5		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 30 V, V_{GS} = 0 V			1	µA
		V_{DS} = 30 V, V_{GS} = 0 V, T_J = 85 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} = ≥ 5 V, V_{GS} = 10 V	20			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 3.8 A		0.041	0.050	Ω
		V_{GS} = 4.5 V, I_D = 3.1 A		0.066	0.079	
Forward Transconductance	g_{fs}	V_{DS} = 15 V, I_D = 3.8 A		8		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		305		pF
Output Capacitance	C_{oss}			52		
Reverse Transfer Capacitance	C_{rss}			27		
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 3.8 A		5.3	10.6	nC
		V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 3.8 A		2.6	5.2	
Gate-Source Charge	Q_{gs}			1.2		
Gate-Drain Charge	Q_{gd}			0.8		
Gate Resistance	R_g	f = 1 MHz	0.44	2.2	4.4	Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 4.8 Ω $I_D \cong$ 3.1 A, V_{GEN} = 10 V, R_g = 1 Ω		4	8	ns
Rise Time	t_r			8	16	
Turn-Off DelayTime	$t_{d(off)}$			11	18	
Fall Time	t_f			7	14	
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 4.8 Ω $I_D \cong$ 3.1 A, V_{GEN} = 4.5 V, R_g = 1 Ω		15	20	
Rise Time	t_r			12	18	
Turn-Off DelayTime	$t_{d(off)}$			8	16	
Fall Time	t_f			9	18	
Drain-Source Body Diode Characteristics						
Continous Source-Drain Diode Current	I_S	T_C = 25 °C			1.25	A
Pulse Diode Forward Current[a]	I_{SM}				20	
Body Diode Voltage	V_{SD}	I_S = 3.1 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 3.1 A, dI/dt = 100 A/µs		11.5	17.8	ns
Body Diode Reverse Recovery Charge	Q_{rr}			5	10	nC
Reverse Recovery Fall Time	t_a			7.6		ns
Reverse Recovery Rise Time	t_b			3.9		

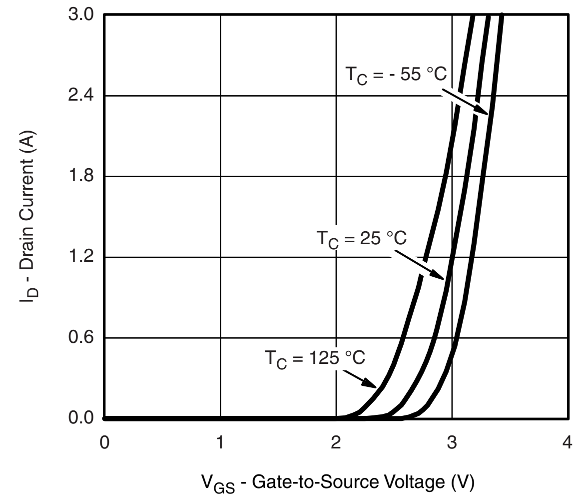
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

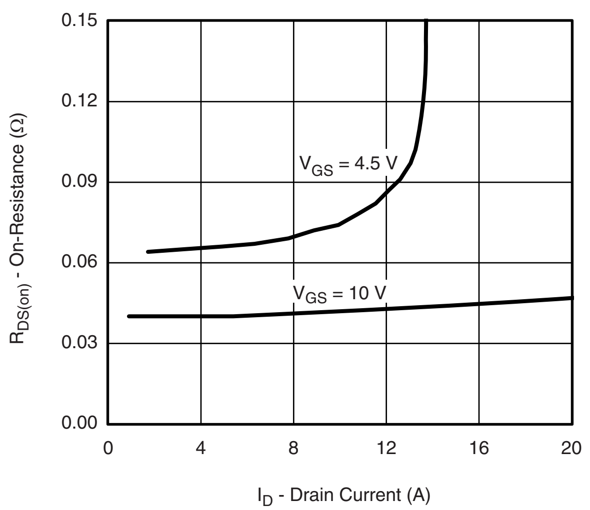
TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



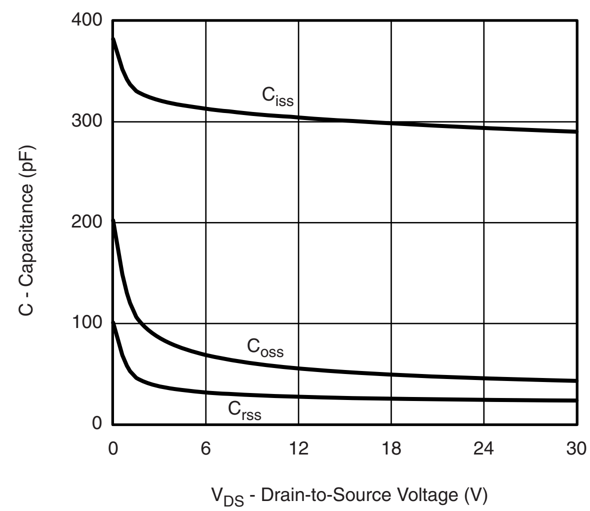
Output Characteristics



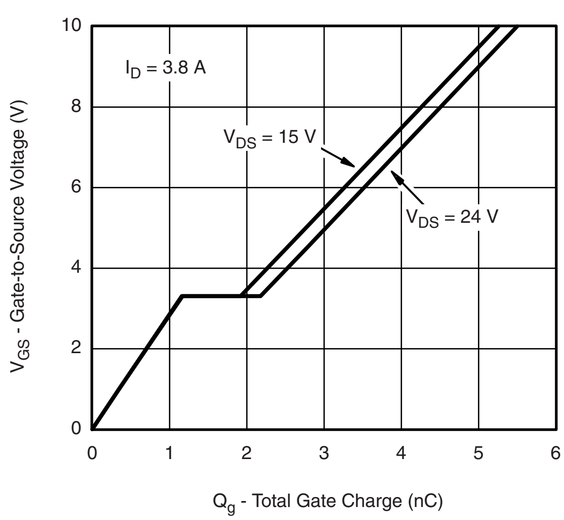
Transfer Characteristics Curves vs. Temp.



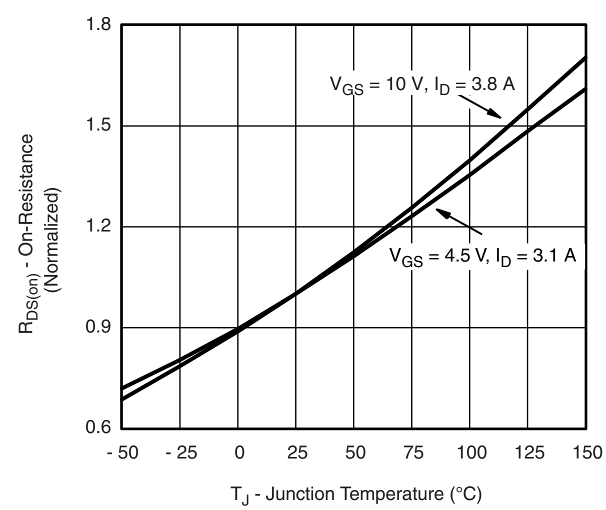
On-Resistance vs. Drain Current



Capacitance

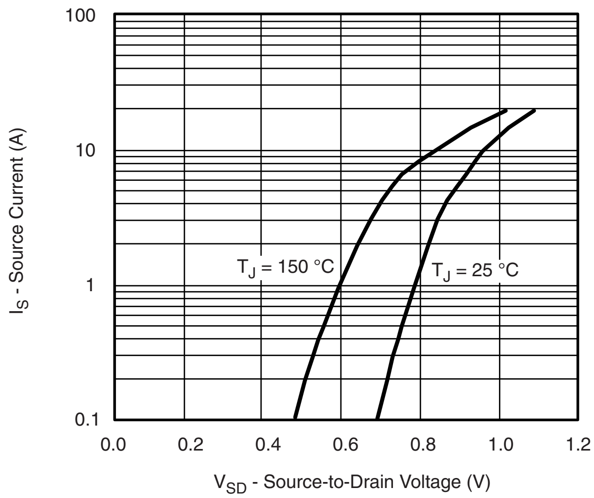


Q_g - Gate Charge

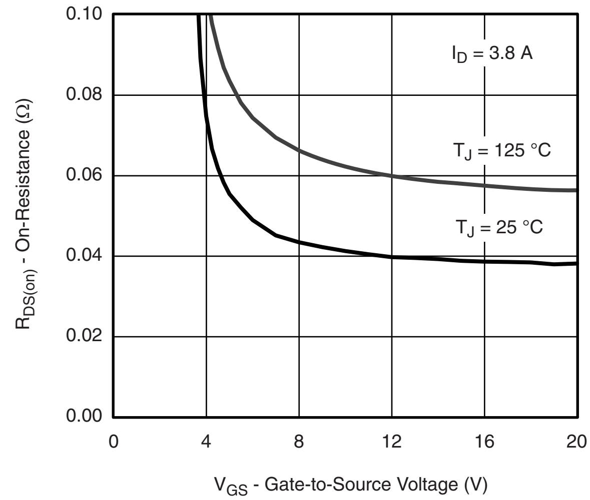


On-Resistance vs. Junction Temperature

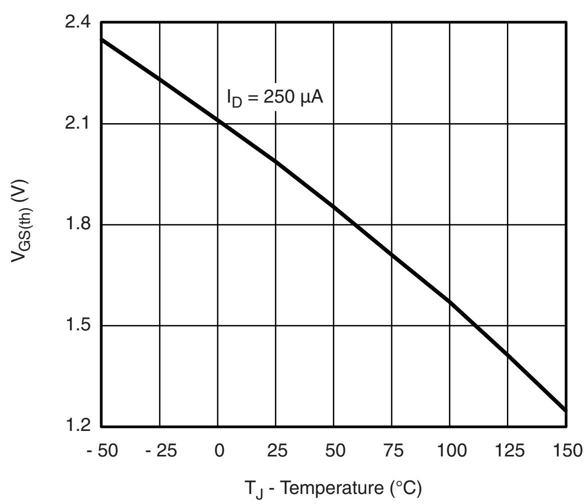
TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



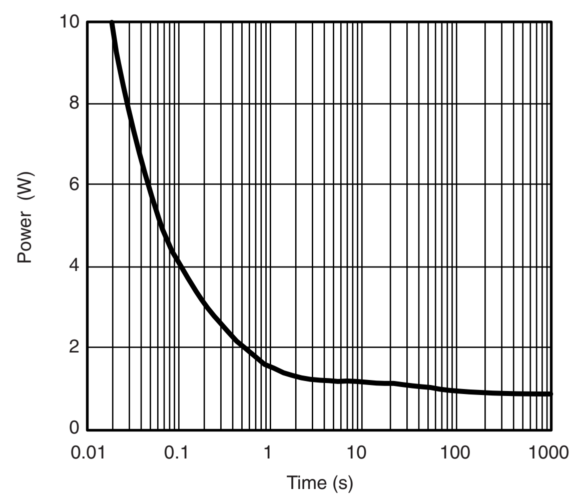
Source-Drain Diode Forward Voltage



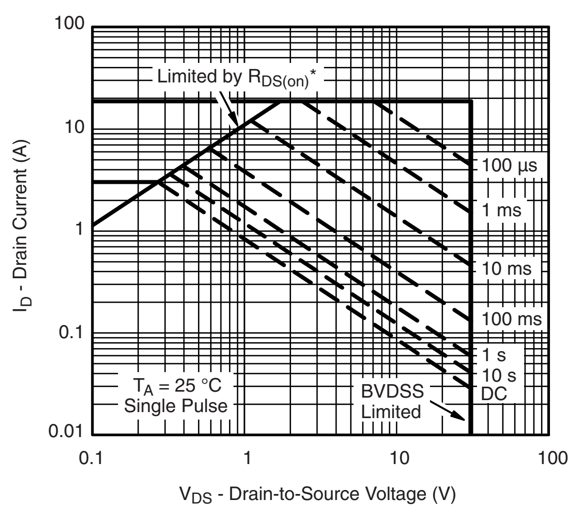
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



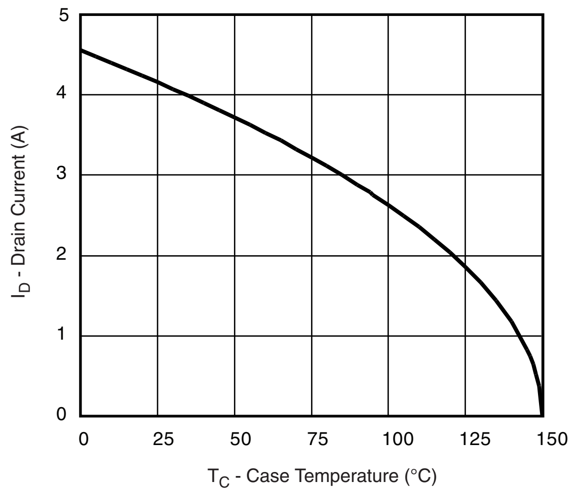
Single Pulse Power



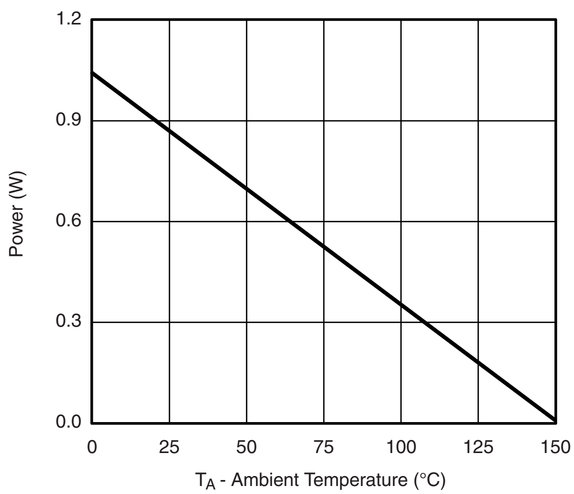
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

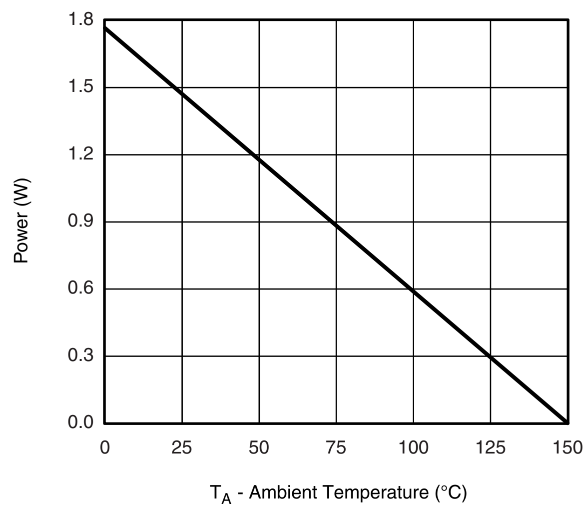
TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



Current Derating*



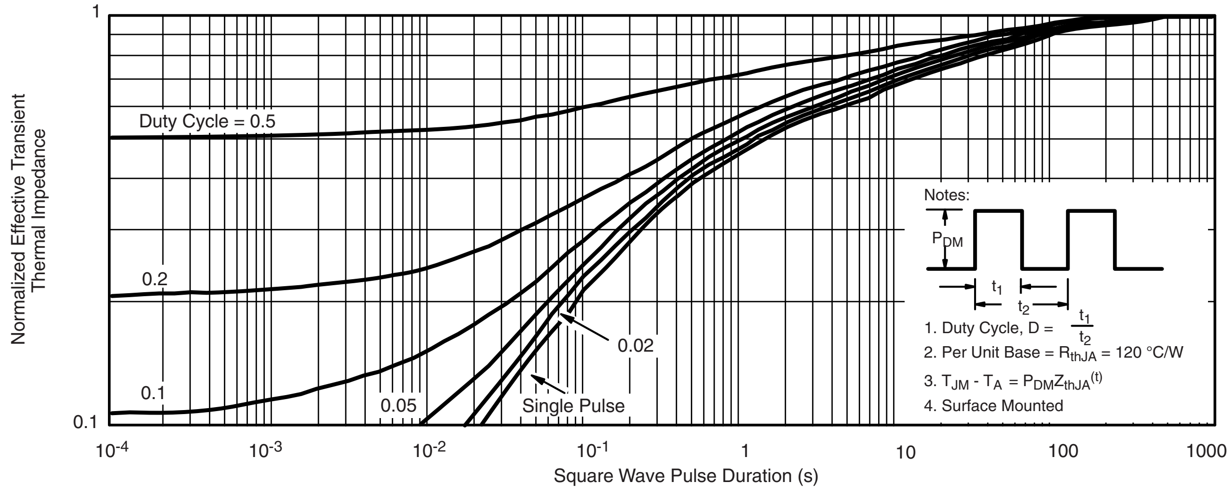
Power Derating, Junction-to-Ambient



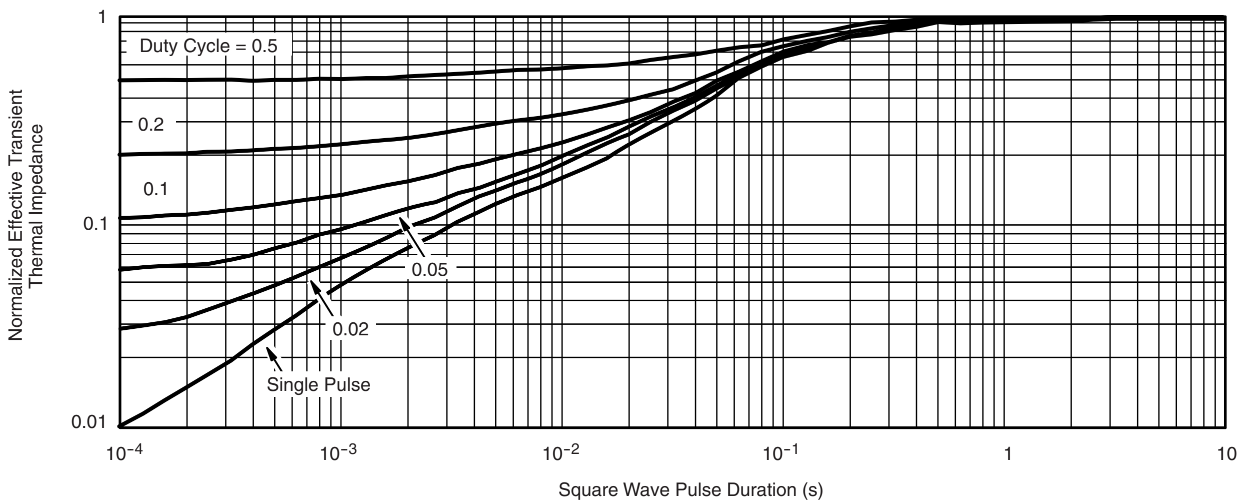
Power Derating, Junction-to-Case

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS T_A = 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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