

Naos Raptor 3A: Non-Isolated DC-DC Power Modules 4.5Vdc –14Vdc input; 0.59Vdc to 6Vdc Output; 3A Output Current



RoHS Compliant

Applications

- Distributed power architectures
- Intermediate bus voltage applications
- Telecommunications equipment
- Servers and storage applications
- Networking equipment
- Industrial applications

Features

- Compliant to RoHS EU Directive 2002/95/EC (Z versions)
- Compatible in a Pb-free or SnPb wave-soldering environment (Z versions)
- Wide Input voltage range (4.5Vdc-14Vdc)
- Output voltage programmable from 0.59 Vdc to 6Vdc via external resistor
- Tunable LoopTM to optimize dynamic output voltage response
- Fixed switching frequency
- Output overcurrent protection (non-latching)
- Over temperature protection
- Remote On/Off
- Cost efficient open frame design
- Small size: 10.4 mm x 16.5 mm x 7.84 mm

(0.41 in x 0.65 in x 0.31 in)

- Wide operating temperature range (-40°C to 85°C)
- UL* 60950-1Recognized, CSA[†] C22.2 No. 60950-1-03 Certified, and VDE[‡] 0805:2001-12 (EN60950-1) Licensed
- ISO** 9001 and ISO 14001 certified manufacturing facilities

Description

The Naos Raptor 3A SIP power modules are non-isolated dc-dc converters in an industry standard package that can deliver up to 3A of output current with a full load efficiency of 93% at 3.3Vdc output voltage ($V_{IN} = 12Vdc$). These modules operate over a wide range of input voltage ($V_{IN} = 4.5Vdc-14Vdc$) and provide a precisely regulated output voltage from 0.59Vdc to 6Vdc, programmable via an external resistor. Features include remote On/Off, adjustable output voltage, over current and over voltage protection. A new feature, the Tunable LoopTM, allows the user to optimize the dynamic response of the converter to match the load.

^{*} UL is a registered trademark of Underwriters Laboratories, Inc.

[†] CSA is a registered trademark of Canadian Standards Association.

[‡] VDE is a trademark of Verband Deutscher Elektrotechniker e.V.
** ISO is a registered trademark of the International Organization of Standards

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

| Parameter | Device | Symbol | Min | Max | Unit |
|--------------------------------------|--------|------------------|------|-----|------|
| Input Voltage | All | V _{IN} | -0.3 | 15 | Vdc |
| Continuous | | | | | |
| Operating Ambient Temperature | All | T _A | -40 | 85 | °C |
| (see Thermal Considerations section) | | | | | |
| Storage Temperature | All | T _{stg} | -55 | 125 | °C |

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|--|------------------------------|--------------------------|-----|-----|-----|------------------|
| Operating Input Voltage | All | V _{IN} | 4.5 | 12 | 14 | Vdc |
| Maximum Input Current | All | I _{IN,max} | | | 2.6 | Adc |
| (V _{IN} =4.5V to 14V, $I_0=I_{0, max}$) | | | | | | |
| Input No Load Current | | | | | | |
| $(V_{IN} = 9Vdc, I_O = 0, module ON)$ | V _{O,set} = 0.6 Vdc | IIN,No load | | 26 | | mA |
| $(V_{IN} = 12Vdc, I_{O} = 0, module ON)$ | $V_{O,set}$ = 5.0Vdc | I _{IN,No load} | | 60 | | mA |
| Input Stand-by Current | All | I _{IN,stand-by} | | 1 | | mA |
| $(V_{IN} = 12Vdc, module disabled)$ | | | | | | |
| Inrush Transient | All | l ² t | | | 1 | A ² s |
| Input Reflected Ripple Current, peak-to-peak (5Hz to 20MHz, 1 μ H source impedance; V _{IN} =0 to 14V, I ₀ = I _{Omax} ; See Test Configurations) | All | | | 35 | | mAp-p |
| Input Ripple Rejection (120Hz) | All | | | 50 | | dB |

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This power module can be used in a wide variety of applications, ranging from simple standalone operation to being part of a complex power architecture. To preserve maximum flexibility, internal fusing is not included, however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a fast-acting fuse with a maximum rating of 5 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data sheet for further information.

Electrical Specifications (continued)

| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|---|------------------------------|---------------------|------|------|------|-----------------------|
| Output Voltage Set-point (with 0.5% tolerance for external resistor used to set output voltage) | All | V _{O, set} | -1.5 | | +1.5 | % V _{O, set} |
| Output Voltage | All | V _{O, set} | -3.0 | _ | +3.0 | % V _{O, set} |
| (Over all operating input voltage, resistive load, and temperature conditions until end of life) | | | | | | |
| end of life) (with 0.1% tolerance trim resistor) | | | | | | |
| Adjustment Range | All | Vo | 0.59 | | 6.0 | Vdc |
| Selected by an external resistor | | | | | | |
| Output Regulation (for $V_o \ge 2.5Vdc$) | | | | | | |
| Line ($V_{IN}=V_{IN, min}$ to $V_{IN, max}$) | All | | -0.2 | | +0.2 | $\% V_{O, set}$ |
| Load $(I_O=I_{O, min} \text{ to } I_{O, max})$ | All | | | | 0.8 | $\% V_{O, set}$ |
| Output Regulation (for V_o <2.5Vdc) | | | | | | |
| Line ($V_{IN}=V_{IN, min}$ to $V_{IN, max}$) | All | | -5 | | +5 | mV |
| Load ($I_0=I_{O, min}$ to $I_{O, max}$) | All | | | | 20 | mV |
| Output Ripple and Noise on nominal output | | | | | | |
| (V _{IN} =V _{IN, nom} and I _O =I _{O, min} to I _{O, max} Cout = 0.0 μ F) | | | | | | |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | V_{o} = 0.59Vdc | | _ | | 20 | mV_{pk-pk} |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | $V_0 = 0.9 V dc$ | | _ | | 25 | mV _{pk-pk} |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | $V_0 = 2.5 V dc$ | | _ | | 30 | mV _{pk-pk} |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | $V_0 = 3.3 Vdc$ | | — | | 40 | mV_{pk-pk} |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | $V_0 = 5.0 V dc$ | | _ | | 50 | $mV_{\text{pk-pk}}$ |
| Peak-to-Peak (5Hz to 20MHz bandwidth) | $V_0 = 6.0 V dc$ | | — | | 60 | mV_{pk-pk} |
| External Capacitance ¹ | | | | | | |
| Without the Tunable Loop [™] | | | | | | |
| ESR ≥ 1 mΩ | All | $C_{O,\text{max}}$ | 0 | | 200 | μF |
| With the Tunable Loop [™] | | | | | | |
| ESR ≥ 0.15 mΩ | All | $C_{O, max}$ | 0 | | 1000 | μF |
| ESR ≥ 10 mΩ | All | $C_{O, max}$ | 0 | | 5000 | μF |
| Output Current | All | I _o | 0 | | 3 | Adc |
| Output Current Limit Inception (Hiccup Mode) | All | I _{O, lim} | | 170 | | % I _{o,max} |
| Output Short-Circuit Current | All | I _{O, s/c} | | 6.5 | | Adc |
| (V₀≤250mV) (Hiccup Mode) | | | | | | |
| Efficiency (Vin=6V) | V _{O,set} = 0.59Vdc | η | | 77.9 | | % |
| V _{IN} = 12Vdc, T _A =25°C | V _{o, set} = 1.2Vdc | η | | 82.5 | | % |
| $I_{O}=I_{O, max}, V_{O}=V_{O, set}$ | V _{O,set} = 1.5Vdc | η | | 87.1 | | % |
| | V _{O,set} = 1.8Vdc | η | | 88.9 | | % |
| | V _{O,set} = 2.5Vdc | η | | 91.4 | | % |
| | V _{O,set} = 3.3Vdc | η | | 93.0 | | % |
| | V _{O,set} = 5.0Vdc | η | | 95.0 | | % |
| | V _{O,set} = 6.0Vdc | η | | 95.8 | | % |
| Switching Frequency | All | f _{sw} | _ | 600 | _ | kHz |

¹ External capacitors may require using the new Tunable Loop[™] feature to ensure that the module is stable as well as getting the best transient response. See the Tunable Loop[™] section for details.

Electrical Specifications (continued)

| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|---|--------|-----------------|-----|-----|-----|------|
| Dynamic Load Response | | | | | | |
| (dlo/dt=10A/µs; V _{IN} = V _{IN, nom} ; V _{out} = 1.8V, T _A =25°C) | | | | | | |
| Load Change from Io= 50% to 100% of Io,max; Co = 0.0 μF | | | | | | |
| Peak Deviation | All | V _{pk} | | 120 | | mV |
| Settling Time (Vo<10% peak deviation) | All | ts | | 120 | | μs |
| Load Change from lo= 100% to 50%of lo,max: Co = 0.0 µF | | | | | | |
| Peak Deviation | All | V _{pk} | | 120 | | mV |
| Settling Time (Vo<10% peak deviation) | All | ts | | 120 | | μs |

General Specifications

| Parameter | Min | Тур | Max | Unit |
|--|-----|------------|-----|---------|
| Calculated MTBF (V _{IN} =12V, V _O =5V, I_O =0.8 $I_{O, max}$, T _A =40°C) Telecordia Method | | 9,518,320 | | Hours |
| Weight | | 2.9 (0.10) | _ | g (oz.) |

Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

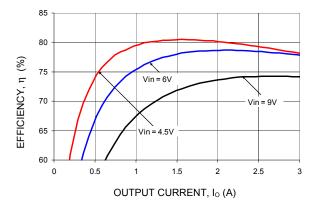
| Parameter | Device | Symbol | Min | Тур | Max | Unit |
|--|--------|--------|------|-----|-----|-----------------------|
| On/Off Signal interface | | | | | | |
| (V_IN=V_{IN, min} to V_{IN, max}; Open collector or equivalent | | | | | | |
| signal referenced to GND) | | | | | | |
| Logic High (On/Off pin open - Module ON) | | | | | | |
| Input High Current | All | Ін | | — | 0.5 | mA |
| Input High Voltage | All | Vін | 1.0 | _ | 12 | V |
| Logic Low (Module Off) | | | | | | |
| Input Low Current | All | li∟ | _ | _ | 200 | μA |
| Input Low Voltage | All | VIL | -0.3 | _ | 0.4 | V |
| Turn-On Delay and Rise Times | | | | | | |
| $(I_0=I_{O, max}, V_{IN} = V_{IN, nom}, V_o$ to within ±1% of steady state) Case 1: On/Off is enabled and then input power is applied (delay from instant at which $V_{IN} = V_{IN, min}$ until Vo=10% of Vo,set) | All | Tdelay | | 2 | 3 | msec |
| Case 2: Input power is applied for at least one second and then On/Off input is set enabled (delay from instant at which On/Off is enabled until Vo=10% of Vo, set) | All | Tdelay | | 2 | 3 | msec |
| Output voltage Rise time (time for Vo to rise from 10% of V _{o,set} to 90% of Vo, set) | All | Trise | | 3 | 5 | msec |
| Output voltage overshoot | | | | | 0.5 | % V _{O, set} |
| $I_{O}\text{=}$ $I_{O,\text{ max}}\text{;}$ V_{IN} = $V_{\text{IN},\text{ min}}$ to $V_{\text{IN},\text{ max}}\text{,}$ T_{A} = 25 $^{\circ}\text{C}$ | | | | | | |
| Overtemperature Protection | All | | | 120 | | °C |
| Input Undervoltage Lockout | | | | | | |
| Turn-on Threshold | All | | | 4.2 | | Vdc |
| Turn-off Threshold | All | | | 4.1 | | Vdc |

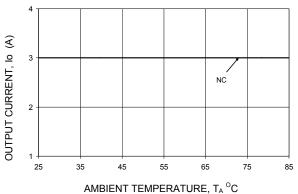
Characteristic Curves

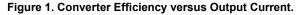
The following figures provide typical characteristics for the Naos Raptor 3A module at 0.6Vout and at 25°C.

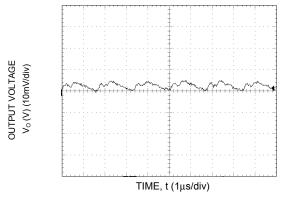
UUIPUI VULIAGE

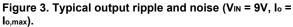
UUIPUI CURRENI,











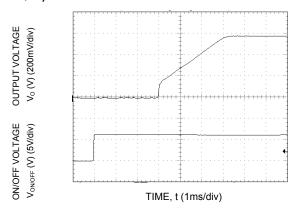


Figure 5. Typical Start-up Using On/Off Voltage ($I_0 = I_{0,max}$).

Figure 2. Derating Output Current versus Ambient Temperature and Airflow.

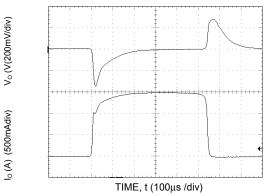


Figure 4. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =9V.

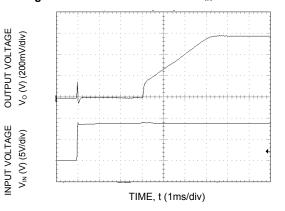


Figure 6. Typical Start-up Using Input Voltage (V_{IN} = 9V, Io = Io,max).

UUIPUI VULIAGE Vo (V) (100mV/div)

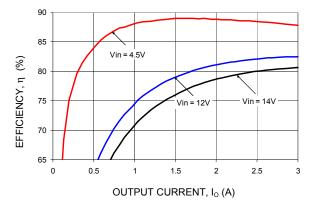
UUTPUT CURRENT, Io (A) (1Adiv)

OUTPUT VOLTAGE

INPUT VOLTAGE

Characteristic Curves (continued)

The following figures provide typical characteristics for the Naos Raptor 3A module at 1.2Vout and at 25°C.



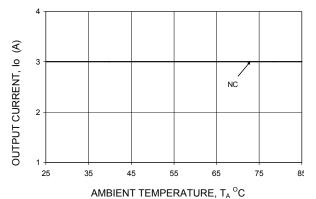


Figure 7. Converter Efficiency versus Output Current.

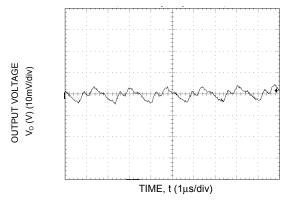


Figure 9. Typical output ripple and noise (V_{IN} = 12V, I_o = $I_{o,max}$).

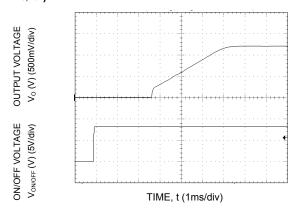


Figure 11. Typical Start-up Using On/Off Voltage ($I_0 = I_{0,max}$).

Figure 8. Derating Output Current versus Ambient Temperature and Airflow.

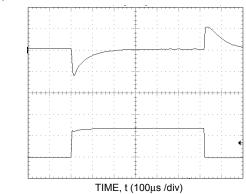


Figure 10. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =12V.

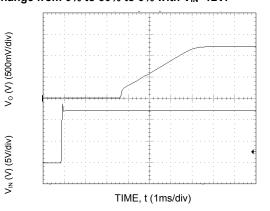


Figure 12. Typical Start-up Using Input Voltage (VIN = 12V, $I_0 = I_{0,max}$).

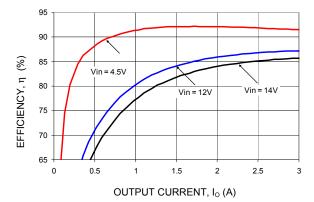
UUIPUI VULIAGE V_o (V) (100mV/div)

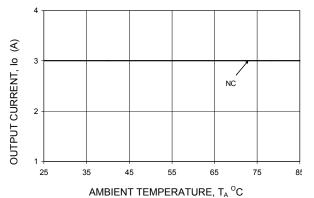
UUIPUI CURRENI, lo (A) (1Adiv)

INPUT VOLTAGE

Characteristic Curves (continued)

The following figures provide typical characteristics for the Naos Raptor 3A module at 1.8Vout and at 25°C.







OUTPUT VOLTAGE V_o (V) (10mV/div) TIME, t (1µs/div)

Figure 15. Typical output ripple and noise (VIN = 12V, Io = lo,max).

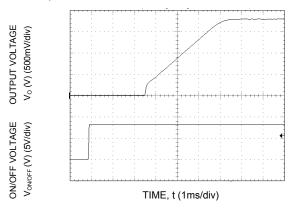


Figure 17. Typical Start-up Using On/Off Voltage (Io = lo,max).

Figure 14. Derating Output Current versus Ambient Temperature and Airflow.

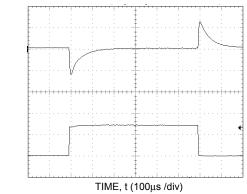


Figure 16. Transient Response to Dynamic Load Change from 0% to 50% to 0% with VIN=12V.

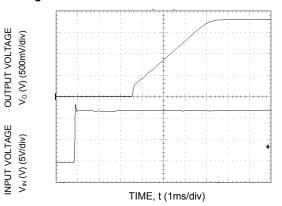


Figure 18. Typical Start-up Using Input Voltage (VIN = $12V, I_0 = I_{0,max}$).

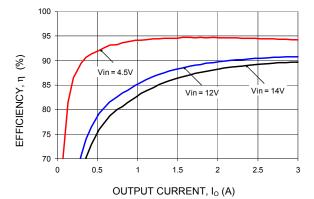
Characteristic Curves (continued)

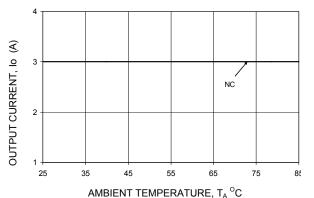
The following figures provide thermal derating curves for the Naos Raptor 3A module at 2.5Vout and at 25°C.

UUIPUI VULIAGE V_o (V) (200mV/div)

UUIPUI CURRENI, (5Adiv)

I_o (A)







OUTPUT VOLTAGE V_o (V) (10mV/div) TIME, t (1µs/div)

Figure 21. Typical output ripple and noise (VIN = 12V, Io = lo,max).

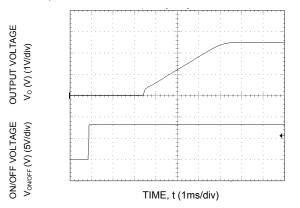


Figure 23. Typical Start-up Using On/Off Voltage (Io = lo,max).

Figure 20. Derating Output Current versus Ambient Temperature and Airflow.

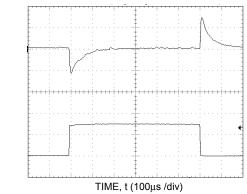


Figure 22. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =12V.

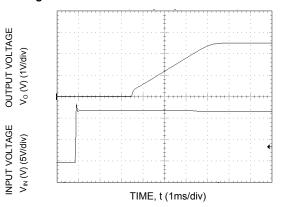


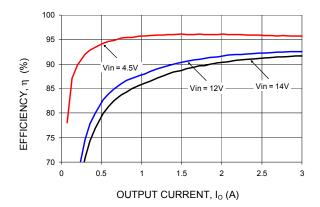
Figure 24. Typical Start-up Using Input Voltage (VIN = $12V, I_0 = I_{0,max}$).

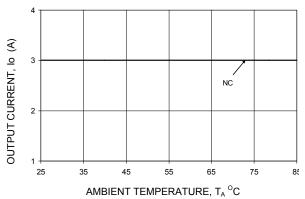
UUIPUI VULIAGE Vo (V) (100mV/div)

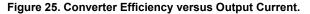
UUTPUT CURRENT, I_o (A) (1Adiv)

Characteristic Curves (continued)

The following figures provide typical characteristics for the Naos Raptor 3A module at 3.3Vout and at 25°C.







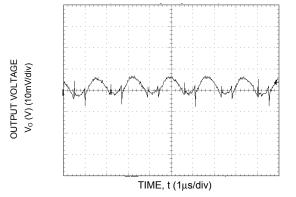


Figure 27. Typical output ripple and noise ($V_{IN} = 12V$, $I_o = I_{o,max}$).

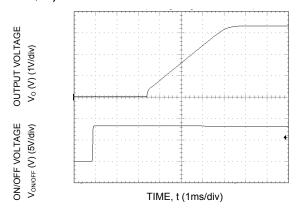


Figure 29. Typical Start-up Using On/Off Voltage ($I_0 = I_{0,max}$).

Figure 26. Derating Output Current versus Ambient Temperature and Airflow.

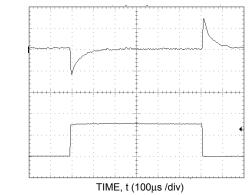


Figure 28. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =12V.

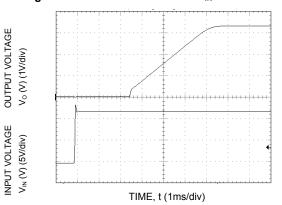


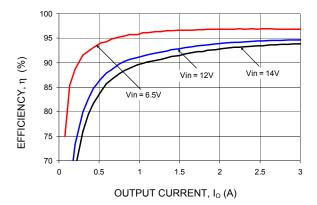
Figure 30. Typical Start-up Using Input Voltage (VIN = 12V, Io = Io,max).

UUIPUI VULIAGE

UUIPUI CURRENI,

Characteristic Curves (continued)

The following figures provide typical characteristics for the Naos Raptor 3A module at 5Vout and at 25°C.





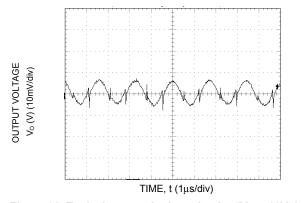


Figure 33. Typical output ripple and noise (VIN = 12V, $I_0 = I_{0,max}$).

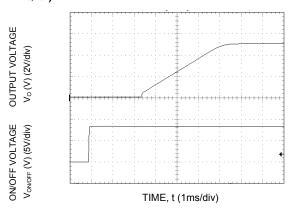


Figure 35. Typical Start-up Using On/Off Voltage (Io = Io,max).

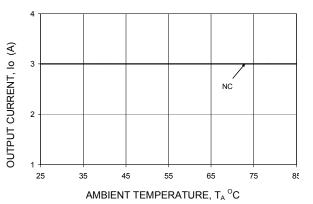


Figure 32. Derating Output Current versus Ambient Temperature and Airflow.

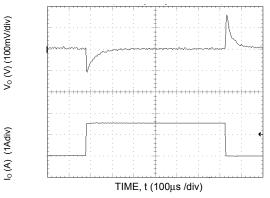


Figure 34. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =12V.

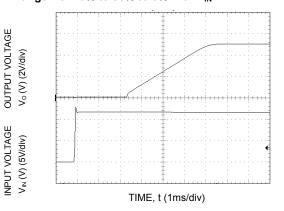


Figure 36. Typical Start-up Using Input Voltage (VIN = 12V, Io = Io,max).

Characteristic Curves

The following figures provide typical characteristics for the Naos Raptor 3A module at 6Vout and at 25°C.

UUIPUI VULIAGE

JUIPUI CURRENI,

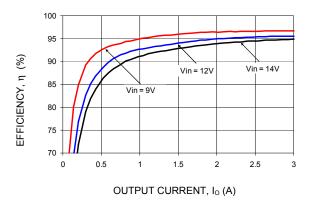


Figure 37. Converter Efficiency versus Output Current.

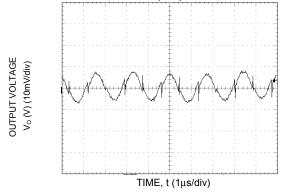


Figure 39. Typical output ripple and noise (VIN = 12V, $I_0 = I_{0,max}$).

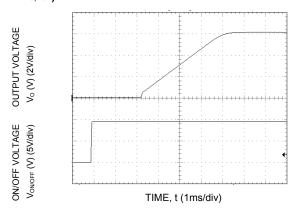


Figure 41. Typical Start-up Using On/Off Voltage (Io = Io,max).

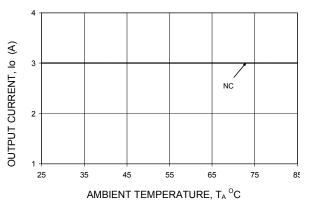


Figure 38. Derating Output Current versus Ambient Temperature and Airflow.

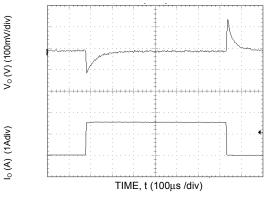


Figure 40. Transient Response to Dynamic Load Change from 0% to 50% to 0% with V_{IN} =12V.

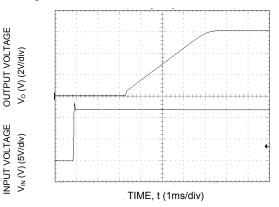


Figure 42. Typical Start-up Using Input Voltage (VIN = 12V, $I_0 = I_{0,max}$).

Test Configurations

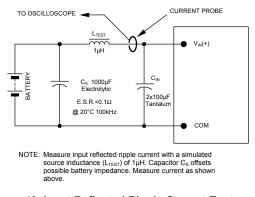
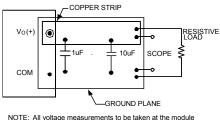
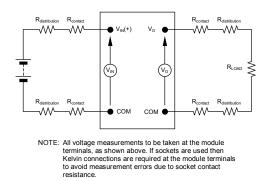


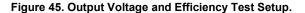
Figure 43. Input Reflected Ripple Current Test Setup.



VO IE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.







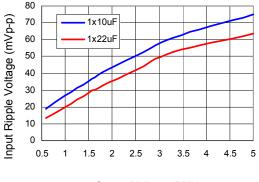
Efficiency
$$\eta = \frac{V_{o. I_0}}{V_{IN. I_{IN}}} \times 100 \%$$

Design Considerations

Input Filtering

The Naos Raptor 3A module should be connected to a low ac-impedance source. A highly inductive source can affect the stability of the module. An input capacitance must be placed directly adjacent to the input pin of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, low-ESR ceramic or polymer capacitors are recommended at the input of the module. Figure 46 shows the input ripple voltage for various output voltages at 3A of load current with 1x10 μ F or 1x22 μ F ceramic capacitors and an input of 12V.



Output Voltage (Vdc)

Figure 46. Input ripple voltage for various output voltages with 1x10 μ F or 1x22 μ F ceramic capacitors at the input (3A load). Input voltage is 12V.

Output Filtering

The Naos Raptor 3A modules are designed for low output ripple voltage and will meet the maximum output ripple specification with no external capacitors. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR ceramic and polymer are recommended to improve the dynamic response of the module. Figure 47 provides output ripple information for different external capacitance values at various Vo and for a load current of 3A. For stable operation of the module, limit the capacitance to less than the maximum output capacitance as specified

in the electrical specification table. Optimal performance of the module can be achieved by using the Tunable LoopTM feature described later in this data sheet.

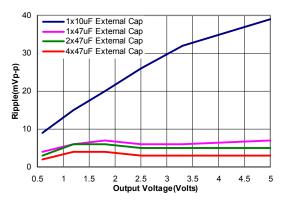


Figure 47. Output ripple voltage for various output voltages with external 1x10 μ F, 1x47 μ F, 2x47 μ F or 4x47 μ F ceramic capacitors at the output (3A load). Input voltage is 12V.

Safety Considerations

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL 60950-1, CSA C22.2 No. 60950-1-03, and VDE 0850:2001-12 (EN60950-1) Licensed.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a fastacting fuse with a maximum rating of 5A in the positive input lead. As an option to using a fuse, no fuse is required, if the module is

- 1. powered by a power source with current limit protection set point less than the recommended protection device value, and
- 2. the module is evaluated in the end-use equipment.

Feature Descriptions

Remote On/Off

The Naos Raptor 3A power modules feature an On/Off pin with positive logic for remote On/Off operation. If the On/Off pin is not being used, leave the pin open (the module will be ON, except for the -49 option modules where leaving the pin open will cause the module to remain OFF). The On/Off signal is referenced to ground. During a Logic High on the On/Off pin, the module remains ON. During Logic-Low, the module is turned OFF.

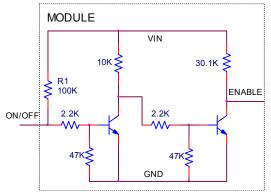


Figure 48. Remote On/Off Implementation. Resistor R1 is absent in the -49Z option module.

Overcurrent Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting continuously. At the point of current-limit inception, the unit enters hiccup mode. The unit operates normally once the output current is brought back into its specified range. The average output current during hiccup is 10% lo, max.

Overtemperature Protection

To provide protection in a fault condition, these modules are equipped with a thermal shutdown circuit. The unit will shut down if the overtemperature threshold of 130° C is exceeded at the thermal reference point T_{ref}. The thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. Once the unit goes into thermal shutdown it will then wait to cool before attempting to restart.

Input Undervoltage Lockout

At input voltages below the input undervoltage lockout limit, the module operation is disabled. The module will begin to operate at an input voltage above the undervoltage lockout turn-on threshold.

Feature Descriptions (continued)

Output Voltage Programming

The output voltage of the Naos Raptor 3A module can be programmed to any voltage from 0.59dc to 6Vdc by connecting a resistor between the Trim+ and GND pins of the module. Certain restrictions apply on the output voltage set point depending on the input voltage. These are shown in the Output Voltage vs. Input Voltage Set Point Area plot in Fig. 49. The Upper Limit curve shows that for output voltages of 0.9V and lower, the input voltage must be lower than the maximum of 14V. The Lower Limit curve shows that for output voltages of 3.8V and higher, the input voltage needs to be larger than the minimum of 4.5V.

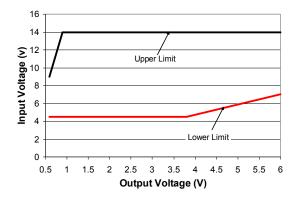


Figure 49. Output Voltage vs. Input Voltage Set Point Area plot showing limits where the output voltage can be set for different input voltages.

Without an external resistor between Trim+ and GND pins, the output of the module will be 0.59Vdc. To calculate the value of the trim resistor, *Rtrim* for a desired output voltage, use the following equation:

$$Rtrim = \frac{1.182}{(Vo - 0.591)} k\Omega$$

Rtrim is the external resistor in $k\Omega$

Vo is the desired output voltage

Table 2 provides Rtrim values required for some common output voltages.

Table 2

| V _{O, set} (V) | <i>Rtrim (</i> KΩ) |
|-------------------------|--------------------|
| 0.59 | Open |
| 1.0 | 2.89 |
| 1.2 | 1.941 |
| 1.5 | 1.3 |
| 1.8 | 0.978 |
| 2.5 | 0.619 |
| 3.3 | 0.436 |
| 5.0 | 0.268 |
| 6.0 | 0.219 |

By using a $\pm 0.5\%$ tolerance trim resistor with a TC of ± 25 ppm, a set point tolerance of $\pm 1.5\%$ can be achieved as specified in the electrical specification. The POL Programming Tool available at www.lineagepower.com under the Design Tools section, helps determine the required trim resistor needed for a specific output voltage.

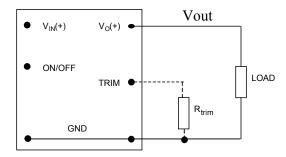


Figure 50. Circuit configuration for programming output voltage using an external resistor.

Voltage Margining

Output voltage margining can be implemented in the Naos Raptor 3A modules by connecting a resistor, R_{margin-up}, from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor, R_{margin-down}, from the Trim pin to output pin for margining-down. Figure 51 shows the circuit configuration for output voltage margining. The POL Programming Tool, available at www.lineagepower.com under the Design Tools section, also calculates the values of R_{margin-up} and R_{margin-down} for a specific output voltage and % margin. Please consult your local Lineage Power technical representative for additional details.

Feature Descriptions (continued)

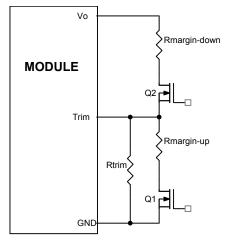


Figure 51. Circuit Configuration for margining Output voltage.

Monotonic Start-up and Shutdown

The Naos Raptor 3A modules have monotonic start-up and shutdown behavior for any combination of rated input voltage, output current and operating temperature range.

Tunable Loop[™]

The Naos Raptor 3A modules have a new feature that optimizes transient response of the module called Tunable LoopTM. External capacitors are usually added to improve output voltage transient response due to load current changes. Sensitive loads may also require additional output capacitance to reduce output ripple and noise. Adding external capacitance however affects the voltage control loop of the module, typically causing the loop to slow down with sluggish response. Larger values of external capacitance could also cause the module to become unstable.

To use the additional external capacitors in an optimal manner, the Tunable $Loop^{TM}$ feature allows the loop to be tuned externally by connecting a series R-C between the VOUT and TRIM pins of the module, as shown in Fig. 52. This R-C allows the user to externally adjust the voltage loop feedback compensation of the module to match the filter network connected to the output of the module.

Recommended values of R_{TUNE} and C_{TUNE} are given in Tables 3 and 4. Table 3 lists recommended values of R_{TUNE} and C_{TUNE} in order to meet 2% output voltage deviation limits for some common output voltages in the presence of a 1.5A to 3A step change (50% of full load), with an input voltage of 12V. Table 4 shows the recommended values of R_{TUNE} and C_{TUNE} for different values of ceramic output capacitors up to 1000uF, again for an input voltage of 12V. The value of R_{TUNE} should never be lower than the values shown in Tables 3 and 4. Please contact your Lineage Power technical representative to obtain more details of this feature as well as for guidelines on how to select the right value of external R-C to tune the module for best transient performance and stable operation for other output capacitance values.

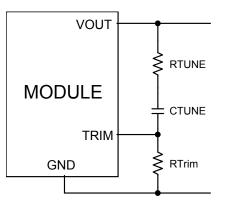


Figure. 52. Circuit diagram showing connection of R_{TUME} and C_{TUNE} to tune the control loop of the module.

Table 3. Recommended values of R_{TUNE} and C_{TUNE} to obtain transient deviation of 2% of Vout for a 1.5A step load with Vin=12V.

| Vout | 5V | 3.3V | 2.5V | 1.8V | 1.2V | 0.69V |
|-------------------|--------|--------|------|--------|--------|------------------------------|
| Cext | 47μF | 47μF | 47μF | 2x47μF | 3x47μF | 3x47μF + 330μF Polymer |
| R _{TUNE} | 150 | 150 | 100 | 75 | 47 | 47 |
| C _{TUNE} | 4700pF | 4700pF | 10nF | 22nF | 33nF | 120nF |
| ΔV | 57mV | 57mV | 44mV | 31mV | 23mV | 12mV |

Table 4. General recommended values of of R_{TUNE} and C_{TUNE} for Vin=12V and various external ceramic capacitor combinations.

| Cext | 1x47μF | 2x47μF | 4x47μF | 6x47μF | 10x47µF |
|-------------------|--------|--------|--------|--------|---------|
| R _{TUNE} | 150 | 75 | 47 | 47 | 47 |
| CTUNE | 4700pF | 22nF | 39nF | 47nF | 56nF |

Naos Raptor 3A: Non-isolated DC-DC Power Modules 4.5 – 14Vdc input; 0.59Vdc to 6Vdc Output; 3A output current

Thermal Considerations

Power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel The test set-up is shown in Figure 53. The preferred airflow direction for the module is in Figure 54.

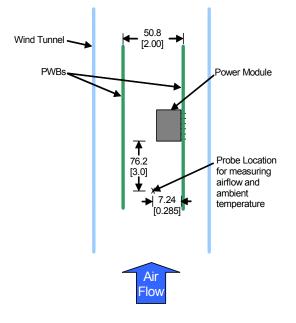


Figure 53. Thermal Test Set-up.

The thermal reference point, T_{ref} used in the specifications of thermal derating curves is shown in Figure 54. For reliable operation this temperature should not exceed 120°C.

The output power of the module should not exceed the rated power of the module (Vo,set x lo,max).

Please refer to the Application Note "Thermal Characterization Process For Open-Frame Board-Mounted Power Modules" for a detailed discussion of thermal aspects including maximum device temperatures.

Airflow Direction

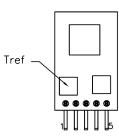


Figure 54. Tref Temperature measurement location.

Post solder Cleaning and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical board testing. The result of inadequate cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning and drying procedures, refer to *Board Mounted Power Modules: Soldering and Cleaning* Application Note.

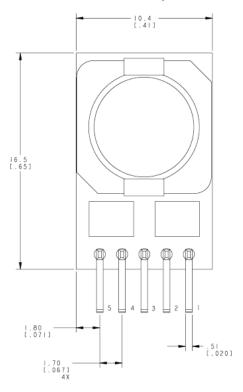
Through-Hole Lead-Free Soldering Information

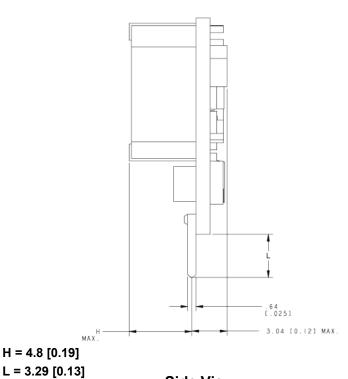
The RoHS-compliant through-hole products use the SAC (Sn/Aq/Cu) Pb-free solder and RoHS-compliant components. They are designed to be processed through single or dual wave soldering machines. The pins have an RoHS-compliant finish that is compatible with both Pb and Pb-free wave soldering processes. A maximum preheat rate of 3°C/s is suggested. The wave preheat process should be such that the temperature of the power module board is kept below 210°C. For Pb solder, the recommended pot temperature is 260°C, while the Pb-free solder pot is 270°C max. Not all RoHS-compliant through-hole products can be processed with paste-through-hole Pb or Pb-free reflow process. If additional information is needed, please consult with your Lineage Power technical representative for more details.

Mechanical Outline

Dimensions are in millimeters and (inches). Tolerances: x.x mm \pm 0.5 mm [x.xx in. \pm 0.02 in.] (unless otherwise indicated)

x.xx mm \pm 0.25 mm [x.xxx in \pm 0.010 in.]









Pin out

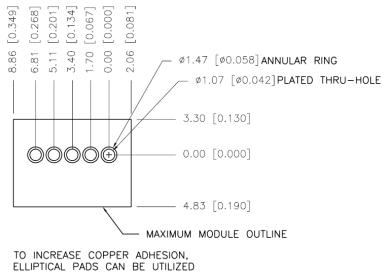
| Pin | Function |
|-----|------------------|
| 1 | On/Off |
| 2 | V _{IN} |
| 3 | GND |
| 4 | V _{out} |
| 5 | Trim+ |

Recommended Pad Layout

Dimensions are in millimeters and (inches).

Tolerances: x.x mm \pm 0.5 mm [x.xx in. \pm 0.02 in.] (unless otherwise indicated)

x.xx mm \pm 0.25 mm [x.xxx in \pm 0.010 in.]



Ordering Information

Please contact your Lineage Power Sales Representative for pricing, availability and optional features.

Table 5. Device Codes

| Device Code | Input Voltage Range | Output Voltage | Output Current | On/Off Logic | Connector Type | Comcodes |
|-----------------|------------------------|-------------------|-------------------|-----------------|-------------------|-------------|
| NSR003A0X4Z | 4.5 – 14Vdc | 0.59 – 6Vdc | ЗA | Positive | SIP | CC109130886 |
| NSR003A0X4-49Z* | 4.5 – 14Vdc | 0.59 – 6Vdc | ЗA | Positive | SIP | CC109138186 |

Z refers to RoHS-compliant product.

* Special code, consult factory before ordering



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