

# **AUIR3315(S)**

## PROGRAMMABLE CURRENT SENSE HIGH SIDE SWITCH

#### **Features**

- Load current feedback
- Programmable over current shutdown
- Active clamp
- ESD protection
- Input referenced to Vcc
- Over temperature shutdown
- Reverse battery protection
- Lead-Free

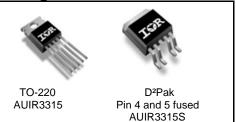
#### **Description**

The AUIR3315(S) is a fully protected 4 terminals high side switch. The input signal is referenced to Vcc. When the input voltage Vcc - Vin is higher than the specified threshold, the output power Mosfet is turned on. When the Vcc - Vin is lower than the specified Vil threshold, the output Mosfet is turned off. A current proportional to the power Mosfet current is sourced to the Ifb pin. Over current shutdown occurs when Vifb-Vin > 4.7V. The current shutdown threshold is adjusted by selecting the proper RIfb. Either over current and over temperature latches off the switch. The device is reset by pulling the input pin high. Other integrated protections (ESD, reverse battery, active clamp) make the switch very rugged in automotive environment.

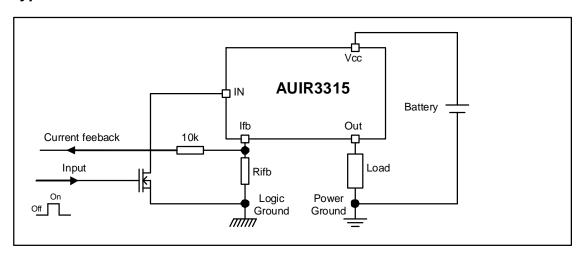
## **Product Summary**

 $\begin{array}{lll} \text{Rds(on)} & 20 \text{ m}\Omega \text{ max.} \\ \text{Vcc op.} & 6 \text{ to } 32\text{V} \\ \text{Current Ratio} & 2800 \\ \text{Prog. Ishutdown} & 3 \text{ to } 30\text{A} \\ \text{Vclamp} & 40\text{V} \end{array}$ 

### **Packages**



## **Typical Connection**





## Qualification Information<sup>†</sup>

		Automot	tin ra			
Qualification Level		Automotive (per AEC-Q100 <sup>††</sup> )				
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.				
Moisture Sensitivity Level		D2PAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)			
		TO220-5L	Not applicable			
	Machine Model	Class M4 (450V) (per AEC-Q100-003)				
ESD	Human Body Model	Class H3A (4 (per AEC-Q1				
	Charged Device Model	Class C4 (1 (per AEC-Q1	,			
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)				
RoHS Compliant		Yes				

<sup>†</sup> Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>

<sup>††</sup> Exceptions to AEC-Q100 requirements are noted in the qualification report.



## **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Vcc lead. (Ti=-40°..150°C. Vcc=6..26V Tambient=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vcc-Vin	Maximum Vcc voltage	-16	37	
Vcc-Vin cont.	Maximum continuous Vcc voltage	-16	32	V
Vcc-Vfb	Maximum Ifb voltage	-16	33	V
Vcc-Vout	Maximum output voltage	-0.3	37	
lds cont.	Maximum body diode continuous current Rth=60°C/W (1)	_	2.8	Α
lds pulsed	Maximum body diode pulsed current (1)	_	100	^
Pd	Maximum power dissipation Rth=60°C/W	_	2	W
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C
Min Rfb	Minimum on the resistor on Ifb pin	0.3	_	kΩ
Ifb max.	Max. Ifb current	-50	50	mA

<sup>(1)</sup> Limited by junction temperature. Pulsed is also limited by wiring

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient D2-Pak Std footprint	60	_	
Rth2	Thermal resistance junction to case D²-Pak	1.6	_	°C/W
Rth3	Thermal resistance junction to case TO-220	1.6	_	

### **Recommended Operating Conditions**

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
lout	Continuous output current			
	Tambient=85°C, Rth=5°C/W, Tj=125°C	_	14	Α
	Tambient=85°C, Rth=60°C/W, Tj=125°C	-	3.9	
Rifb	Recommended Ifb resistor (2)(3)	0.5	3.5	kΩ
Pulse min.	Minimum turn-on pulse width	1	_	ms
Fmax.	Maximum operating frequency	_	200	Hz

<sup>(2)</sup> If Rifb is too low, the device can be damaged.

<sup>(3)</sup> If Rifb is too high, the device may not switch on.



### **Protection Characteristics**

Tj=-40°..150°C, Vcc=6..26V, Rifb=500 to 5kΩ

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Vifb-Vin@Isd	Over-current shutdown threshold	3.8	4.7	5.9	V	
Tsd	Over temperature threshold	_	165	_	°C	See fig. 5
OV	Over voltage protection (not latched)	33	35	39	V	
Isdf	Fixed over current shutdown	30	38	50	Α	Vifb <vifb-vin@isd< td=""></vifb-vin@isd<>
lsd_1k	Programmable over current shutdown 1k	9	12	17	Α	Rifb=1kΩ
treset	Time to reset protection	_	50	500	0	See fig. 5
Min. pulse	Min. pulse width (no WAIT state)	150	400	1200	μs	Tj=25°C
WAIT	WAIT function timer	0.4	1	2	ms	See fig. 4 and 5
Rds(on) rev.	Reverse battery On state resistance	_	16	28		Vcc-Vin=-14V,
	Tj=25°C				mΩ	lout=10A
	Tj=125°C	_	24	42		

#### **Static Electrical Characteristics**

Ti=25°C. Vcc=14\/ (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Vcc op.	Operating Voltage range	6	_	32	V	
Icc off	Supply leakage current	_	1.5	5	μA	Vin=Vcc, Vcc-Vout=14V, Vcc-Vifb=14V, Tj=25°C
lin, on	On state IN positive current	1.5	3	6	mA	Vcc-Vin=14V, Tj=25°C
Vih	High level Input threshold voltage (4)	_	5.4	6.3		
Vil	Low level Input threshold voltage (4)	4	4.9	5.8	V	
Vhyst	Input hysteresis Vih-Vil	0.2	0.4	1.5		
lout	Drain to source leakage current	_	1.2	5	μA	Vin=Vcc, Vcc-Vifb=0V, Vcc-Vout=14V, Tj=25°C
Rds(on)	On state resistance (5) Tj=25°C	10	15	20		Iout=10A, Vcc-Vin=14V
	On state resistance (5) Tj=25°C	10	16	28	mΩ	Iout=7A, Vcc-Vin=6V
	On state resistance (5) Tj=150°C	20	28.5	38		Iout=10A, Vcc-Vin=14V
V clamp1	Vcc to Vout clamp voltage 1	36	39	_	\/	lout=50mA
V clamp2	Vcc to Vout clamp voltage 2	_	40	43	) <sup>v</sup>	lout=10A, Tj=25°C

<sup>(4)</sup> Input thresholds are measured directly between the input pin and the tab. Any parasitic resistance in common between the load current path and the input signal path can significantly affect the thresholds.
(5) Rdson is measured between the tab and the Out pin, 5mm away from the package.

## **Switching Electrical Characteristics**

Vcc=14V. Resistive load=4Ω. Ti=25°C

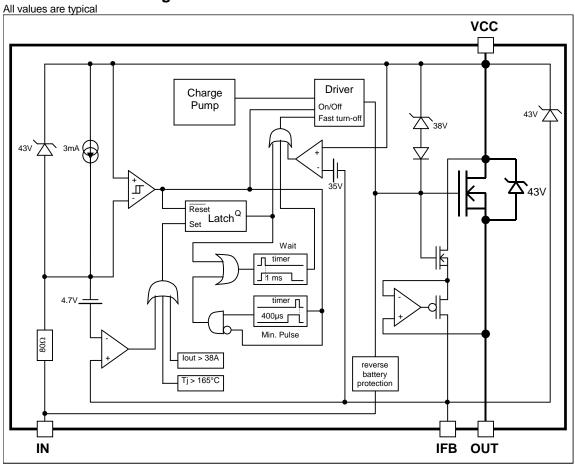
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
tdon	Turn on delay time to 10% Vcc	3	11	27		
tr1	Rise time to Vcc-Vout=5V	1	4	10	μs	
tr2	Rise time to Vcc-Vout=0.1Vcc	2	8	20		
Eon	Turn on energy	_	0.2	_	mJ	See figure 2
tdoff	Turn off delay time	10	40	100	110	
tf	Fall time to Vout=10% of Vcc	2	8	20	μs	
Eoff	Turn off energy		0.1	_	mJ	

### **Current Sense Characteristics**

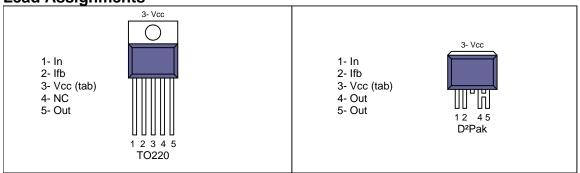
Tj=-40°..150°C, Vcc=6..26V (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ratio	I Load/lifb current ratio	2,400	2,800	3,200		Rfb=500Ω, lout=20A
Ratio_TC	I Load/lifb variation aver temperature	-5	_	+5	%	Tj=-40°C to 150°C
Offset	Load current diagnostic offset	-0.08	0	+0.09	Α	lout=1A
trst	Ifb response time (low signal)	_	1	_	μs	90% of the lout step

# **Functional Block Diagram**



**Lead Assignments** 



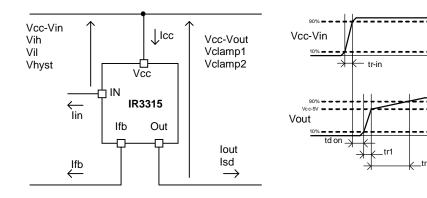
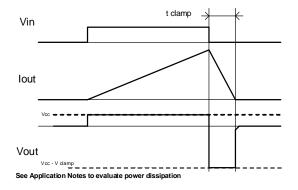


Figure 1 - Voltages and current definitions

Figure 2 - Switching time definitions





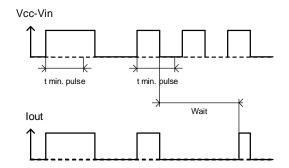


Figure 3 – Active clamp waveforms

Figure 4 - Min. pulse and Wait function

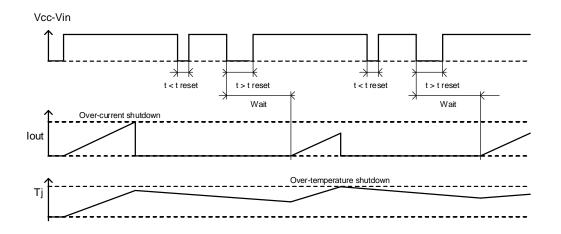


Figure 5 – Protection Timing Diagrams



All curves are typical characteristics. Operation in hatched areas is not recommended. Tj=25°C, Rifb=500ohm, Vcc=14V (unless otherwise specified).

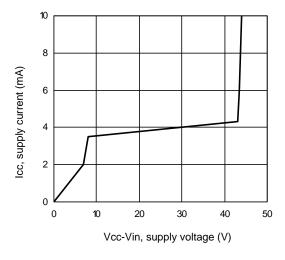
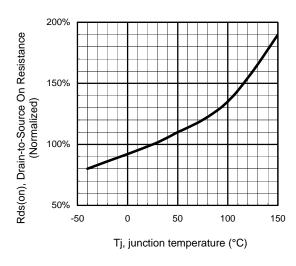


Figure 6 - Icc (mA) Vs Vcc-Vin (V)

Figure 7 – Icc off (µA) Vs Tj (°C)



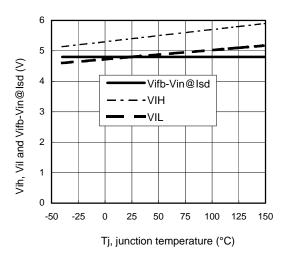


Figure 8 - Normalized Rds(on) (%) Vs Tj (°C)

Figure 9 - Vih, Vil and Vifb-Vin@Isd (V) Vs Tj (°C)

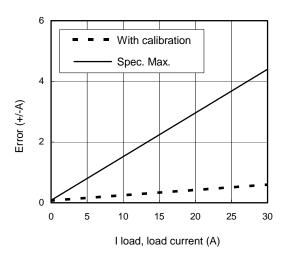


Figure 10 - Error (+/- A) Vs I load (A)

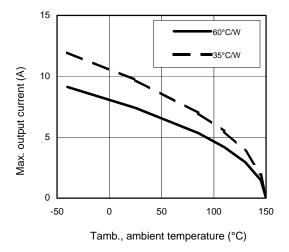


Figure 12 - Max. lout (A) Vs Tamb. (°C)

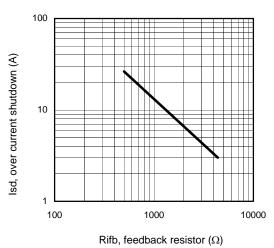


Figure 11 – Ids (A) Vs Rifb ( $\Omega$ )

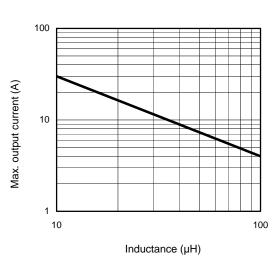
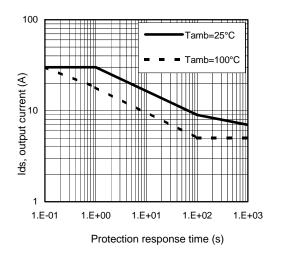


Figure 13 - Max. lout (A) Vs inductance (µH)



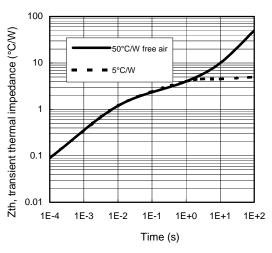
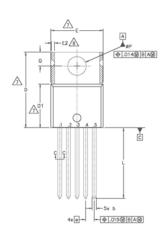
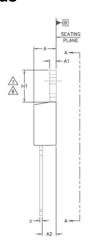


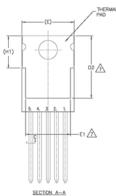
Figure 14 – Ids (A) Vs over temperature protection response time (s)

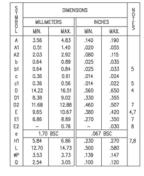
Figure 15 – Transient thermal impedance (°C/W) Vs time (s)

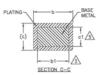
### Case Outline - TO220 5 leads









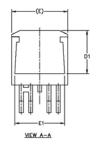


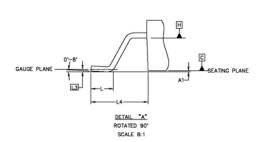
#### NOTES:

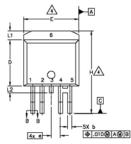
- DISCISSIONAL AND TOLERANCING AS PER ASSIE Y14.5 M- 1994.
  DISCISSIONS ARE SHOWN IN INCHES [MILLIMETERS].
  LEAD DIMINISTORA PRINSH UNCONFORCED IN 11.
  DISCISSION 0, DI & E DO NOT INCLIDE MOLD FLASH MOLD FLASH
  SHALL HOT DESCED, 00% (01.27) PER DISC. THESE DIMINISTORS ARE
  MAUSISIED AT THE OUTERISTS EXTREMES OF THE PLASTIC BODY.
  DIMINISTOR OF ALE APPLY TO BESE METAL, ONLY.
  CONTROLLING DIMINISTORY IN ONDES.
  THESMAL PAR CONTROL OF THOMAL WITHIN DIMINISTORS E-HI, D.2 & E-I
  DIMINISTORY E-Y A ZONE HITCHES STAPPING
  OUTLINE CONVENIENT OF THE TOLERAN PAR CONTROL OF THE PLASTIC BODY.
  MHERE DIMINISTORY TO JETE TO A ZONE HITCHES STAPPING
  OUTLINE CONVENIENT OF JETE TO A ZONE HITCHES (mex.) AND D.2 (min.)
  MHERE DIMINISTORY OF JETE TO A ZONE HITCHES AND THE ACTUAL PACKAGE CUITINE.

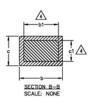
10.- LEADS AND DRAIN ARE PLATED WITH 100% Sn

### Case Outline - D2PAK - 5 Leads



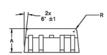


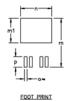




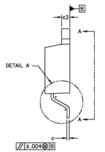
M					
B	MILLIM	ETERS	INC	HES	Ţ
Ľ	MIN.	MAX.	MIN.	MAX.	Ė
Α	4.06	4.83	.160	.190	
A1		0.254		.010	
ь	0.66	0.91	.026	.036	4
ь1	0.66	0.81	.026	.032	
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	4
c2	1.14	1.65	.045	.065	
D	8.51	9.65	.335	.380	3
D1	6.86		.270		
Ε	9.65	10.67	.380	.420	3
E1	6.22		.245		
е	1.70	BSC	.067	BSC	
н	14.73	15.49	.580	.609	
L	1.14	1.39	.045	.055	
L1		1.65		.065	
L2	1.27	1.78	.050	.070	
L3	0.25	BSC	.010	BSC	
L4	4.78	5.28	.188	.208	
m	17.78		.700		
m1	8.89		.350		
n	11.43		.450		
٥	1.93		.076		
р	3.81		.150		
R	0.51	0.71	.020	.028	

DIMENSIONS





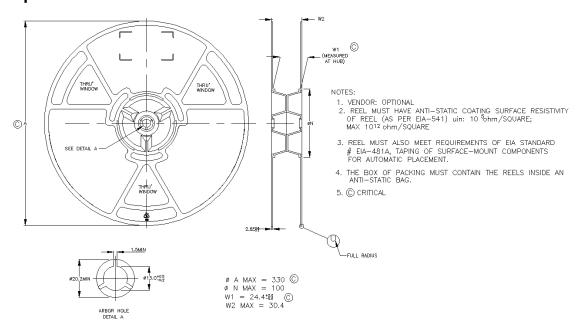
SCALE	2:1		



#### NOTES:

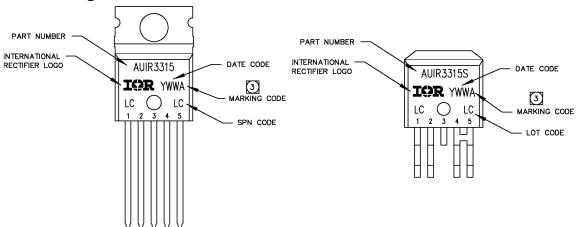
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- 4. DIMENSION 61 AND 61 APPLY TO BASE METAL ONLY.
- 5. CONTROLLING DIMENSION: MILLIMETERS
- 6. LEADS AND DRAIN ARE PLTED WITH 100% Sn

# Tape & Reel - D2PAK - 5 leads





# **Part Marking Information**



# **Ordering Information**

Base Part Number	Davidson Tons	Standard Pack	Occupated a Board Normalism	
base i art ivamber	Package Type	Form	Quantity	Complete Part Number
	TO220 – 5Leads	Tube	50	AUIR3315
AUIR3315	D2-Pak-5-Leads	Tube	50	AUIR3315S
AURSSIS		Tape and reel left	800	AUIR3315STRL
		Tape and reel right	800	AUIR3315STRR



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**Revision History** 

Revision	Date	Notes/Changes	
Α	01/09/2006	First release	
В	22/01/2007	Pin asignment	
С	14/01/2010	AU release	
D	14/11/2010	Change description	

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**Authorized Distributor** 

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