# Single Supply Dual Operational Amplifiers

Utilizing the circuit designs perfected for Quad Operational Amplifiers, these dual operational amplifiers feature low power drain, a common mode input voltage range extending to ground/ $V_{\rm EE}$ , and single supply or split supply operation. The LM358S and LM2904S are half of the LM324S and LM2902S, respectively.

These amplifiers have several distinct advantages over standard operational amplifier types in single supply applications. The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications. The output voltage range also includes the negative power supply voltage.

#### **Features**

- Short Circuit Protected Outputs
- True Differential Input Stage
- Single Supply Operation: 3.0 V to 32 V
- Low Input Bias Currents
- Internally Compensated
- Common Mode Range Extends to Negative Supply
- Single and Split Supply Operation
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

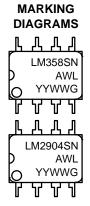


#### ON Semiconductor®

http://onsemi.com



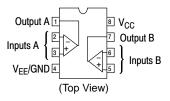
PDIP-8 N SUFFIX CASE 626



LMxxxx = Specific Device Code A, AL = Assembly Location

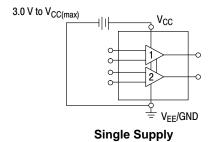
WL = Wafer Lot
Y, YY = Year
W, WW = Work Week
G or = = Pb-Free Package

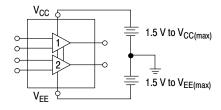
#### PIN CONNECTIONS



#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 8 of this data sheet.





Split Supplies

Figure 1.

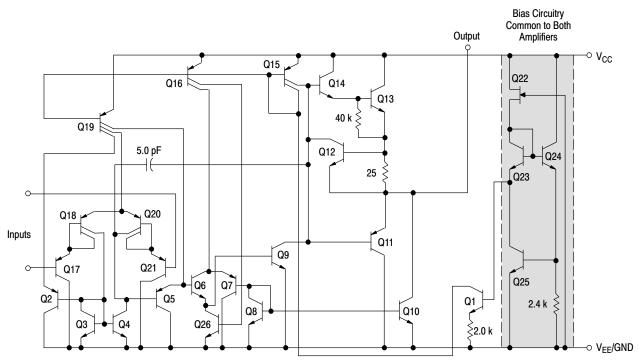


Figure 2. Representative Schematic Diagram (One-Half of Circuit Shown)

# **MAXIMUM RATINGS** ( $T_A = +25^{\circ}C$ , unless otherwise noted.)

Rating		Symbol	Value	Unit
Power Supply Voltages	Single Supply Split Supplies	V <sub>CC</sub> V <sub>CC</sub> , V <sub>EE</sub>	32 ±16	Vdc
Input Differential Voltage Range (Note 1)		V <sub>IDR</sub>	±32	Vdc
Input Common Mode Voltage Range (Note 2)		V <sub>ICR</sub>	-0.3 to 32	Vdc
Output Short Circuit Duration		t <sub>SC</sub>	Continuous	
Junction Temperature		TJ	150	°C
Thermal Resistance, Junction-to-Air (Note 3)	Case 626	$R_{ hetaJA}$	161	°C/W
Storage Temperature Range		T <sub>stg</sub>	-65 to +150	°C
Operating Ambient Temperature Range	LM358S LM2904S	T <sub>A</sub>	0 to +70 -40 to +105	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- For supply voltages less than 32 V the absolute maximum input voltage is equal to the supply voltage.
   All R<sub>θJA</sub> measurements made on evaluation board with 1 oz. copper traces of minimum pad size. All device outputs were active.

# **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 5.0 \text{ V}$ , $V_{EE} = GND$ , $T_A = 25^{\circ}C$ , unless otherwise noted.)

Imput Offset Voltage		LM35		LM358S	;	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Characteristic	Symbol	Min	Тур	Max	Unit
T <sub>A</sub> = 25°C		V <sub>IO</sub>				mV
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$						
T <sub>A</sub> = T <sub>Low</sub> (Note 4)   Average Temperature Coefficient of Input Offset Voltage   T <sub>A</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>A</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>high</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> = T <sub>B</sub> = T <sub>B</sub> = T <sub>High</sub> to T <sub>Low</sub> (Note 4)   T <sub>B</sub> =	**		_	2.0	7.0	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	· · · · · · · · · · · · · · · · · · ·		_	_	9.0	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			_	_	9.0	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Average Temperature Coefficient of Input Offset Voltage	$\Delta V_{IO}/\Delta T$	_	7.0	-	μV/°C
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$T_A = T_{high}$ to $T_{low}$ (Note 4)					
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Input Offset Current	I <sub>IO</sub>	_	5.0	50	nA
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$T_A = T_{high}$ to $T_{low}$ (Note 4)		_	_	150	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Input Bias Current	I <sub>IB</sub>	_	-45	-250	nA
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$T_A = T_{high}$ to $T_{low}$ (Note 4)		_	-50	-500	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		$\Delta I_{IO}/\Delta T$	-	10	-	pA/°C
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Input Common Mode Voltage Range (Note 5), V <sub>CC</sub> = 30 V	$V_{ICR}$	0	-	28.3	V
	$V_{CC} = 30 \text{ V}, T_A = T_{high} \text{ to } T_{low}$		0	_	28	
	Differential Input Voltage Range	V <sub>IDR</sub>	_	_	V <sub>CC</sub>	V
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Large Signal Open Loop Voltage Gain					V/mV
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$R_L = 2.0 \text{ k}\Omega$ , $V_{CC} = 15 \text{ V}$ , For Large $V_O$ Swing,		25	100	_	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$T_A = T_{high}$ to $T_{low}$ (Note 4)		15	_	_	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	<u> </u>	CS	_	-120	_	dB
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$						
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		CMR	65	70	-	dB
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Power Supply Rejection	PSR	65	100	_	dB
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	Output Voltage–High Limit	V <sub>OH</sub>				V
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		0	3.3	3.5	_	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			26	_	_	
	3 .			28	_	
Output Source Current $I_O +$ <t< td=""><td>Output Voltage-Low Limit</td><td>V<sub>OL</sub></td><td></td><td></td><td>20</td><td>mV</td></t<>	Output Voltage-Low Limit	V <sub>OL</sub>			20	mV
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		lo+				mA
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	i i	1.0	20	45	_	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		lo-		.0		
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		.0	10	30	_	mΑ
Output Short Circuit to Ground (Note 6) $I_{SC} - 45 = 60  \text{mA}$ Power Supply Current (Total Device) $I_{CC} = 30 \text{ V}, \text{ V}_{O} = 0 \text{ V}, \text{ R}_{L} = \infty$ $I_{CC} = 3.0 \text{ V}, \text{ V}_{O} = 0 \text{ V}, \text{ R}_{L} = \infty$ $I_{CC} = 3.0 \text{ V}$					_	
Power Supply Current (Total Device) $I_{CC}$ $I$		loo			60	
$T_A = T_{high} \text{ to } T_{low} \text{ (Note 4)}$ $V_{CC} = 30 \text{ V, } V_O = 0 \text{ V, } R_L = \infty$ - 0.5 3.0	, ,		<del>-</del>	70	- 50	
	$T_A = T_{high}$ to $T_{low}$ (Note 4)	'CC				IIIA
$V_{CC} = 5 \text{ V}, V_{O} = 0 \text{ V}, R_{L} = \infty$ - 0.3 1.2	_		-	0.5	3.0	
	$V_{CC} = 5 \text{ V}, V_O = 0 \text{ V}, R_L = \infty$		_	0.3	1.2	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. LM358S: T<sub>low</sub> = 0°C, T<sub>high</sub> = +70°C
LM2904S: T<sub>low</sub> = -40°C, T<sub>high</sub> = +105°C

5. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is V<sub>CC</sub> - 1.7 V.

6. Short circuits from the output to V<sub>CC</sub> can cause excessive heating and eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers

simultaneous shorts on all amplifiers.

# **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 5.0 \text{ V}$ , $V_{EE} = GND$ , $T_A = 25^{\circ}C$ , unless otherwise noted.)

			LM29045	3	
Characteristic	Symbol	Min	Тур	Max	Unit
Input Offset Voltage $V_{CC}$ = 5.0 V to 30 V, $V_{IC}$ = 0 V to $V_{CC}$ –1.7 V, $V_{O}$ $\simeq$ 1.4 V, $R_{S}$ = 0 $\Omega$	V <sub>IO</sub>				mV
$T_A = 25^{\circ}C$		_	2.0	7.0	
$T_A = T_{high}$ (Note 7)		-	-	10	
$T_A = T_{low}$ (Note 7)		_	_	10	
Average Temperature Coefficient of Input Offset Voltage $T_A = T_{high}$ to $T_{low}$ (Note 7)	$\Delta V_{IO}/\Delta T$	-	7.0	_	μV/°C
Input Offset Current	I <sub>IO</sub>	-	5.0	50	nA
$T_A = T_{high}$ to $T_{low}$ (Note 7)		_	45	200	
Input Bias Current	I <sub>IB</sub>	-	-45	-250	nA
$T_A = T_{high}$ to $T_{low}$ (Note 7)		_	-50	-500	
Average Temperature Coefficient of Input Offset Current $T_A = T_{high}$ to $T_{low}$ (Note 7)	$\Delta I_{IO}/\Delta T$	-	10	-	pA/°C
Input Common Mode Voltage Range (Note 8), $V_{CC} = 30 \text{ V}$	V <sub>ICR</sub>	0	_	28.3	V
$V_{CC} = 30 \text{ V}, T_A = T_{high} \text{ to } T_{low}$		0	_	28	
Differential Input Voltage Range	$V_{IDR}$	_	_	V <sub>CC</sub>	V
Large Signal Open Loop Voltage Gain	A <sub>VOL</sub>				V/mV
$R_L = 2.0 \text{ k}\Omega$ , $V_{CC} = 15 \text{ V}$ , For Large $V_O$ Swing,		25	100	_	
$T_A = T_{high}$ to $T_{low}$ (Note 7)		15	_	_	
Channel Separation 1.0 kHz $\leq$ f $\leq$ 20 kHz, Input Referenced	CS	-	-120	-	dB
Common Mode Rejection $R_S \leq 10 \; k\Omega$	CMR	50	70	-	dB
Power Supply Rejection	PSR	50	100	-	dB
Output Voltage-High Limit	V <sub>OH</sub>				V
$V_{CC}$ = 5.0 V, $R_L$ = 2.0 k $\Omega$ , $T_A$ = 25°C		3.3	3.5	-	
$V_{CC}$ = 30 V, $R_L$ = 2.0 k $\Omega$ , $T_A$ = $T_{high}$ to $T_{low}$ (Note 7)		26	_	-	
$V_{CC}$ = 30 V, $R_L$ = 10 k $\Omega$ , $T_A$ = $T_{high}$ to $T_{low}$ (Note 7)		27	28	-	
Output Voltage–Low Limit $V_{CC}$ = 5.0 V, $R_L$ = 10 k $\Omega$ , $T_A$ = $T_{high}$ to $T_{low}$ (Note 7)	V <sub>OL</sub>	-	5.0	20	mV
Output Source Current V <sub>ID</sub> = +1.0 V, V <sub>CC</sub> = 15 V	I <sub>O+</sub>	20	45	_	mA
Output Sink Current	I <sub>O</sub> –				
$V_{ID} = -1.0 \text{ V}, V_{CC} = 15 \text{ V}$		10	30	_	mA
$V_{ID} = -1.0 \text{ V}, V_O = 200 \text{ mV}$		_	_	_	μΑ
Output Short Circuit to Ground (Note 9)	I <sub>SC</sub>	-	45	60	mA
Power Supply Current (Total Device) $T_A = T_{high} \text{ to } T_{low} \text{ (Note 7)}$	I <sub>CC</sub>				mA
$V_{CC}$ = 30 V, $V_{O}$ = 0 V, $R_{L}$ = $\infty$		_	0.5	3.0	
$V_{CC} = 5 \text{ V}, V_O = 0 \text{ V}, R_L = \infty$		_	0.3	1.2	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product

performance and not be indicated by the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7. LM358S: T<sub>low</sub> = 0°C, T<sub>high</sub> = +70°C
LM2904S: T<sub>low</sub> = -40°C, T<sub>high</sub> = +105°C

8. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is V<sub>CC</sub> - 1.7 V.

9. Short circuits from the output to V<sub>CC</sub> can cause excessive heating and eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifices.

simultaneous shorts on all amplifiers.

#### **CIRCUIT DESCRIPTION**

The LM358S and LM2904S are made using two internally compensated, two-stage operational amplifiers. The first stage of each consists of differential input devices Q20 and Q18 with input buffer transistors Q21 and Q17 and the differential to single ended converter Q3 and Q4. The first stage performs not only the first stage gain function but also performs the level shifting and transconductance reduction functions. By reducing the transconductance, a smaller compensation capacitor (only 5.0 pF) can be employed, thus saving chip area. The transconductance reduction is accomplished by splitting the collectors of Q20

and Q18. Another feature of this input stage is that the input common mode range can include the negative supply or ground, in single supply operation, without saturating either the input devices or the differential to single–ended converter. The second stage consists of a standard current source load amplifier stage.

Each amplifier is biased from an internal-voltage regulator which has a low temperature coefficient thus giving each amplifier good temperature characteristics as well as excellent power supply rejection.

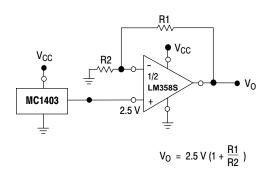


Figure 3. Voltage Reference

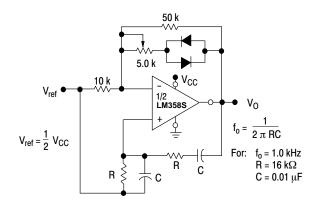


Figure 4. Wien Bridge Oscillator

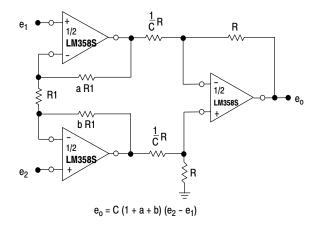


Figure 5. High Impedance Differential Amplifier

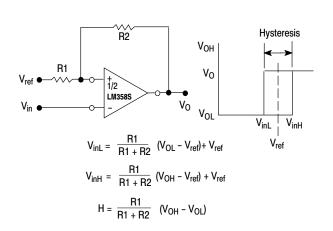


Figure 6. Comparator with Hysteresis

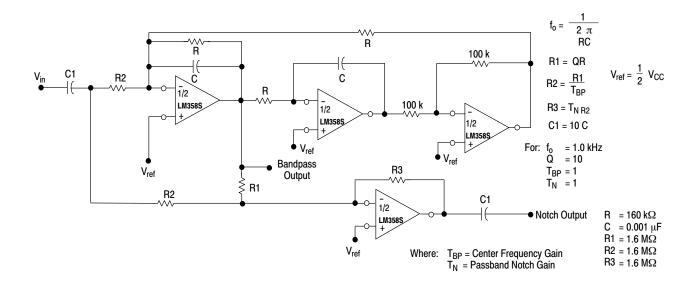
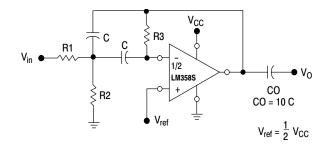


Figure 7. Bi-Quad Filter



Given:  $f_0$  = center frequency  $A(f_0)$  = gain at center frequency

Choose value fo, C

Then: R3 = 
$$\frac{Q}{\pi f_0 C}$$
  
R1 =  $\frac{R3}{2 A(f_0)}$   
R2 =  $\frac{R1 R3}{4Q^2 R1 - R3}$ 

For less than 10% error from operational amplifier.  $\frac{Q_0 \, f_0}{BW} < 0.1$ 

Where fo and BW are expressed in Hz.

If source impedance varies, filter may be preceded with voltage follower buffer to stabilize filter parameters.

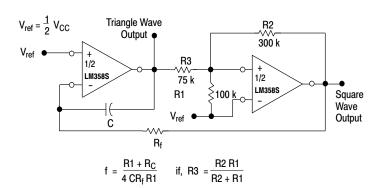


Figure 8. Function Generator

Figure 9. Multiple Feedback Bandpass Filter

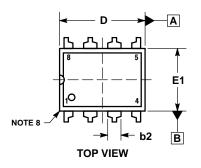
#### **ORDERING INFORMATION**

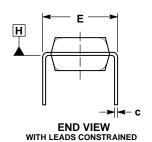
Device	Operating Temperature Range	Package	Shipping <sup>†</sup>
LM358SNG	0°C to +70°C	PDIP-8 (Pb-Free)	50 Units / Rail
LM2904SNG	−40°C to +105°C	PDIP-8 (Pb-Free)	50 Units / Rail

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

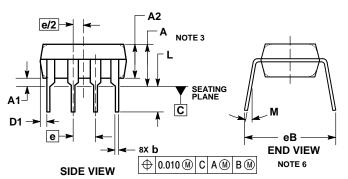
#### PACKAGE DIMENSIONS

#### PDIP-8 CASE 626-05 ISSUE N





NOTE 5



- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: INCHES.
  DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACK-
- AGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
  DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH
- OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
- DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
  DIMENSION E3 IS MEASURED AT THE LEAD TIPS WITH THE
- DIMENSION ES IS MEASURED AT THE LEAD THE WITH THE LEAD UNCONSTRAINED. DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CONTOUR SOUTH

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α		0.210		5.33
A1	0.015		0.38	-
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060	0.060 TYP		TYP
C	0.008	0.014	0.20	0.36
D	0.355	0.400	9.02	10.16
D1	0.005		0.13	
Е	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
е	0.100	BSC	2.54	BSC
eВ		0.430		10.92
L	0.115	0.150	2.92	3.81
М		10°		10°

ON Semiconductor and iii) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: LM2904SNG LM358SNG