

Vishay Siliconix

Dual P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 8	0.021 at V _{GS} = - 4.5 V	- 8.0		
	0.027 at V _{GS} = - 2.5 V	- 7.0		
	0.040 at V _{GS} = - 1.8 V	- 5.8		

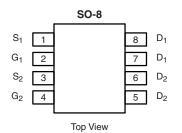
FEATURES

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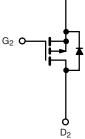
- Halogen-free According to IEC 61249-2-21
 Definition
 - TrenchFET[®] Power MOSFETs: 1.8 V Rated
- Compliant to RoHS Directive 2002/95/EC



FREE Available



G1 OF G2 D1



 S_2

Q

Ordering Information: Si4965DY-T1-E3 (Lead (Pb)-free) Si4965DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise n	oted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 8	V	
Gate-Source Voltage		V _{GS}	± 8		
	T _A = 25 °C	- I _D	- 8.0		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^{a, b}$	T _A = 70 °C		- 6.4	^	
Pulsed Drain Current		I _{DM}	- 30	A	
Continuous Source Current (Diode Conduction) ^{a, b}		ا _S	- 1.7		
Maximum Power Dissipation ^{a, b}	T _A = 25 °C	Pn	2.0	W	
	T _A = 70 °C	ГD	1.3	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA}		62.5	°C/W
Maximum Junction-to-Ambient ^a	Steady State		93		0/1

Notes:

a. Surface Mounted on FR4 board.

b. $t \leq 10$ s.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -8 V, V_{GS} = 0 V$	V, V _{GS} = 0 V		- 1	μΑ	
		V_{DS} = - 8 V, V_{GS} = 0 V, T_{J} = 70 °C		- 5			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge$ - 5 V, V_{GS} = - 4.5 V	- 20			А	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -8.0 \text{ A}$		0.0175	0.021	Ω	
		V_{GS} = - 2.5 V, I_D = - 7.0 A		0.022	0.027		
		V_{GS} = - 1.8 V, I_D = - 5.8 A		0.031	0.040		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -5 V, I_{D} = -8.0 A$		27		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = - 1.7 A, $V_{\rm GS}$ = 0 V			- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			36	55	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -4 V, V_{GS} = -4.5 V, I_{D} = -8.0 A$		7.5			
Gate-Drain Charge	Q _{gd}			5.0			
Turn-On Delay Time	t _{d(on)}			35	70		
Rise Time	t _r	V_{DD} = - 4 V, R_L = 4 Ω		45	90	ns	
Turn-Off Delay Time	t _{d(off)}	$\rm I_D\cong$ - 1 A, $\rm V_{GEN}$ = - 4.5 V, $\rm R_g$ = 6 Ω		170	340		
Fall Time	t _f			90	180		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		60	90		

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

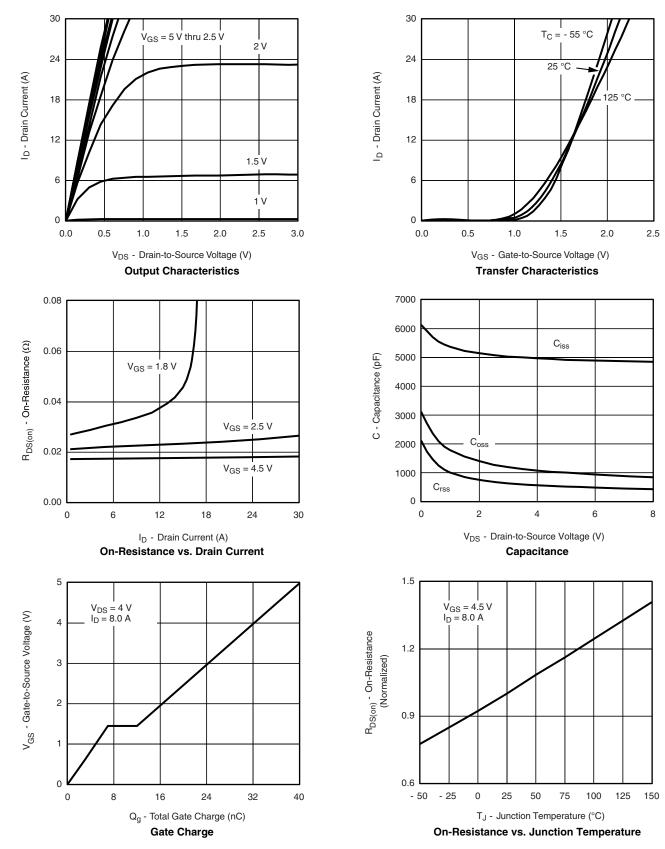
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si4965DY Vishay Siliconix





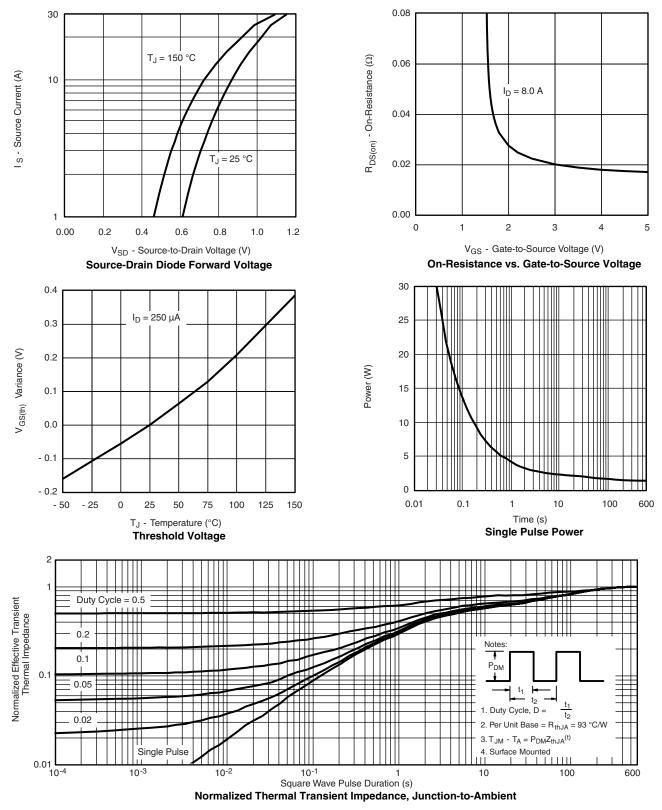
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Si4965DY

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70826.



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