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## FDB029N06 N-Channel PowerTrench<sup>®</sup> MOSFET 60 V, 193 A, 3.1 m $\Omega$

## Features

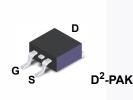
- $R_{DS(on)}$  = 2.4 m $\Omega$  (Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 75 A
- · Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low  $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

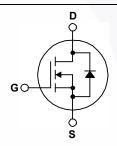
## Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench<sup>®</sup> process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

## Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Renewable System





### MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		Parameter	FDB029N06	Unit
V <sub>DSS</sub>	Drain to Source Voltage		60	V
V <sub>GSS</sub>	Gate to Source Voltage		±20	V
I <sub>D</sub>		- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C, Silicon Limited)	193	A
	Drain Current	- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C, Silicon Limited)	136	
		- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C, Package Limited)	120	
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	772	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2		1434	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		6	V/ns
P <sub>D</sub>	Power Dissipation	(T <sub>C</sub> = 25 <sup>o</sup> C)	231	W
		- Derate Above 25°C	1.54	W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

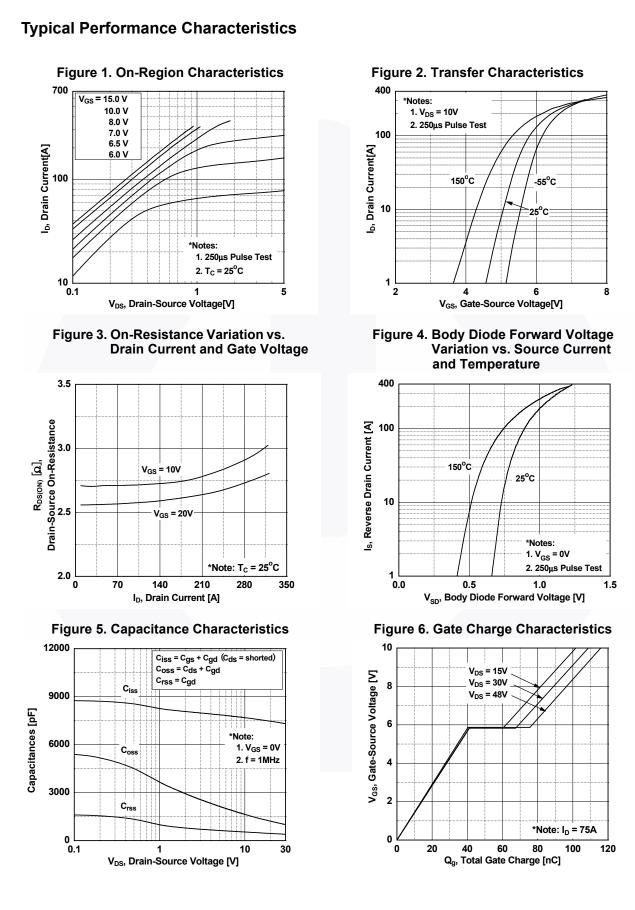
## **Thermal Characteristics**

Symbol	Parameter	FDB029N06	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.65	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
	Thermal Resistance, Junction to Ambient (1 in <sup>2</sup> Pad of 2-oz Copper), Max.	40	

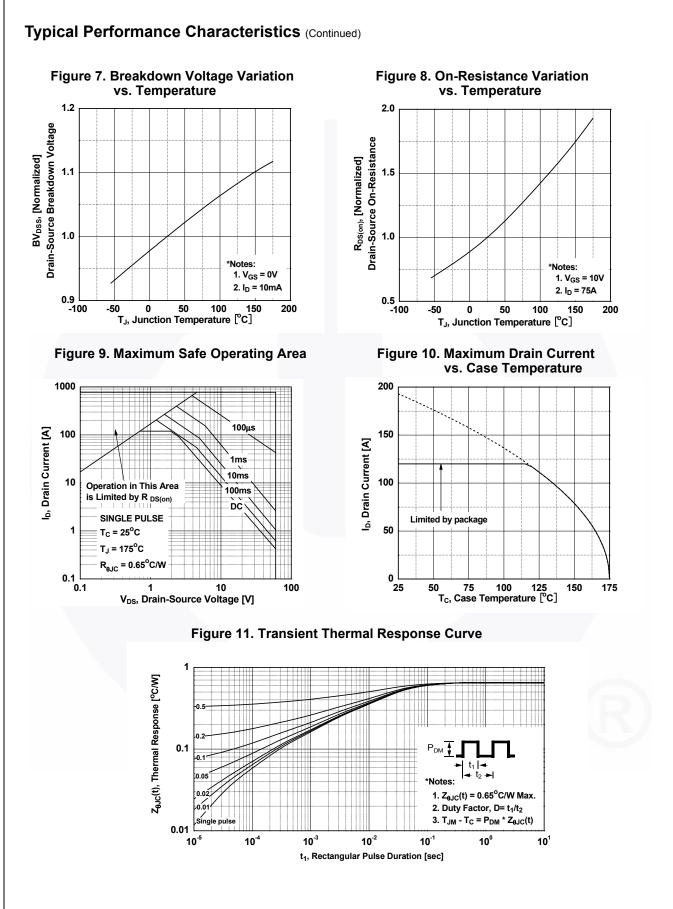
November 2013

FDB029N06	
N-Channel Pov	
werTrench <sup>®</sup>	
MOSFET	

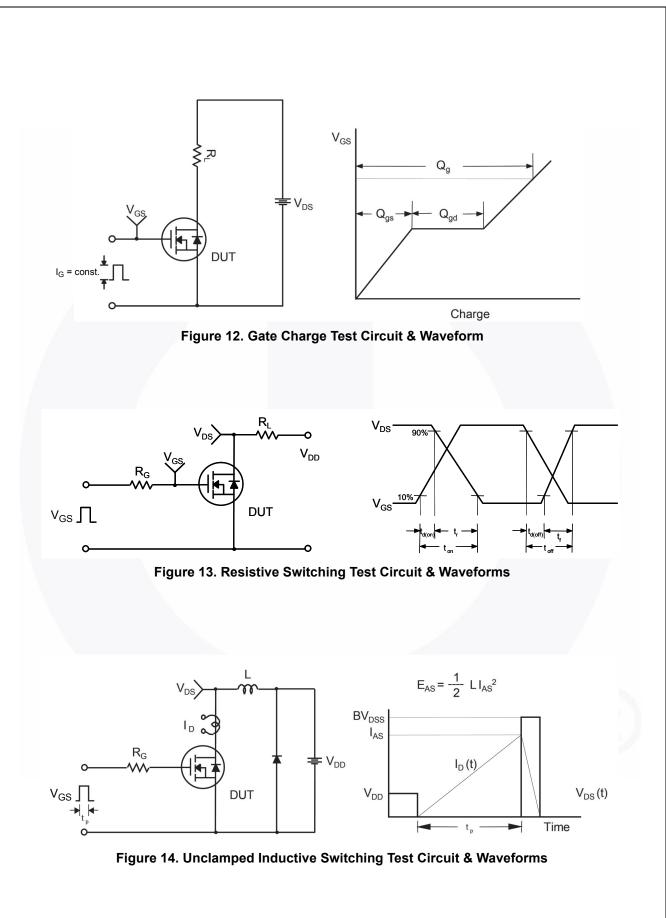
Typ. Max.   - -   0.05 -   - 1   - 500   - ±100   3.5 4.5	- V - V/ <sup>0</sup> <u>1</u> μ <sup>λ</sup> 500 π <sup>λ</sup>
 0.05 - - 1 - 500 - ±100 3.5 4.5	- V - V/ <sup>0</sup> <u>1</u> μ <sup>λ</sup> 500 π <sup>λ</sup>
 0.05 - - 1 - 500 - ±100 3.5 4.5	- V - V/ <sup>0</sup> <u>1</u> μ <sup>λ</sup> 500 π <sup>λ</sup>
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
- 1 - 500 - ±100 3.5 4.5	1 500 μ <sup>μ</sup> ±100 n <sup>μ</sup>
- 500 - ±100 3.5 4.5	500 μ/ ±100 n/
- ±100 3.5 4.5	±100 n/
3.5 4.5	
	45 V
	45 V
	3.1 m
154 -	-
	9815 pl
	1455 pl
415 625	
116 151 40 -	
35 -	
39 87	87 ns
178 366	366 ns
54 118	
33 76	76 ns
- 193	193 A
- 772	
- 1.3	
46 -	- ns
50 -	- n
	- - - 46







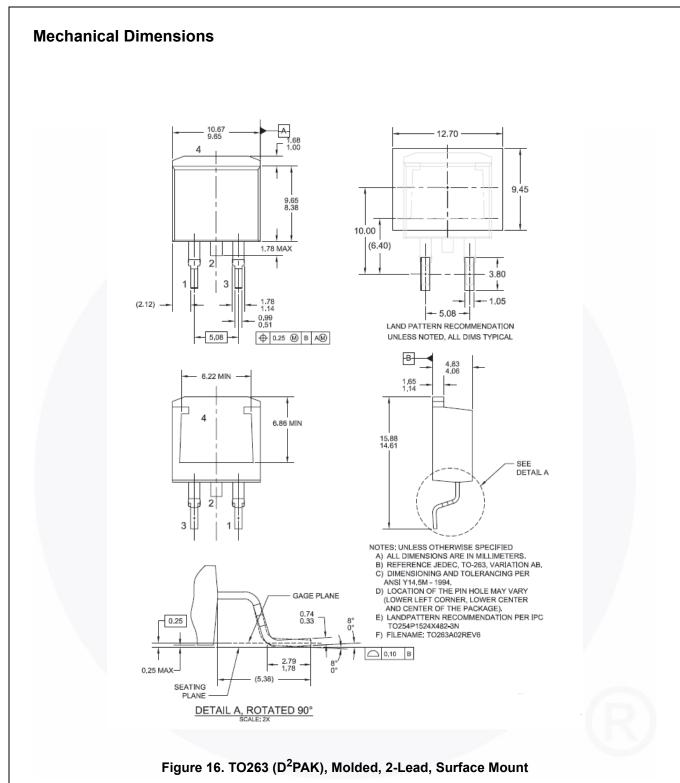
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DUT +  $V_{DS}$ a ۱<sub>SD</sub> م L Driver R<sub>G</sub>, Same Type as DUT L F ∨<sub>DD</sub>  $\prod V_{GS}$ • dv/dt controlled by  $R_{G}$ • I<sub>SD</sub> controlled by pulse period Î Gate Pulse Width  $\mathbf{V}_{\mathbf{GS}}$ D = Gate Pulse Period 10V (Driver) I<sub>FM</sub>, Body Diode Forward Current I <sub>SD</sub> di/dt (DUT)  $I_{RM}$ Body Diode Reverse Current  $V_{DS}$ (DUT) Body Diode Recovery dv/dt  $V_{SD}$ V<sub>DD</sub> Body Diode Forward Voltage Drop Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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