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FDB029N06 N-Channel PowerTrench[®] MOSFET 60 V, 193 A, 3.1 m Ω

Features

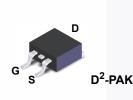
- $R_{DS(on)}$ = 2.4 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 75 A
- · Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

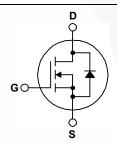
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench[®] process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Renewable System





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter	FDB029N06	Unit
V _{DSS}	Drain to Source Voltage		60	V
V _{GSS}	Gate to Source Voltage		±20	V
I _D		- Continuous (T _C = 25 ^o C, Silicon Limited)	193	A
	Drain Current	- Continuous (T _C = 100 ^o C, Silicon Limited)	136	
		- Continuous (T _C = 25 ^o C, Package Limited)	120	
I _{DM}	Drain Current	- Pulsed (Note 1)	772	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2		1434	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		6	V/ns
P _D	Power Dissipation	(T _C = 25 ^o C)	231	W
		- Derate Above 25°C	1.54	W/ºC
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

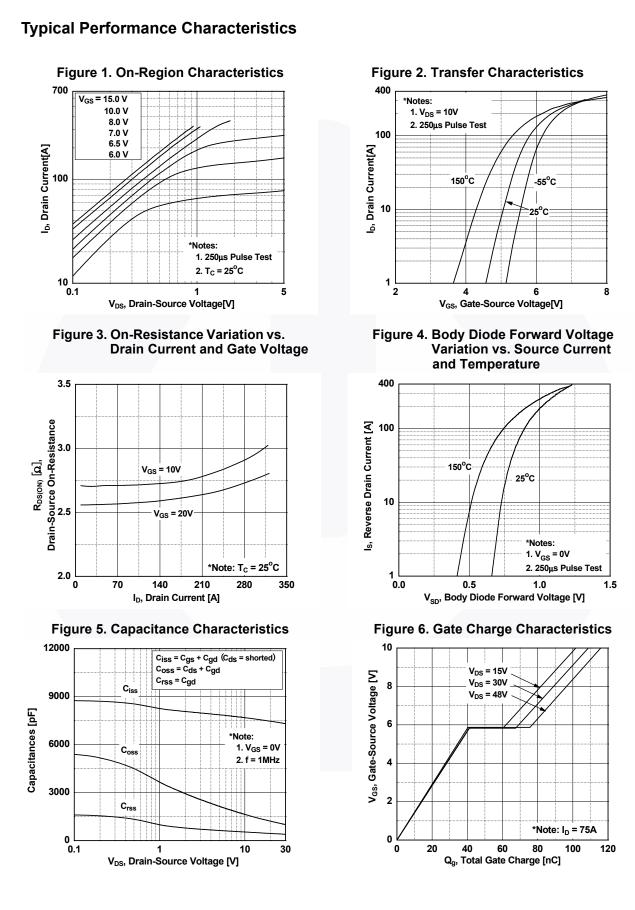
Thermal Characteristics

Symbol	Parameter	FDB029N06	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.65	
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
	Thermal Resistance, Junction to Ambient (1 in ² Pad of 2-oz Copper), Max.	40	

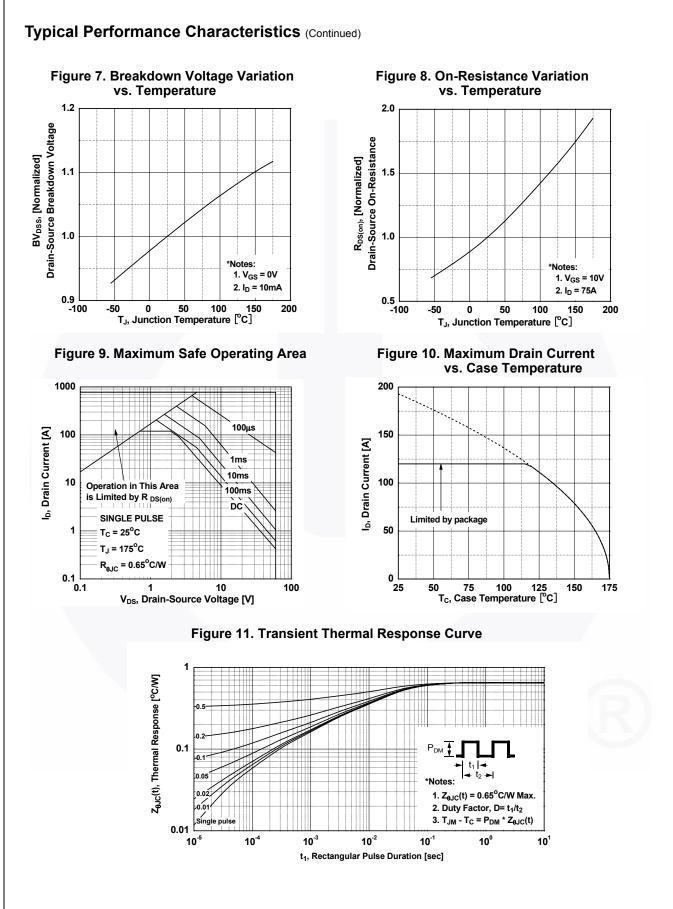
November 2013

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N-Channel Pov	
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MOSFET	

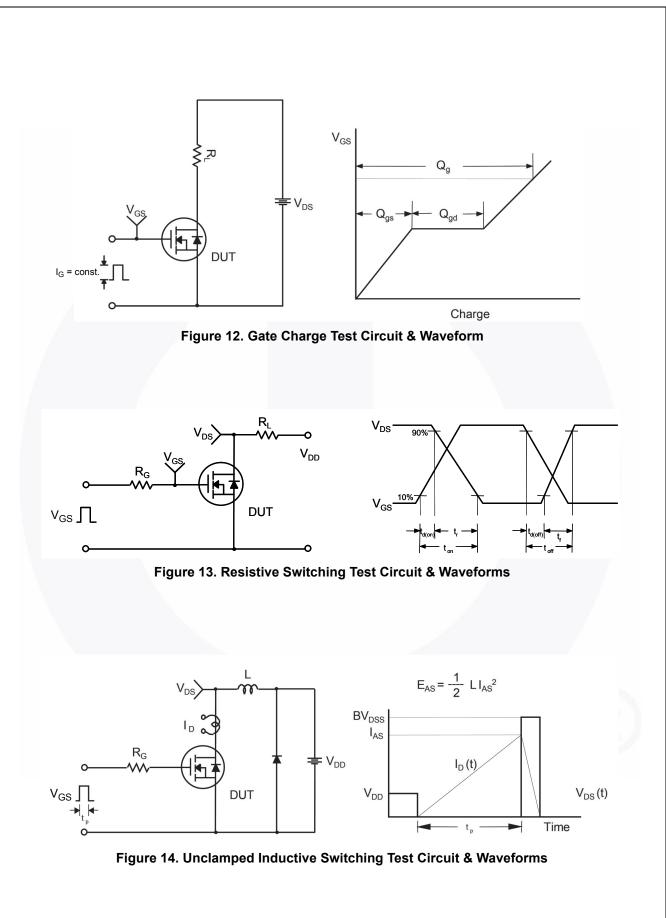
Typ. Max. - - 0.05 - - 1 - 500 - ±100 3.5 4.5	- V - V/ ⁰ <u>1</u> μ ^λ 500 π ^λ
 0.05 - - 1 - 500 - ±100 3.5 4.5	- V - V/ ⁰ <u>1</u> μ ^λ 500 π ^λ
 0.05 - - 1 - 500 - ±100 3.5 4.5	- V - V/ ⁰ <u>1</u> μ ^λ 500 π ^λ
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
0.05 - - 1 - 500 - ±100 3.5 4.5	- V/° <u>1</u> μA <u>500</u> μA ±100 nA
- 1 - 500 - ±100 3.5 4.5	1 500 μ ^μ ±100 n ^μ
- 500 - ±100 3.5 4.5	500 μ/ ±100 n/
- ±100 3.5 4.5	±100 n/
3.5 4.5	
	45 V
	45 V
	3.1 m
154 -	-
	9815 pl
	1455 pl
415 625	
116 151 40 -	
35 -	
39 87	87 ns
178 366	366 ns
54 118	
33 76	76 ns
- 193	193 A
- 772	
- 1.3	
46 -	- ns
50 -	- n
	- - - 46







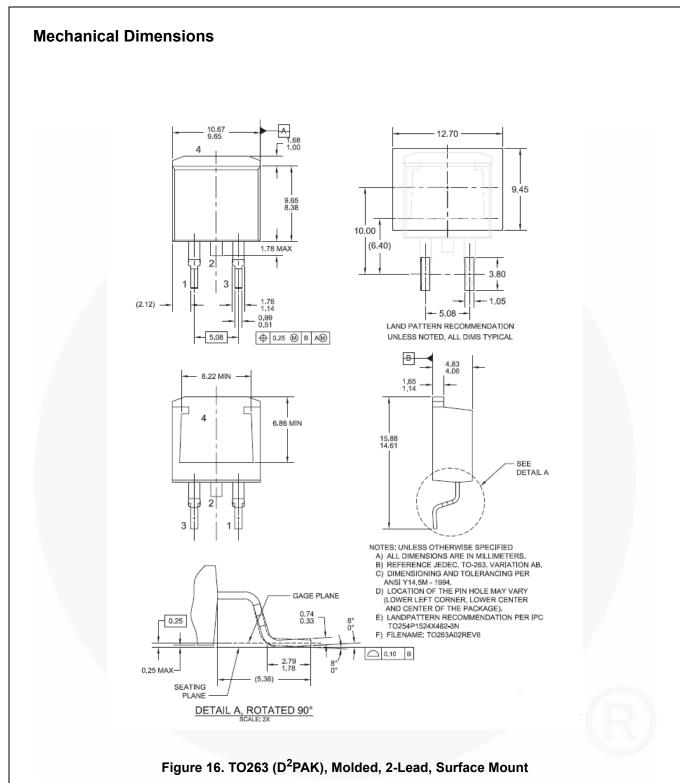
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FDB029N06 — N-Channel PowerTrench[®] MOSFET

DUT + V_{DS} a ۱_{SD} م L Driver R_G, Same Type as DUT L F ∨_{DD} $\prod V_{GS}$ • dv/dt controlled by R_{G} • I_{SD} controlled by pulse period Î Gate Pulse Width $\mathbf{V}_{\mathbf{GS}}$ D = Gate Pulse Period 10V (Driver) I_{FM}, Body Diode Forward Current I _{SD} di/dt (DUT) I_{RM} Body Diode Reverse Current V_{DS} (DUT) Body Diode Recovery dv/dt V_{SD} V_{DD} Body Diode Forward Voltage Drop Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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