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N-Channel Power Trench[®] MOSFET 30 V, 14.8 A, 7.2 m Ω

Features

- Max $r_{DS(on)}$ = 7.2 m Ω at V_{GS} = 10 V, I_D = 14.8 A
- Max $r_{DS(on)}$ = 9.5 m Ω at V_{GS} = 4.5 V, I_D = 12.4 A
- High performance technology for extremely low r_{DS(on)}
- Termination is Lead-free and RoHS Compliant

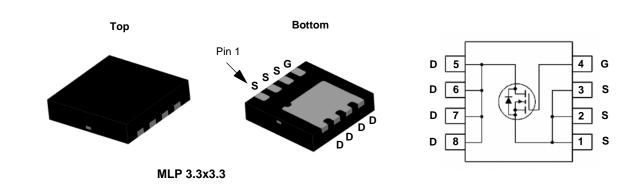


General Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced Power Trench[®] process that has been especially tailored to minimize the on-state resistance. This device is well suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.

Application

- DC DC Buck Converters
- Notebook battery power management
- Load switch in Notebook



MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V	
V _{GS}	Gate to Source Voltage			±20	V	
I _D	Drain Current -Continuous	T _C = 25 °C		18		
	-Continuous	T _A = 25 °C	(Note 1a)	14.8	Α	
	-Pulsed			45		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	72	mJ	
P _D	Power Dissipation	T _C = 25 °C		31	W	
	Power Dissipation	T _A = 25 °C	(Note 1a)	2.3		
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	4.0	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	53	C/W

Package Marking and Ordering Information

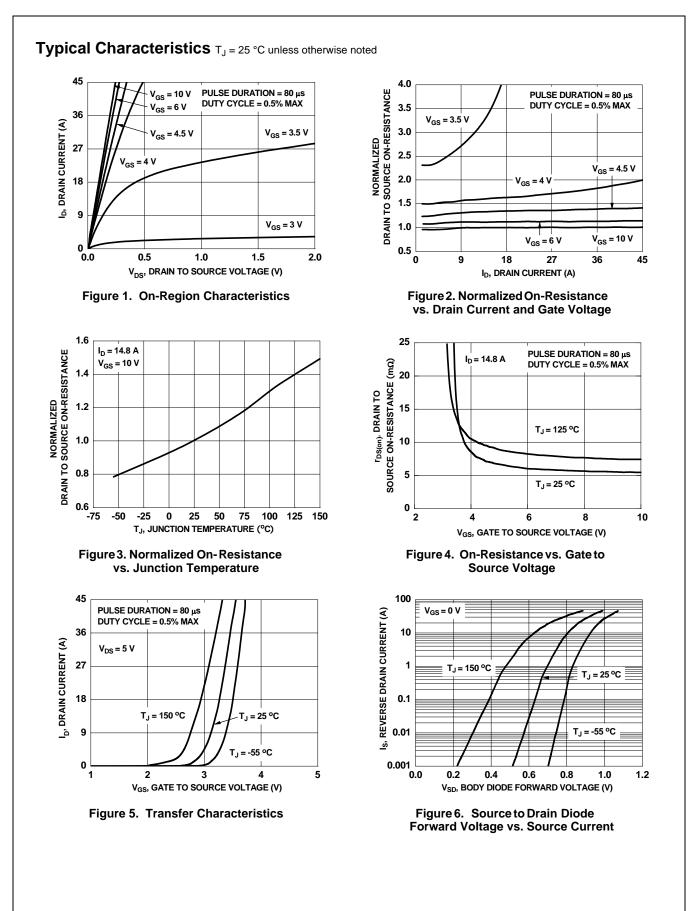
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMC7680	FDMC7680	MLP 3.3x3.3	13 "	12 mm	3000 units

June 2014

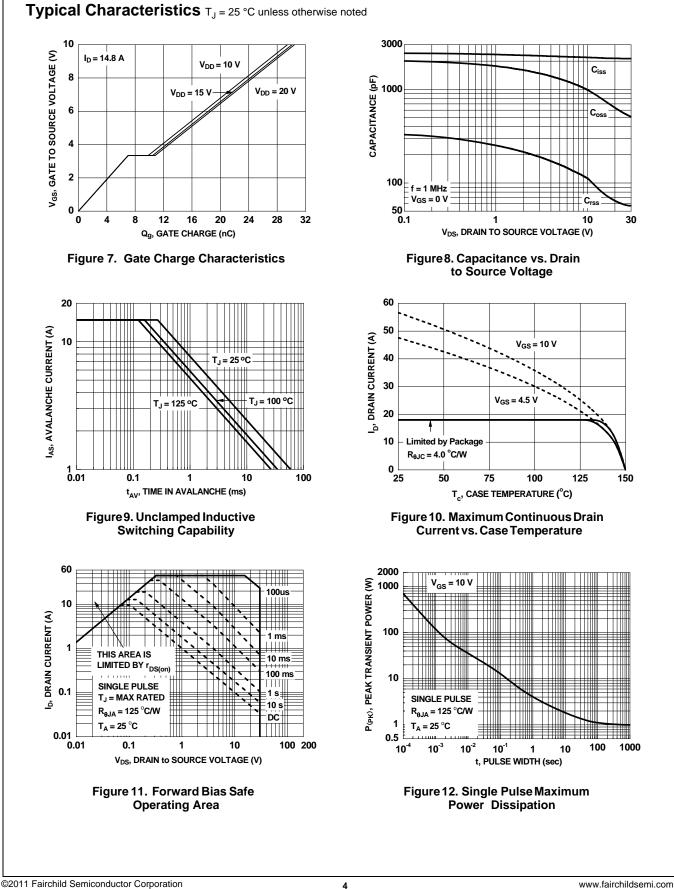
Off Characteristics BV_{DSS} Drain to Source Breakdown Voltage $I_D = 250 \ \mu$ A, $V_{GS} = 0 \ V$ 30Image: Constraint of Constraint	<i>l</i> lin Typ Max Uni	Min Typ	Test Conditions	Parameter	Symbol
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$\begin{array}{c c c c c c c c c c c c c c c c c c c $	u/			Zero Gate Voltage Drain Current	I _{DSS}
$ \begin{array}{c c c c c c c c c } \hline V_{GS}(th) & Gate to Source Threshold Voltage & V_{GS} = V_{DS}, I_D = 250 \ \mu\text{A} & 1.2 & 2.0 & 3.0 \\ \hline \Delta V_{GS}(th) & Gate to Source Threshold Voltage Temperature Coefficient & I_D = 250 \ \mu\text{A}, referenced to 25 °C & -6 & V_{GS} = 10 \ V, I_D = 14.8 \ A & 5.8 & 7.2 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 5.8 & 7.2 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 7.3 & 9.5 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 7.4 & 9.2 \\ \hline g_{FS} & Forward Transconductance & V_{DD} = 5 \ V, I_D = 14.8 \ A & 68 & D \\ \hline Dynamic Characteristics \\ \hline C_{iss} & Input Capacitance & V_{DS} = 15 \ V, V_{GS} = 0 \ V, I_D = 14.8 \ A & 68 & 0.5 & 1.6 \\ \hline Switching Characteristics \\ \hline Switching Characteristics \\ \hline t_{d(off)} & Turn-On Delay Time & V_{DD} = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_{d(off)} & Turn-Off Delay Time & V_{GS} = 0 \ V, I_D = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_{d(off)} & Turn-Off Delay Time & V_{GS} = 0 \ V, I_D = 15 \ V, I_D = 15 \ V, I_D = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_{d(off)} & Turn-Off Delay Time & V_{GS} = 0 \ V, I_D = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_{d(off)} & Turn-Off Delay Time & V_{GS} = 0 \ V to 10 \ V \\ \hline t_{f} & Fall Time & 3 & 10 \\ \hline t_{ggs} & Total Gate Charage & V_{GS} = 0 \ V to 10 \ V \\ \hline t_{ggs} & Total Gate Charage & V_{GS} = 0 \ V to 10 \ V \\ \hline t_{ggs} & Total Gate Charage & V_{GS} = 0 \ V to 10 \ V \\ \hline t_{ggd} & Gate to Drain "Miller" Charge & V_{GS} = 0 \ V to 14 \ V_{DD} = 15 \ V \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 \ D \\ \hline t_{D} = 14.8 \ A & 7 $			$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	Gate to Source Leakage Current	I _{GSS}
$ \begin{array}{c c c c c c c c } \hline V_{GS(th)} & Gate to Source Threshold Voltage & V_{GS} = V_{DS}, I_D = 250 \ \mu\text{A} & 1.2 & 2.0 & 3.0 \\ \hline \Delta V_{GS(th)} & Gate to Source Threshold Voltage Temperature Coefficient & I_D = 250 \ \mu\text{A}, referenced to 25 °C & -6 & V_{GS} = 10 \ V, I_D = 14.8 \ A & 5.8 & 7.2 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 5.8 & 7.2 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 7.3 & 9.5 \\ \hline V_{GS} = 10 \ V, I_D = 14.8 \ A & 7.4 & 9.2 \\ \hline g_{FS} & Forward Transconductance & V_{DD} = 5 \ V, I_D = 14.8 \ A & 68 & D \\ \hline Dynamic Characteristics \\ \hline C_{iss} & Input Capacitance & V_{DS} = 15 \ V, V_{GS} = 0 \ V, I_D = 14.8 \ A & 770 & 1020 \\ \hline C_{rss} & Reverse Transfer Capacitance & I & 755 & 115 \\ \hline R_g & Gate Resistance & I & 0.5 & 1.6 \\ \hline Switching Characteristics \\ \hline t_{d(off)} & Turn-On Delay Time & V_{DD} = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_{d(off)} & Turn-Off Delay Time & V_{GS} = 0 \ V, I_D = 15 \ V, I_D = 14.8 \ A, & 4 & 10 \\ \hline t_g(rOT) & Total Gate Charge & V_{GS} = 0 \ V to 10 \ V & 0 \ S = 15 \ V, I_D = 15 \ V, I_D = 15 \ V, I_D = 14.8 \ A & 4 & 10 \\ \hline t_g(rOT) & Turn-Off Delay Time & V_{GS} = 0 \ V to 10 \ V & 0 \ S = 15 \ V, I_D = 15 \ V, I_D = 14.8 \ A & 4 & 10 \\ \hline t_g(rOT) & Turn-Off Delay Time & V_{GS} = 0 \ V to 10 \ V & 0 \ S = 15 \ V, I_D = 15 \ V, I_D = 14.8 \ A & 7 \ S & 115 \\ \hline Total Gate Charge & V_{GS} = 0 \ V to 10 \ V & 0 \ S = 15 \ V, I_D = 15 \ V & 14 \ 19 \\ \hline t_g(rOT) & Total Gate Charge & V_{GS} = 0 \ V to 10 \ V & 0 \ S & 14 \ 19 \ S & 10 \ V_{DD} = 15 \ V & 14 \ 19 \ S & 10 \ V_{DD} = 14.8 \ A & 7 \ S & 114 \ 19 \ S & 14 \ S & 10 \ S & 14 \ S & 115 \$	I I	I			On Chara
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V _{GS} = 10 V, I _D = 14.8 A T _J = 125 °C 7.4 9.2 g _{FS} Forward Transconductance V _{DD} = 5 V, I _D = 14.8 A 68 Dynamic Characteristics C _{iss} Input Capacitance V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz 2145 2855 Coss Output Capacitance V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz 770 1020 Crss Reverse Transfer Capacitance V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz 775 115 Rg Gate Resistance 0.5 1.6 Switching Characteristics 12 22 td _(on) Turn-On Delay Time V _{DD} = 15 V, I _D = 14.8 A, V _{GS} = 10 V, R _{GEN} = 6 Ω 12 22 tf Fall Time 3 10 30 42 Qg(TOT) Total Gate Charge V _{GS} = 0 V to 10 V V _{GS} = 0 V to 4.5 V V _{DD} = 15 V 14 19 Qgs Total Gate Charge V _{GS} = 0 V to 4.5 V V _{DD} = 15 V 14 19 Qgd Gate to Drain "Miller" Charge V _{DD} = 0 V, I _D = 14.8 A 7 2 2 Drain-Source Diode Char	7.3 9.5 mg	7.3		Static Drain to Source On Resistance	rus(on)
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RgGate Resistance 0.5 1.6 Switching Characteristics $t_{d(on)}$ Turn-On Delay Time $V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}, V_{DD} = 15 \text{ V}, I_D = 14.8 \text{ A}, V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$ 12 22 $t_{d(off)}$ Turn-Off Delay Time $V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$ 25 40 $t_{d(off)}$ Turn-Off Delay Time $V_{GS} = 0 \text{ V}$ to $10 \text{ V}, V_{GS} = 0 \text{ V}$ to 10 V 30 42 $Q_{g(TOT)}$ Total Gate Charge $V_{GS} = 0 \text{ V}$ to $10 \text{ V}, V_{DD} = 15 \text{ V}$ 14 19 Q_{gs} Total Gate Charge $V_{GS} = 0 \text{ V}$ to $4.5 \text{ V}, V_{DD} = 15 \text{ V}$ 14 19 Q_{gd} Gate to Drain "Miller" Charge $V_{OS} = 0 \text{ V}, I_S = 14.8 \text{ A}$ 7 4 Drain-Source Diode Characteristics		75	t = 1 MHz		
Switching Characteristics $t_{d(on)}$ Turn-On Delay Time $V_{DD} = 15 \text{ V}, \text{ I}_D = 14.8 \text{ A},$ 1222 t_r Rise Time $V_{DD} = 15 \text{ V}, \text{ I}_D = 14.8 \text{ A},$ 410 $t_{d(off)}$ Turn-Off Delay Time $V_{GS} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$ 2540 t_f Fall Time310 $Q_g(TOT)$ Total Gate Charge $V_{GS} = 0 \text{ V to } 10 \text{ V}$ 3042 Q_{gs} Total Gate Charge $V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $I_D = 15 \text{ V}$ 1419 Q_{gd} Gate to Drain "Miller" Charge $I_D = 14.8 \text{ A}$ 74Drain-Source Diode Characteristics	0.5 1.6 Ω	0.5		Gate Resistance	
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$Q_{g(TOT)}$ Total Gate Charge $V_{GS} = 0 \ V \text{ to } 4.5 \ V$ $V_{DD} = 15 \ V$ 1419 Q_{gs} Total Gate Charge $I_D = 14.8 \ A$ 74 Q_{gd} Gate to Drain "Miller" Charge44Drain-Source Diode Characteristics			$V_{CC} = 0 V to 10 V$		4
Q _{gs} Total Gate Charge I _D = 14.8 Å 7 Q _{gd} Gate to Drain "Miller" Charge 4 Drain-Source Diode Characteristics Voc = 0 V. Ic = 14.8 Å Note 2) 0.84 1.2					Q _{g(TOT)}
Qgd Gate to Drain "Miller" Charge 4 Drain-Source Diode Characteristics Voc = 0 V. Ic = 14.8 A (Note 2) 0.84 1.2			$I_{D} = 14.8 \text{ A}$	0	Q
Drain-Source Diode Characteristics				-	
V _{CC} = 0 V, I _C = 14.8 A (Note 2) 0.84 1.2				·	×
$V_{GS} = 0.04$ (Note 2) 0.04 1.2	0.84 1.2	0.84	$V_{} = 0 V_{} = 14.8 A_{}$ (Note 2)		Drain-Sot
V_{SD} Source to Drain Diode Forward Voltage $V_{GS} = 0 V, I_S = 1.9 A$ (Note 2) 0.73 1.2	V			Source to Drain Diode Forward Voltage	V _{SD}
t Reverse Recovery Time 34 54				Reverse Recovery Time	t
t _{rr} Reverse Recovery Time Q _{rr} Reverse Recovery Charge I _F = 14.8 A, di/dt = 100 A/μs 15 24			I _F = 14.8 A, di/dt = 100 A/μs		
NOTES: 1: R_{0JA} is determined with the device mounted on a 1 in ² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is d the user's board design.	nteed by design while $R_{\theta CA}$ is determine	uaranteed by design wh	on a 1.5 x 1.5 in. board of FR-4 material. $R_{\theta JC}$ is gu	ined with the device mounted on a 1 in ² pad 2 oz copper pa	NOTES: 1: R _{0JA} is detern

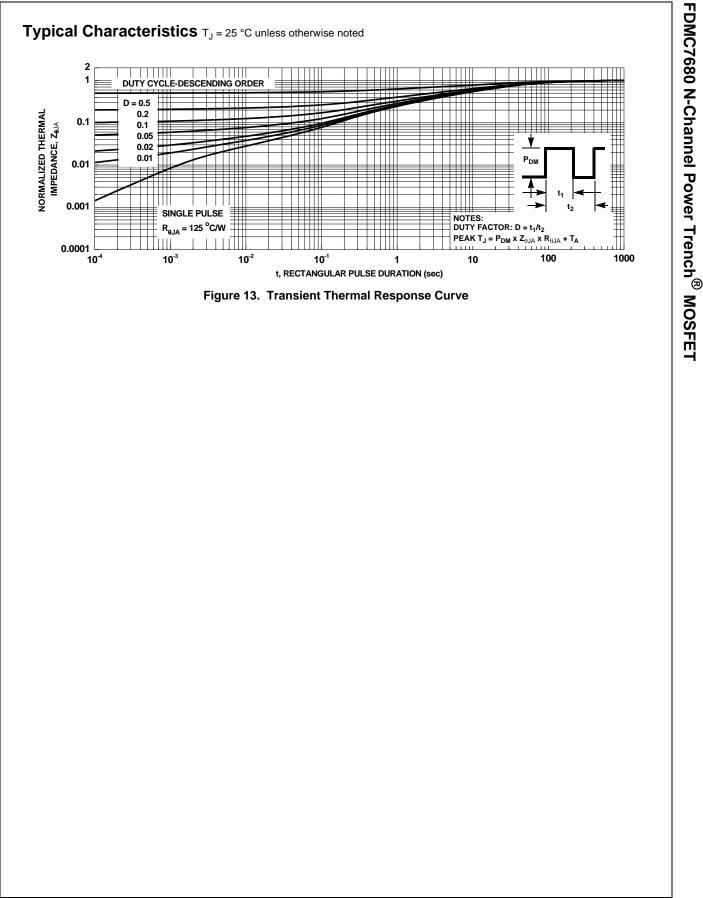
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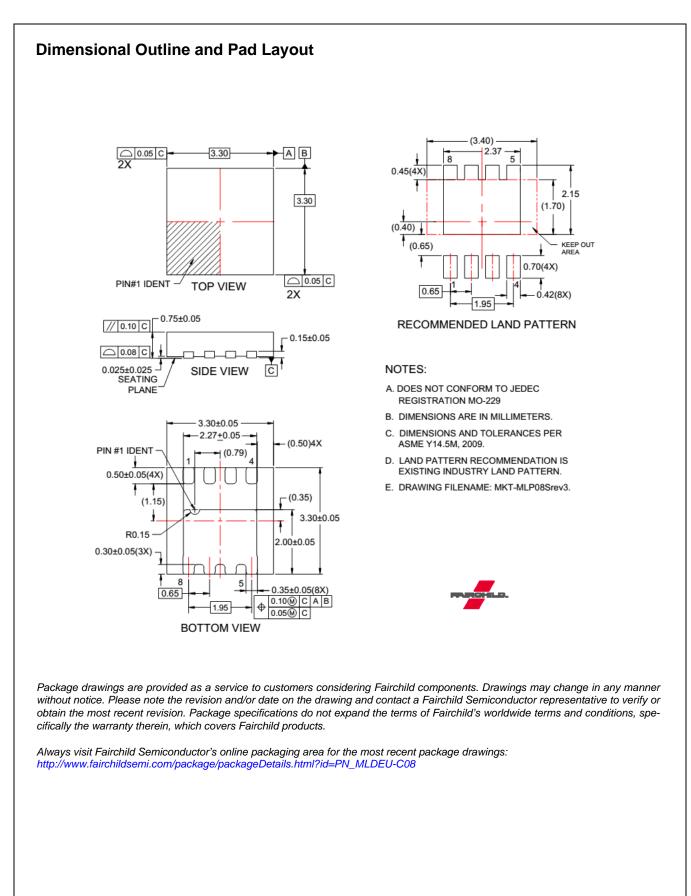
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Product Status	Definition
Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
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