

-500mA/-50V Digital transistor (with built-in resistors)

Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-500mA
R ₁	2.2kΩ
R ₂	2.2kΩ

SOT-346 SC-59

Outline

(SMT3)

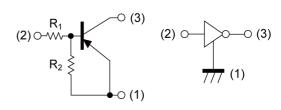
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.(see equivalent circuit)
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive

biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.

3)Only the on/off conditions need to be set for operation, making the device design easy.

•Inner circuit



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTB123EK	SOT-346 (SMT3)	2928	T146	180	8	3000	F12

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	-50	V
Input voltage	V _{IN}	-12 to 10	V
Collector current	I _{C(MAX)} *1	-500	mA
Power dissipation	P _D *2	200	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

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Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input valtage	$V_{I(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.5	\/	
Input voltage	V _{I(on)}	$V_O = -0.3V$, $I_O = -20$ mA	-3.0	-	-	V	
Output voltage	V _{O(on)}	$I_0 = -50 \text{mA}, I_1 = -2.5 \text{mA}$	-	-100	-300	mV	
Input current	I _I	V _I = -5V	-	-	-3.8	mA	
Output current	I _{O(off)}	V _{CC} = -50V, V _I = 0V	-	-	-500	nA	
DC current gain	G _I *3	$V_O = -5V$, $I_O = -50$ mA	39	-	-	-	
Input resistance	R ₁	-	1.54	2.2	2.86	kΩ	
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-	
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 50mA, f = 100MHz	-	200	-	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land

^{*3} Pulsed

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

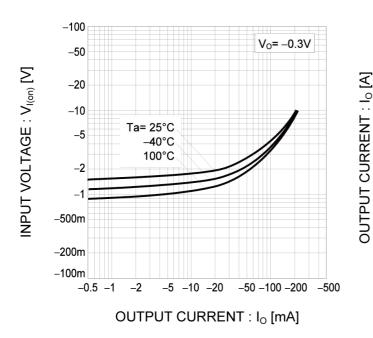


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

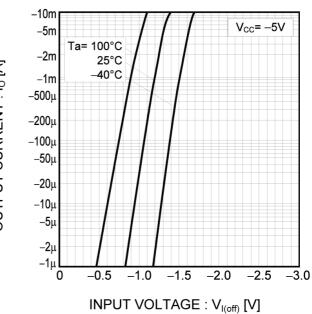


Fig.3 Output Current vs. Output Voltage

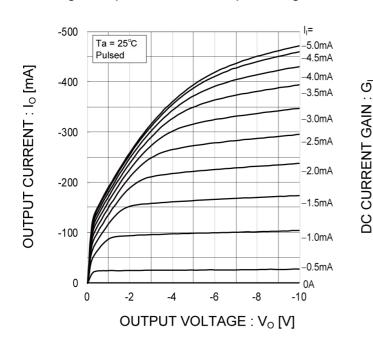
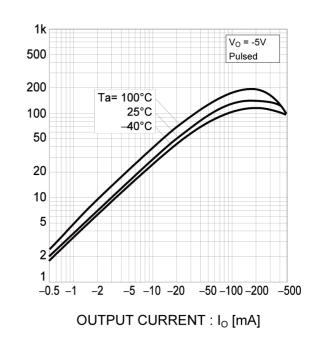
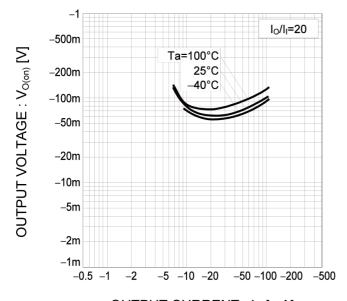


Fig.4 DC Current Gain vs. Output Current



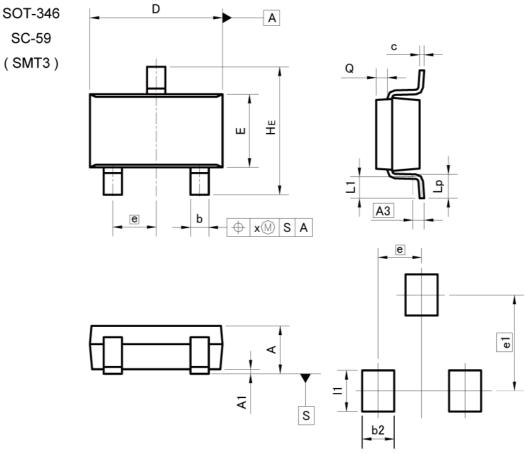
●Electrical characteristic curves (T_a =25°C)

Fig.5 Output Voltage vs. Output Current



OUTPUT CURRENT : I_O [mA]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
х	-	0.10	-	0.004	
у	- >	0.10	(-	0.004	

DIM		MILIMETERS		INCHES		
	DIM	MIN MAX		MIN	MAX	
	b2	-	0.60	_	0.024	
	e1	2.10		0.0	83	
	11	-3	0.90	-	0.035	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
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