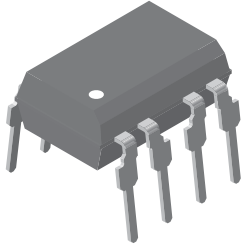
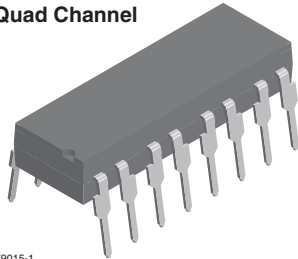


## Optocoupler, Phototransistor Output, (Dual, Quad Channel), 110 °C Rated

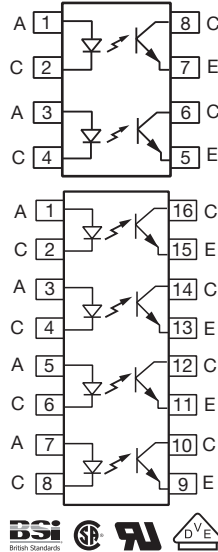
Dual Channel



Quad Channel



1179015-1



### FEATURES

- Operating temperature from - 55 °C to + 110 °C
- Identical channel to channel footprint
- Dual and quad packages feature:
  - Reduced board space
  - Lower pin and parts count
  - Better channel to channel CTR match
  - Improved common mode rejection
- Isolation test voltage, 5300 V<sub>RMS</sub>
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



RoHS COMPLIANT

### AGENCY APPROVALS

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- BSI IEC 60950; IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending

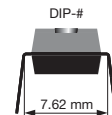
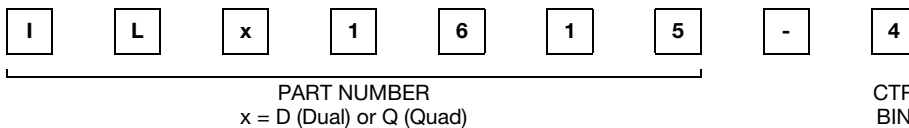
### DESCRIPTION

The ILD1615, ILQ1615 are multi-channel 110 °C rated phototransistor optocouplers that use GaAs IRLED emitters and high gain NPN phototransistors. These devices are constructed using over/under leadframe optical coupling and double molded insulation technology resulting a withstand test voltage of 7500 V<sub>AC PEAK</sub> and a working voltage of 1700 V<sub>RMS</sub>.

The binned min./max. and linear CTR characteristics make these devices well suited for DC or AC voltage detection. Eliminating the phototransistor base connection provides added electrical noise immunity from the transients found in many industrial control environments.

Because of guaranteed maximum non-saturated and saturated switching characteristics, the ILD1615, ILQ1615 can be used in medium speed data I/O and control systems. The binned min./max. CTR specification allow easy worst case interface calculations for both level detection and switching applications. Interfacing with a CMOS logic is enhanced by the guaranteed CTR at I<sub>F</sub> = 1.0 mA.

### ORDERING INFORMATION



AGENCY CERTIFIED/PACKAGE	DUAL CHANNEL	QUAD CHANNEL
	CTR (%)	
UL, CSA, BSI	160 to 320	160 to 320
DIP-8	ILD1615-4	-
DIP-16	-	ILQ1615-4

# ILD1615, ILQ1615



Vishay Semiconductors Optocoupler, Phototransistor Output,  
(Dual, Quad Channel), 110 °C Rated

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
<b>INPUT</b>				
Peak reverse voltage		V <sub>R</sub>	6.0	V
Forward current		I <sub>F</sub>	60	mA
Surge current		I <sub>FSM</sub>	1.5	A
Power dissipation		P <sub>diss</sub>	100	mW
Derate linearly from 25 °C			1.0	mW/°C
<b>OUTPUT</b>				
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7.0	V
Collector current		I <sub>C</sub>	50	mA
	t < 1.0 ms	I <sub>C</sub>	100	mA
Power dissipation		P <sub>diss</sub>	150	mW
Derate linearly from 25 °C			1.5	mW/°C
<b>COUPLER</b>				
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C
Operating temperature		T <sub>amb</sub>	- 55 to + 110	°C
Soldering temperature <sup>(1)</sup>	2.0 mm distance from case bottom	T <sub>slid</sub>	260	°C
Package power dissipation ILD1615			400	mW
Derate linearly from 25 °C			5.33	mW/°C
Package power dissipation ILQ1615			500	mW
Derate linearly from 25 °C			6.67	mW/°C
Isolation test voltage	t = 1.0 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Creepage distance			≥ 7.0	mm
Clearance distance			≥ 7.0	mm
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω

## Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to wave profile for soldering conditions for through hole devices.

ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>						
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.0	1.15	1.3	V
Breakdown voltage	I <sub>R</sub> = 10 μA	V <sub>BR</sub>	6.0	30		V
Reverse current	V <sub>R</sub> = 6.0 V	I <sub>R</sub>		0.01	10	μA
Capacitance	V <sub>R</sub> = 0 V, f = 1.0 MHz	C <sub>O</sub>		25		pF
<b>OUTPUT</b>						
Collector emitter capacitance	V <sub>CE</sub> = 5.0 V, f = 1.0 MHz	C <sub>CE</sub>		6.8		pF
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>		5.0	100	nA
Collector emitter breakdown voltage	I <sub>CE</sub> = 0.5 mA	BV <sub>CEO</sub>	70			V
Emitter collector breakdown voltage	I <sub>E</sub> = 0.1 mA	BV <sub>ECO</sub>	7.0			V
<b>PACKAGE TRANSFER CHARACTERISTICS</b>						
Channel/channel CTR match	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 5.0 V	CTR <sub>X</sub> /CTR <sub>Y</sub>	1 to 1		2 to 1	
<b>COUPLER</b>						
Capacitance (input to output)	V <sub>IO</sub> = 0 V, f = 1.0 MHz	C <sub>IO</sub>		0.8		pF
Insulation resistance	V <sub>IO</sub> = 500 V, T <sub>A</sub> = 25 °C	R <sub>S</sub>	10 <sup>12</sup>	10 <sup>14</sup>		Ω
Channel to channel isolation			500			V <sub>AC</sub>

## Note

- Minimum and maximum values are tested requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.



<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio (collector emitter saturated)	$I_F = 1.0\text{ mA}$ , $V_{CE} = 0.4\text{ V}$	ILD1615-4	$CTR_{CEsat}$		100		%
		ILQ1615-4					
Current transfer ratio (collector emitter)	$I_F = 10\text{ mA}$ , $V_{CE} = 5.0\text{ V}$	ILD1615-4	$CTR_{CE}$	160	200	320	%
		ILQ1615-4					
	$I_F = 1.0\text{ mA}$ , $V_{CE} = 5.0\text{ V}$	ILD1615-4	$CTR_{CE}$	56	90		%
	ILQ1615-4						

<b>SWITCHING CHARACTERISTICS</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>NON-SATURATED</b>							
Turn-on time	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_{on}$		3.0		$\mu\text{s}$
Rise time	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_r$		2.0		$\mu\text{s}$
Turn-off time	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_{off}$		2.3		$\mu\text{s}$
Fall time	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_f$		2.0		$\mu\text{s}$
Propagation H to L	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_{PHL}$		1.1		$\mu\text{s}$
Propagation L to H	$I_F = 10\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 75\text{ }\Omega$ , 50 % of $V_{PP}$		$t_{PLH}$		2.5		$\mu\text{s}$
<b>SATURATED</b>							
Turn-on time	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_{on}$		6.0		$\mu\text{s}$
Rise time	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_r$		4.6		$\mu\text{s}$
Turn-off time	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_{off}$		25		$\mu\text{s}$
Fall time	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_f$		15		$\mu\text{s}$
Propagation H to L	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_{PHL}$		5.4		$\mu\text{s}$
Propagation L to H	$I_F = 5.0\text{ mA}$ , $V_{CC} = 5.0\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ , $V_{HT} = 1.5\text{ V}$		$t_{PLH}$		7.4		$\mu\text{s}$

<b>COMMON MODE TRANSIENT IMMUNITY</b> ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Common mode rejection output high	$V_{CM} = 50\text{ V}_{P-P}$ , $R_L = 1.0\text{ k}\Omega$ , $I_F = 0\text{ mA}$	$CM_H$		5000		V/ $\mu\text{s}$	
Common mode rejection output low	$V_{CM} = 50\text{ V}_{P-P}$ , $R_L = 1.0\text{ k}\Omega$ , $I_F = 10\text{ mA}$	$CM_L$		5000		V/ $\mu\text{s}$	
Common mode coupling capacitance		$C_{CM}$		0.01		pF	

# ILD1615, ILQ1615



Vishay Semiconductors Optocoupler, Phototransistor Output,  
(Dual, Quad Channel), 110 °C Rated

## TYPICAL CHARACTERISTICS ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ , unless otherwise specified)

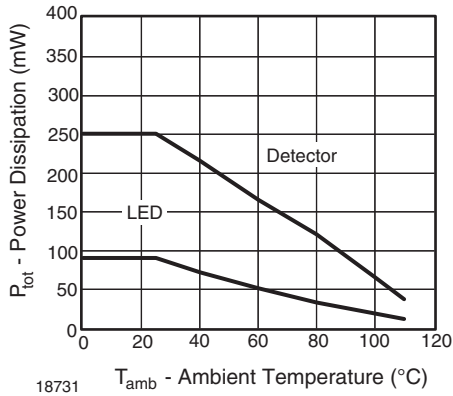


Fig. 1 - Permissible Power Dissipation vs. Temperature Non-Saturation Operation

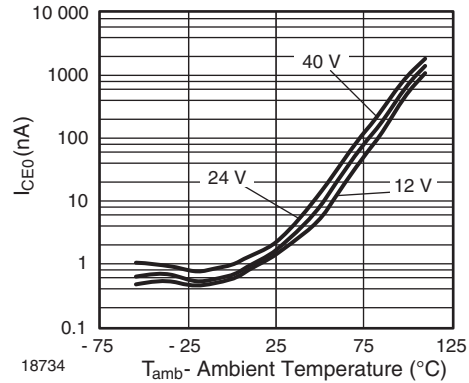


Fig. 4 - Collector to Emitter Dark Current vs. Ambient Temperature

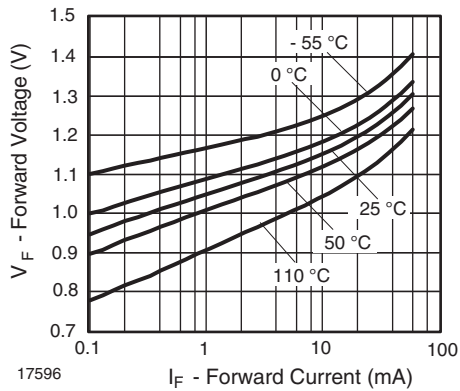


Fig. 2 - Forward Voltage vs. Forward Current

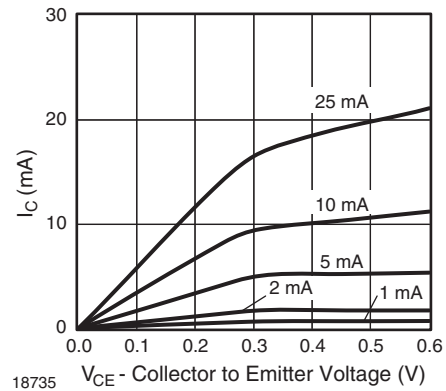


Fig. 5 - Normalized Current vs. Collector Emitter Saturation Voltage

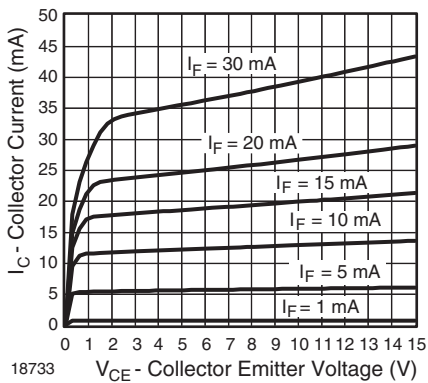


Fig. 3 - Collector Current vs. Collector Emitter Voltage

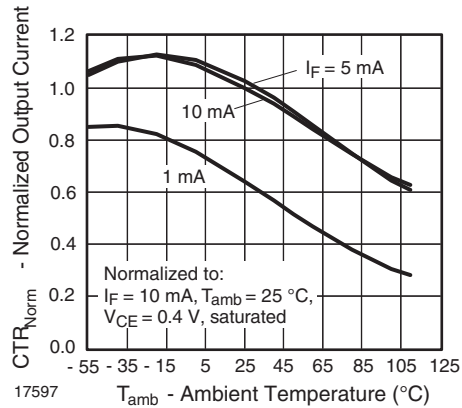


Fig. 6 - Normalized Current Transfer Ratio vs. Ambient Temperature

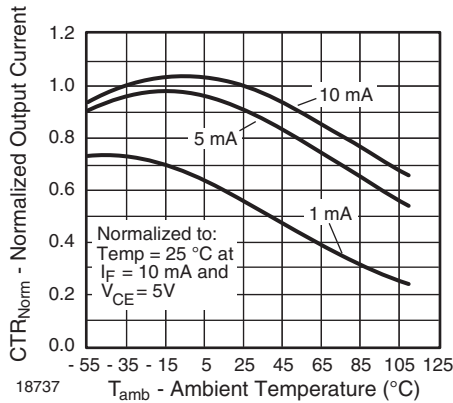


Fig. 7 - Normalized CTR vs. Temperature

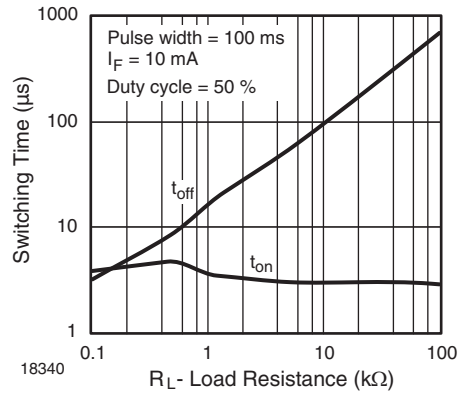


Fig. 10 - Forward Resistance vs. Forward Current

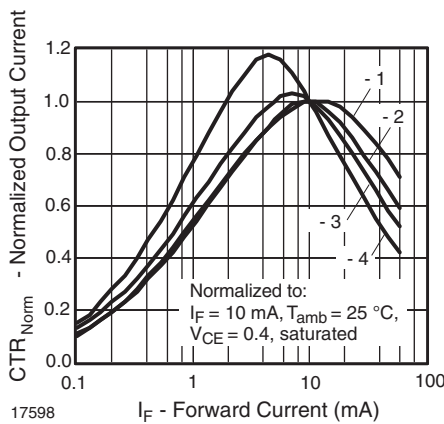


Fig. 8 - Normalized CTR vs. Forward Current

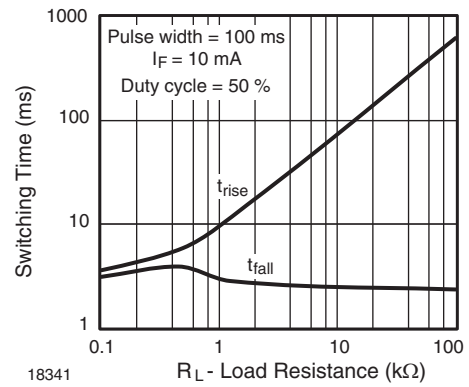


Fig. 11 - Forward Resistance vs. Forward Current

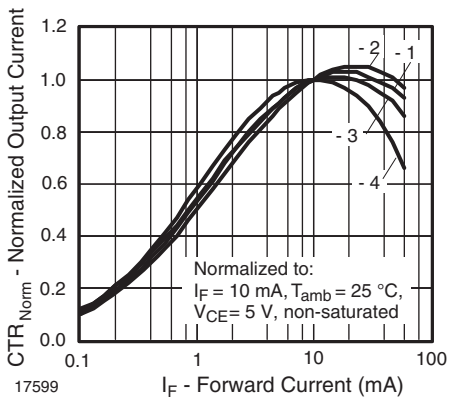


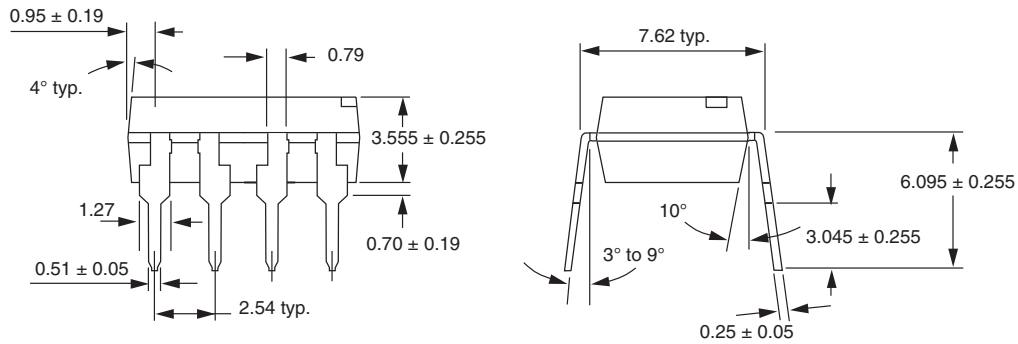
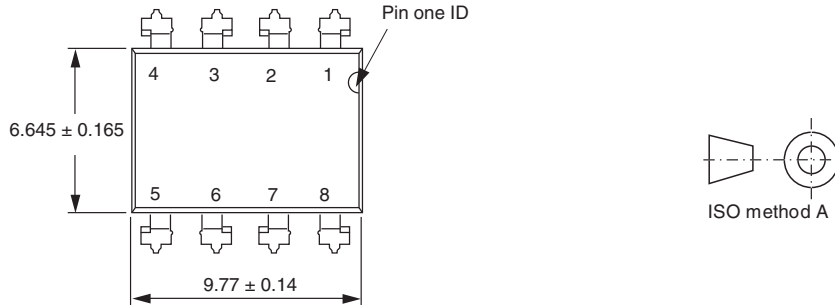
Fig. 9 - Normalized CTR vs. Forward Current

# ILD1615, ILQ1615

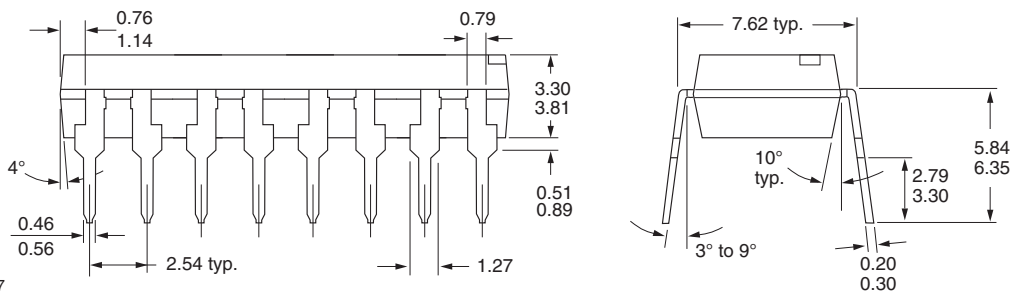
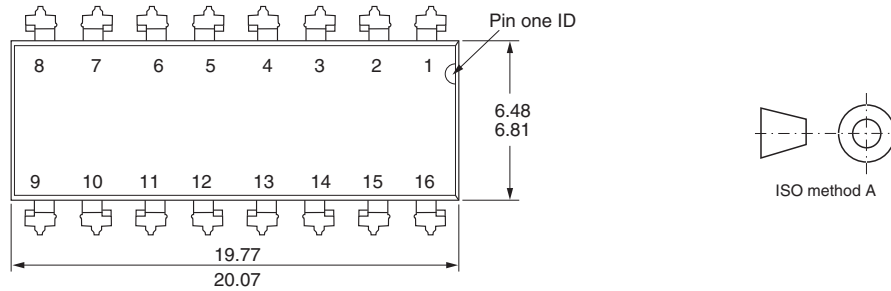


Vishay Semiconductors Optocoupler, Phototransistor Output,  
(Dual, Quad Channel), 110 °C Rated

## PACKAGE DIMENSIONS in millimeters

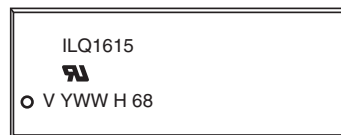
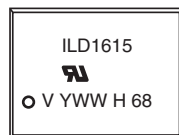


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## PACKAGE MARKING





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