



DUP45V6P5

QUAD SURFACE MOUNT TVS ARRAY

Features

- Quad TVS in Common Anode Configuration
- Ultra-Small Surface Mount Package
- Ideal For Transient Suppression and ESD Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

ESD Capability

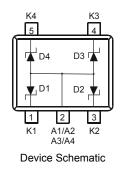
- IEC 61000-4-2 Contact Method ±8kV
- IEC 61000-4-2 Air Discharge Method ±15kV



- Case: SOT953
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish: Matte Tin, Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.002 grams (approximate)



Top View



Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|--------|--------------------|
| DUP45V6P5-7 | SOT953 | 10,000/Tape & Reel |

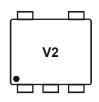
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



V2 = Product type marking code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|---|--------|-------|------|
| Forward Voltage @ I _F = 10mA | VF | 0.9 | V |

Thermal Characteristics

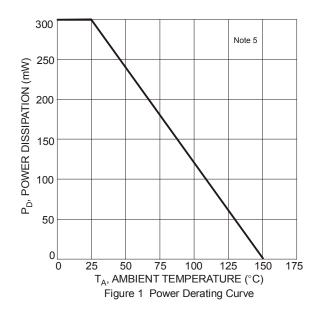
| Characteristic | Symbol | Value | Unit |
|--|----------------------------------|-------------|------|
| Power Dissipation (Notes 5) | PD | 300 | mW |
| Peak Power Dissipation, 8x20µS Waveform (Note 6) | P _{pk} | 20 | W |
| Thermal Resistance, Junction-to-Ambient (Note 5) | R _{0JA} | 417 | °C/W |
| Operating and Storage Temperature Range | T _{J,} T _{STG} | -55 to +150 | ۵° |

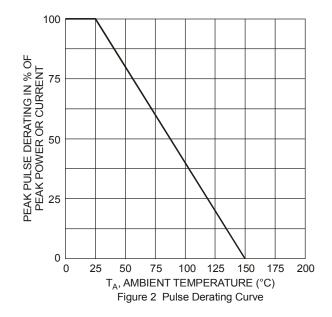
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Туре | Marking | Breakdown Voltage (Note 7) | | Leakage Current (Note 7) | | Max. Clamping Voltage (Note 6) | | Capacitance @0V Bias(pF) (Note 8) | | Capacitance @3V Bias(pF) (Note 8) | | |
|-----------|---------|--|---------|-----------------------------|-----------------|--------------------------------------|-------|---|-----|---|-----|------|
| Number | Code | V _{BR} @ I _T = 1mA | | I _{RM} @ | V _{RM} | V _C @ I _{PP} | | CT | | CT | | |
| | | Min (V) | Nom (V) | Max (V) | Max(μA) | (V) | Vc(V) | IPP(A) | Тур | Мах | Тур | Max |
| DUP45V6P5 | V2 | 5.3 | 5.6 | 5.9 | 1.0 | 3.0 | 10.5 | 1.0 | 13 | 17 | 7.0 | 11.5 |

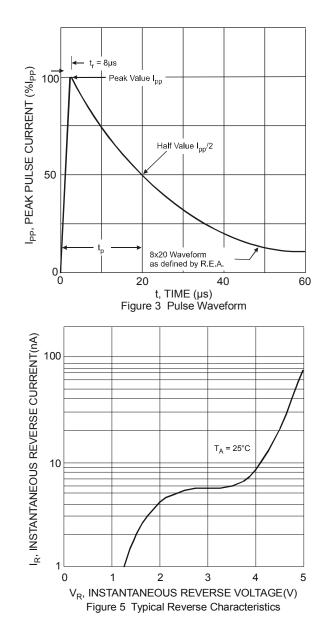
Notes: 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. Suggested Pad Layout Document AP02001, which can be found on our website at http://www.diodes.com.

6. Non-repetitive current pulse per Figure 3 and derate above $T_A = +25^{\circ}C$ per Figure 3. 7. Short duration pulse test used to minimize self-heating effect. 8. Per element, f = 1MHZ, $T_A = +25^{\circ}C$









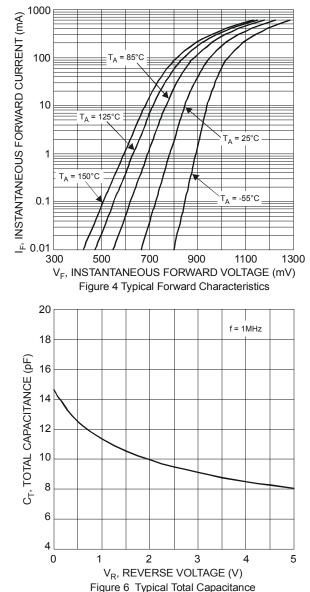
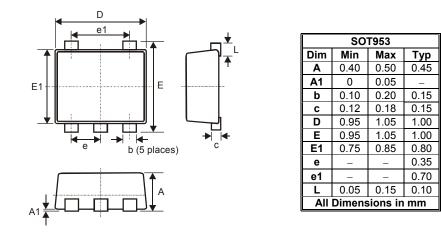


Figure 6 Typical Total Capacitance vs. Reverse Voltage (Per Element)



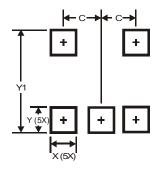
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 0.350 |
| X | 0.200 |
| Y | 0.200 |
| Y1 | 1.100 |



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