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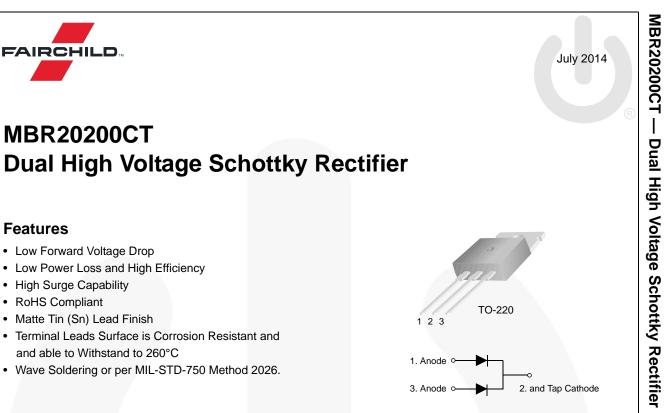


# **ON Semiconductor**®

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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="mailto:www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to <a href="mailto:Fairchild\_questions@onsemi.com">Fairchild\_questions@onsemi.com</a>.

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## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
MBR20200CTTU	MBR20200CT	TO-220 3L	Rail

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage		200	V
V <sub>R</sub>	Maximum DC Reverse Voltage		200	V
1	Average Rectified Forward Current, at T <sub>C</sub> = 115°C	per Leg	10	A
I <sub>F(AV)</sub>		per Device	20	
I <sub>FSM</sub>	Peak Forward Surge Current, 8.3 ms Half-Sine Wave		150	А
T <sub>STG</sub>	Storage Temperature Range		-50 to +150	°C
TJ	Operating Junction Temperature		150	°C

## Thermal Characteristics<sup>(1)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter Val		Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case per Leg	1.5	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient per Leg62.5		°C/W

#### Note:

1. MIL standard 883-1012 and JESD51-10.

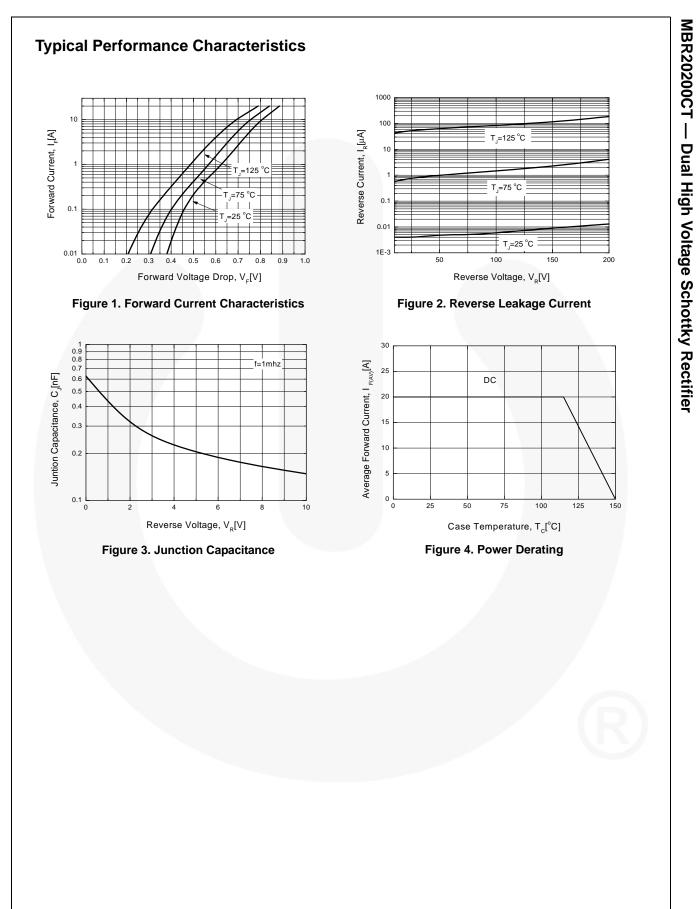
## **Electrical Characteristics**<sup>(2)</sup>

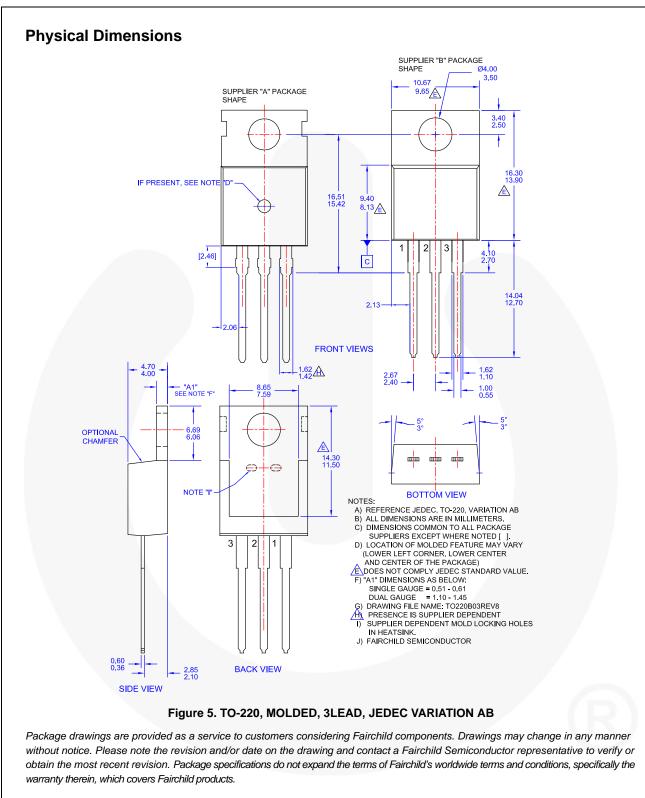
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
I <sub>R</sub>	Reverse Current	$V_{R} = 200 \text{ V}, \text{ T}_{C} = 25^{\circ}\text{C}$		0.2	mA
		$V_{R} = 200 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$		2.0	
V <sub>F</sub>	Forward Voltage	$I_{\rm F} = 10 \text{ A}, \text{ T}_{\rm C} = 25^{\circ} \text{C}$		0.9	v
		$I_{F} = 10 \text{ A}, \text{ T}_{C} = 125^{\circ}\text{C}$		0.8	
		$I_{\rm F} = 20 \text{ A}, \text{ T}_{\rm C} = 25^{\circ} \text{C}$		1.0	
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 125°C		0.9	

### Note:

2. DC Item are tested by pulse test: pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2%.





Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: <u>http://www.fairchildsemi.com/dwg/TO/TO220B03.pdf</u>.

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