

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized applications, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an equif prese



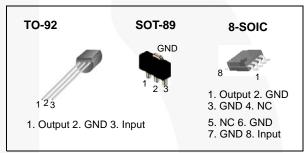
MC78LXXA / LM78LXXA 3-Terminal 0.1 A Positive Voltage Regulator

Features

- Maximum Output Current of 100 mA
- Output Voltage of 5 V, 6 V, 8 V, 12 V, and 15 V
- Thermal Overload Protection
- Short-Circuit Current Limiting
- Output Voltage Offered in ±5% Tolerance

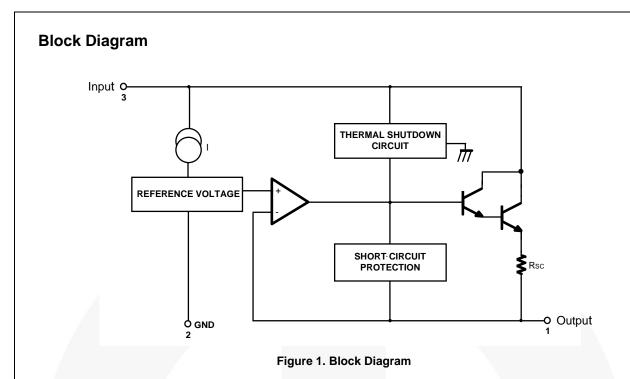
Description

The MC78LXXA / LM78LXXA series of fixed-voltage monolithic integrated circuit voltage regulators are suitable for applications that required supply current up to 100 mA.



Product Number	Package	Packing Method	Output Voltage Tolerance	Operating Temperature				
LM78L05ACZ		Bulk						
LM78L05ACZX		Tape & Reel						
LM78L05ACZXA		Ammo						
LM78L12ACZ		Bulk						
LM78L12ACZX		Tape & Reel						
MC78L05ACP	TO-92	Bulk						
MC78L05ACPXA		Ammo						
MC78L06ACP		Bulk	±5%	-40 to +125°C				
MC78L08ACP		Bulk						
MC78L15ACP		Bulk						
MC78L15ACPXA		Ammo						
MC78L05ACD	0.000	Rail						
MC78L05ACDX	8-SOIC	Tape & Reel						
MC78L05ACHX	SOT-89	Tape & Reel	1					
MC78L08ACHX	301-09	Tape & Reel	1					

Ordering Information



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Paramete	r	Value	Unit
V		$V_0 = 5 V \text{ to } 8 V$	30	V
VI	Input Voltage	V _O = 12 V to 15 V	35	V
T _{OPR}	Operating Temperature Range		-40 to +125°C	°C
T _{J(MAX)}	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-65 to +150	°C	
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-Case	TO-92	50	°C/W
		TO-92	150	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-Air	SOT-89	225	°C/W
		8-SOIC	160	°C/W

Electrical Characteristics (MC78L05A / LM78L05A)

 $V_I = 10 \text{ V}, \text{ } I_O = 40 \text{ mA}, \text{ } -40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, \text{ } C_I = 0.33 \text{ } \mu\text{F}, \text{ } C_O = 0.1 \text{ } \mu\text{F}, \text{ } \text{unless otherwise specified}.$

Symbol	Parameter		Conc	ditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		T _J = 25°C			5.0	5.2	V
A\/	ΔV_{O} Line Regulation ⁽¹⁾		T,∣ = 25°C	$7 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$		8	150	mV
Δvo			1 _J = 25 C	$8~V \le V_I \le 20~V$		6	100	mV
ΔV _O	Load Regulation ⁽¹⁾		T _{.1} = 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		11	60	mV
7v0			1 j = 25 C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		5.0	30.0	mV
V	Output Voltage	ut Voltago		$1 \text{ mA} \le I_O \le 40 \text{ mA}$			5.25	V
Vo	Output voltage		$7 \text{ V} \leq \text{V}_{\text{I}} \leq \text{V}_{\text{MAX}}^{(2)}$	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	4.75		5.25	V
Ι _Q	Quiescent Current		$T_J = 25^{\circ}C$			2.0	5.5	mA
ΔI_Q	Quiescent Current	With Line	$8 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	A la			0.1	mA
V _N	Output Noise Voltag	е	T _A = 25°C, 10 Hz	≤ f ≤ 100 kHz		40		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coefficient of V _O		l _O = 5 mA			-0.65		mV/°C
RR	Ripple Rejection		f = 120 Hz, 8 V ≤ V	$V_{\rm I} \le 18 {\rm V}, {\rm T}_{\rm J} = 25^{\circ}{\rm C}$	41	80		dB
V _D	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

1. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests.

2. Power dissipation $P_D \leq 0.75$ W.

Electrical Characteristics (MC78L06A)

 $V_I = 12 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}C \leq T_J \leq 125^{\circ}C, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Paramete	er		Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		5.75	6.0	6.25	V
A \ /	D Line Regulation ⁽³⁾		т огоо	$8.5 \text{ V} \le \text{V}_{\text{I}} \le 20 \text{ V}$		64	175	mV
ΔV_O			T _J = 25°C	$9 \text{ V} \le \text{V}_{\text{I}} \le 20 \text{ V}$		54	125	mV
A) /	Lood Degulation ⁽³⁾		T 05%C	1 mA ≤ I _O ≤ 100 mA		12.8	80.0	mV
ΔV_O	Load Regulation ⁽³⁾		T _J = 25°C	1 mA ≤ I _O ≤ 70 mA		5.8	40.0	mV
M	Output Maltage		$8.5 \text{ V} \leq \text{V}_{\text{I}} \leq$	≤ 20 V, 1 mA ≤ I _O ≤ 40 mA	5.7		6.3	V
Vo	Output Voltage		$8.5 \text{ V} \le \text{V}_{\text{I}} \le \text{V}_{\text{MAX}}^{(4)}, 1 \text{ mA} \le \text{I}_{\text{O}} \le 70 \text{ mA}$		5.7		6.3	V
	Quiescent Current		T _J = 25°C				5.5	mA
Ι _Q			T _J = 125°C	;		3.9	6.0	mA
ΔI_Q	Quiescent Current	With Line	$9 \text{ V} \le \text{V}_1 \le 20 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	1 mA ≤ I _O s	≤ 40 mA			0.1	mA
V _N	Output Noise Voltage		T _A = 25°C,	$10 \text{ Hz} \le f \le 100 \text{ kHz}$		40		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coefficie	ent of V _O	of V_0 $I_0 = 5 \text{ mA}$			0.75		mV/°C
RR	Ripple Rejection		f = 120 Hz,	10 V \leq V _I \leq 20 V, T _J = 25°C	40	46		dB
VD	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

3. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 4. Power dissipation $P_D \le 0.75$ W.

Electrical Characteristics (MC78L08A)

 $V_I = 14 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, \text{ } C_I = 0.33 \text{ } \mu\text{F}, \text{ } C_O = 0.1 \text{ } \mu\text{F}, \text{ } \text{unless otherwise specified}.$

Symbol	Parameter		Conditions			Тур.	Max.	Unit
V _O	Output Voltage		$T_J = 25^{\circ}C$		7.7	8.0	8.3	V
ΔV _O L	Line Regulation ⁽⁵⁾		T 25%C	$10.5~V \leq V_{I} \leq 23~V$		10	175	mV
Δv _O			T _J = 25°C	$11~V \le V_I \le 23~V$		8	125	mV
41/	Load Regulation ⁽⁵⁾		T - 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		15	80	mV
ΔV_O		r_0 = 20 C	T _J = 25°C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		8	40	mV
	V _O Output Voltage		$10.5V \le V_1 \le 23V$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	7.6		8.4	V
vo			$10.5V \le V_I \le V_{MAX}^{(6)}$	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	7.6		8.4	V
Ι _Q	Quiescent Current		T _J = 25°C			2.0	5.5	mA
ΔI_Q	Quiescent Current	With Line	$11 \text{ V} \leq \text{V}_{\text{I}} \leq 23 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volta	ge	$T_A = 25^{\circ}C$, 10 Hz \leq f	≤100 kHz		60		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coefficient of V _O		l _O = 5 mA			-0.8		mV/°C
RR	Ripple Rejection		f = 120 Hz, 11 V \leq V _I	\leq 21 V, T _J = 25°C	39	70		dB
V _D	Dropout Voltage		Т _Ј = 25°С			1.7		V

Notes:

5. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests.

6. Power dissipation $P_D \le 0.75$ W.

Electrical Characteristics (MC78L12A / LM78L12A)

 $V_I = 19 \text{ V}, \text{ } I_O = 40 \text{ mA}, \text{ } -40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, \text{ } C_I = 0.33 \text{ } \mu\text{F}, \text{ } C_O = 0.1 \text{ } \mu\text{F}, \text{ } \text{unless otherwise specified}.$

Symbol	Parame	eter	Conditions			Тур.	Max.	Unit
Vo	Output Voltage		T _J = 25°C		11.5	12.0	12.5	V
	Line Regulation (7	7)	T 25%C	$14.5~V \leq V_{I} \leq 27~V$		20	250	mV
ΔV_O	Line Regulation V	,	T _J = 25°C	$16 \text{ V} \le \text{V}_{I} \le 27 \text{ V}$		15	200	mV
A) /		Load Regulation ⁽⁷⁾	T - 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		20	100	mV
ΔV_O		. ,	$T_J = 25^{\circ}C$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		10	50	mV
V	Output Voltage		14.5 V \le V _I \le 27 V	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	11.4		12.6	V
Vo			$14.5 V \le V_I \le V_{MAX}^{(8)}$	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	11.4		12.6	V
Ι _Q	Quiescent Current		$T_J = 25^{\circ}C$			2.1	6.0	mA
ΔI_Q	Quiescent	With Line	$16 \text{ V} \leq \text{V}_{\text{I}} \leq 27 \text{ V}$				1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volt	age	$T_A = 25^{\circ}C$, 10 Hz $\leq f$	≤ 100 kHz		80		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coefficient of VO		l _O = 5 mA			-1.0		mV/°C
RR	Ripple Rejection		f = 120 Hz, 15 V \leq V _I	≤ 25 V, T _J = 25°C	37	65		dB
VD	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests.

8. Power dissipation $P_D \le 0.75$ W.

Electrical Characteristics (MC78L15A)

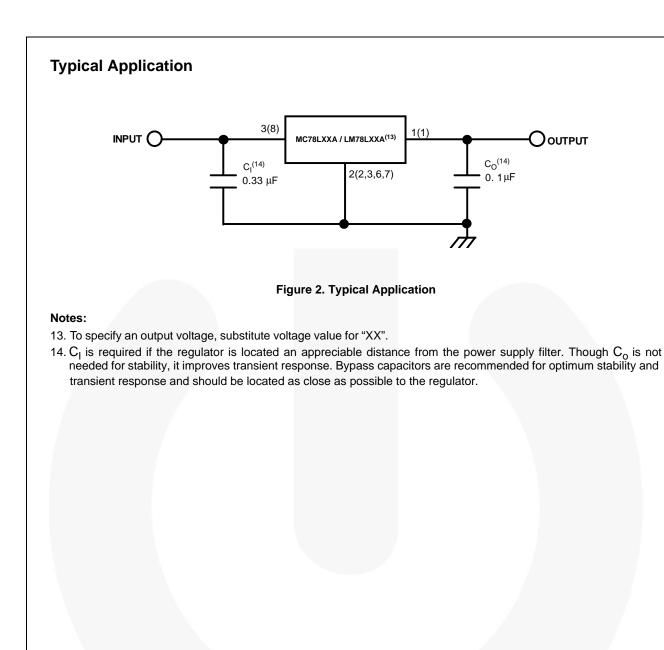
 $V_I = 23 \text{ V}, \text{ I}_O = 40 \text{ mA}, \text{ -}40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, \text{ C}_I = 0.33 \text{ }\mu\text{F}, \text{ C}_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

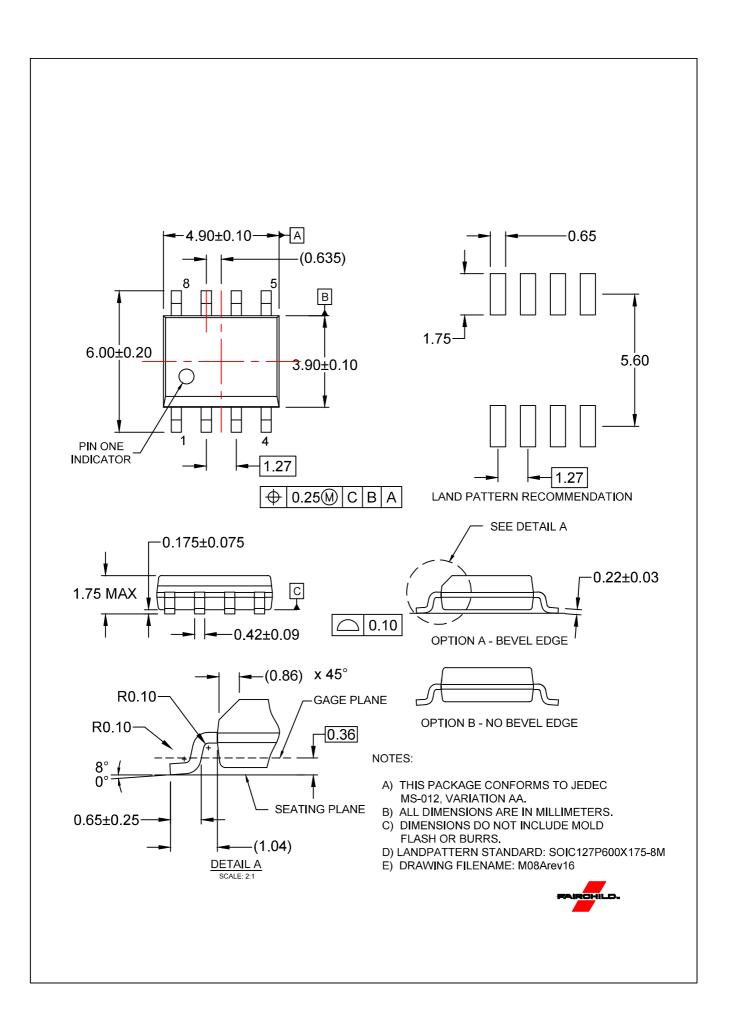
Symbol	Parame	ter	Conditions			Min.	Тур.	Max.	Unit
Vo	Output Voltage		T _J = 25°C			14.4	15.0	15.6	V
41/	V _O Line Regulation ⁽⁹⁾		T _{.1} = 25°C	17.5 V ≤ V _I ≤	30 V		25	300	mV
Δv _O			$T_{\rm J} = 25 {\rm C}$	$20 \text{ V} \le \text{V}_1 \le 30 \text{ V}$			20	250	mV
A\/	Load Regulation ⁽⁹⁾		T _{.1} = 25°C	$1 \text{ mA} \le I_0 \le 1$	00 mA		25	150	mV
ΔV_{O}			$1_{j} = 25 C$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$			12	75	mV
V	Output Voltage		$17.5 \text{ V} \le \text{V}_{\text{I}} \le 30 \text{ V}$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		14.25		15.75	V
Vo			$17.5 \text{ V} \le \text{V}_{\text{I}} \le \text{V}_{\text{MAX}}^{(10)}$	1 mA ≤ I _O ≤ 7	70 mA	14.25		15.75	V
Ι _Q	Quiescent Current		$T_J = 25^{\circ}C$				2.1	6.0	mA
ΔI_Q	Quiescent	With Line	$20 \text{ V} \leq \text{V}_{\text{I}} \leq 30 \text{ V}$					1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$					0.1	mA
V _N	Output Noise Voltage		$T_A = 25^{\circ}C$, 10 Hz \leq f \leq	100 kHz			90		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coefficient of V _O		I _O = 5 mA				-1.3		mV/°C
RR	Ripple Rejection		f = 120 Hz, 18.5 V \leq V _I	≤28.5 V, T _J =	25°C	34	60		dB
V _D	Dropout Voltage		T _J = 25°C				1.7		V

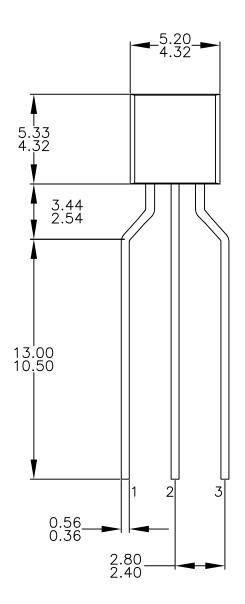
Notes:

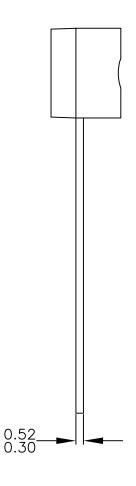
9. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests.

10. Power dissipation $P_D \le 0.75$ W.





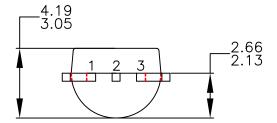


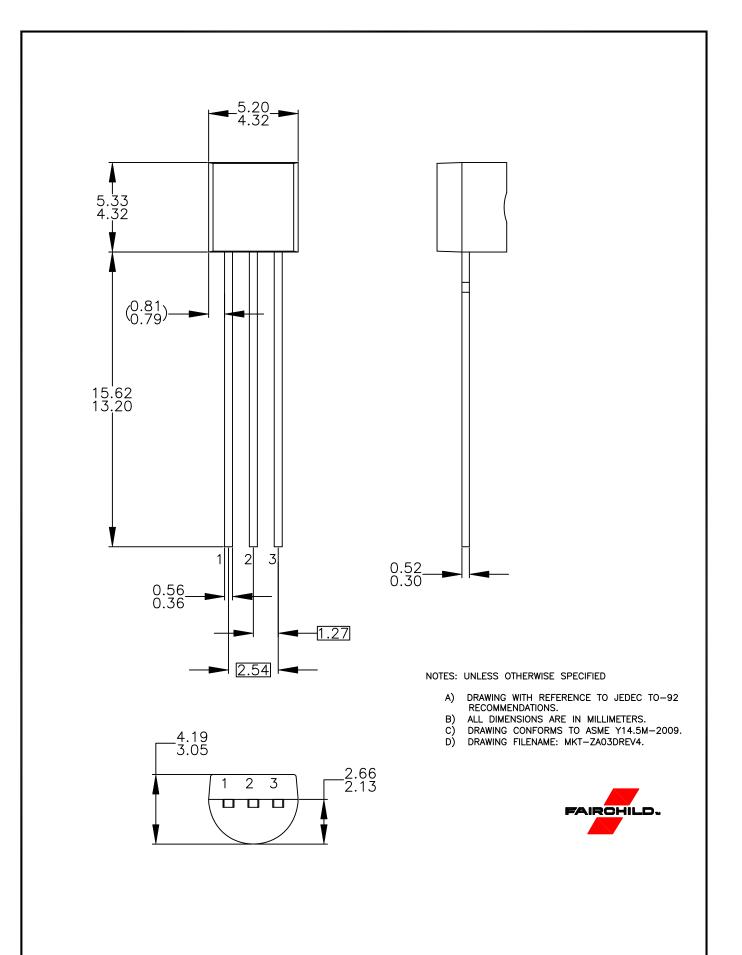


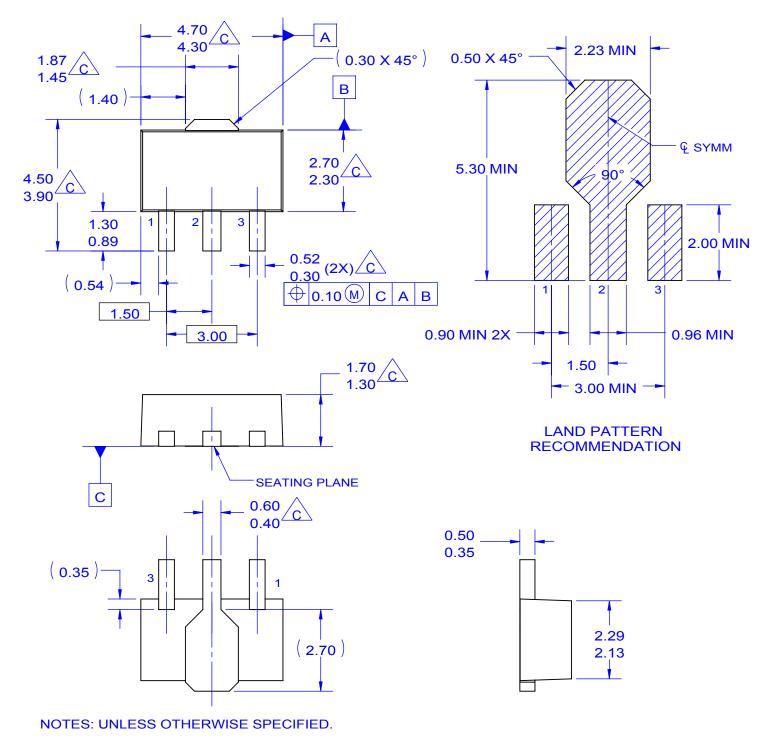
NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZA03FREV3. FAIRCHILD SEMICONDUCTOR. Α.

- В. С. D. Е.







A. REFERENCE TO JEDEC TO-243 VARIATION AA.

B. ALL DIMENSIONS ARE IN MILLIMETERS.

 C DOES NOT COMPLY JEDEC STANDARD VALUE.
D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSION.
E. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.

F. DRAWING FILE NAME: MA03CREV3

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor haves against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death a

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: <u>MC78L06ACPXA</u> MC78L06ACP