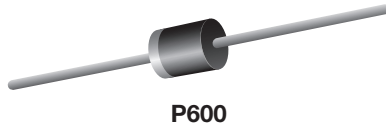


TRANSZORB® Transient Voltage Suppressors



FEATURES

- P600 glass passivated chip junction
- Available in uni-directional polarity only
- 5000 W peak pulse power capability with a 10/1000 μ s waveform, repetitive rate (duty cycle): 0.01 %
- Excellent clamping capability
- Very fast response time
- Low incremental surge resistance
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lighting on ICs, MOSFET, signal lines of sensor units for consumer, computer, industrial, automotive, and telecommunication.

MECHANICAL DATA

Case: Molded epoxy body over passivated junction
Molding compound meets UL 94 V-0 flammability rating
Base P/N-E3 - RoHS compliant, commercial grade
Base P/NHE3 - RoHS compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

PRIMARY CHARACTERISTICS	
V_{WM}	8.5 V to 188 V
V_{BR}	9.4 V to 231 V
P_{PPM}	5000 W
P_D	8.0 W
I_{FSM}	500 A
T_J max.	175 °C
Polarity	Uni-directional
Package	P600

MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Peak pulse power dissipation with a 10/1000 μ s waveform ⁽¹⁾	P_{PPM}	5000	W
Peak pulse current with a 10/1000 μ s waveform ⁽¹⁾	I_{PPM}	See next table	A
Power dissipation on infinite heatsink at $T_L = 75$ °C (fig. 5)	P_D	8.0	W
Peak forward surge current 8.3 ms single half sine-wave (fig. 5)	I_{FSM}	600	A
Instantaneous forward voltage at 100 A ⁽²⁾	V_F	3.5	V
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +175	°C

Notes

⁽¹⁾ Non-repetitive current pulse, per fig. 3 and derated above $T_A = 25$ °C per fig. 2

⁽²⁾ Measured 8.3 ms single half sine-wave or equivalent square wave, duty cycle = 4 pulses per minute maximum



ELECTRICAL CHARACTERISTICS (JEDEC REGISTERED DATA) (T_A = 25 °C unless otherwise noted)

Table with 9 columns: DEVICE TYPE, BREAKDOWN VOLTAGE VBR AT IT (1) (V) (MIN., MAX.), TEST CURRENT IT (mA), STAND-OFF VOLTAGE VWM (V), MAXIMUM REVERSE LEAKAGE AT VWM ID (uA), MAXIMUM PEAK PULSE CURRENT IPPM (2) (A), MAXIMUM CLAMPING VOLTAGE AT IPPM VC (V), MAXIMUM TEMP. COEFFICIENT OF VBR (%/°C). Rows list various diode models from 5KP8.5A to 5KP188A.

Notes

- (1) Pulse test: tp ≤ 50 ms
(2) Surge current waveform per fig. 3 and derate per fig. 2
(3) All terms and symbols are consistent with ANSI/IEEE CA62.35



ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
5KP8.5A-E3/54	2.776	54	800	13" diameter paper tape and reel
5KP8.5AHE3/54 (1)	2.776	54	800	13" diameter paper tape and reel

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

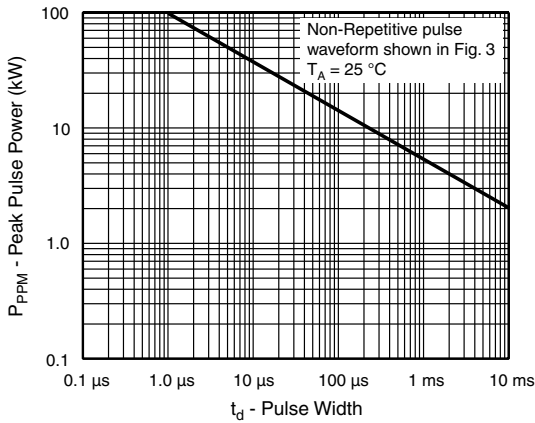


Fig. 1 - Peak Pulse Power Rating Curve

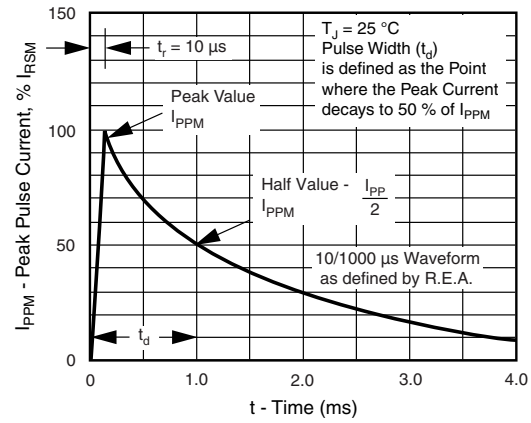


Fig. 3 - Pulse Waveform

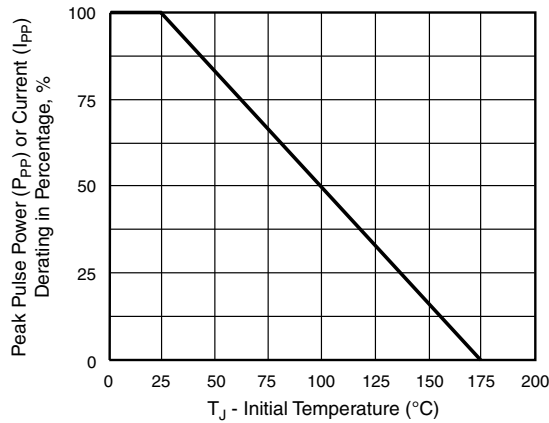


Fig. 2 - Pulse Power or Current vs. Initial Junction Temperature

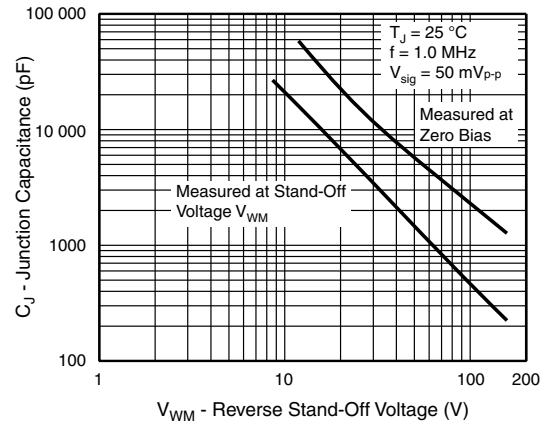


Fig. 4 - Typical Junction Capacitance

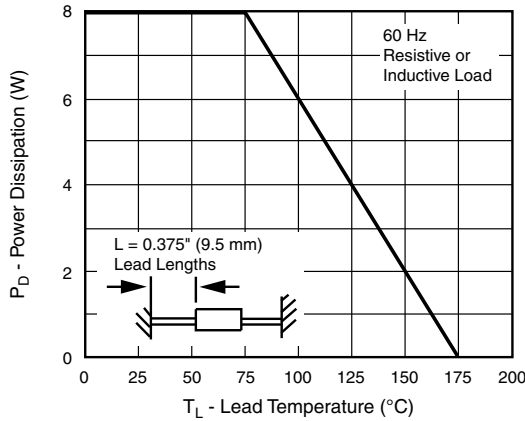


Fig. 5 - Power Derating Curve

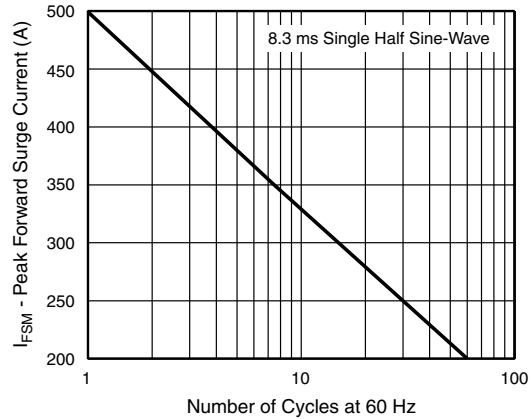
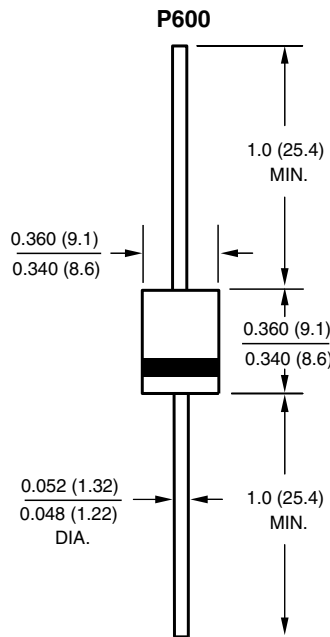


Fig. 6 - Maximum Non-Repetitive Forward Surge Current

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



APPLICATION NOTES

The 5KP series of high power transient voltage suppressors were designed to be used on the output of switching power supplies. These devices may be used to replace crowbar circuits. Both the 5 % and 10 % voltage tolerances are referenced to the power supply output voltage level.

They are able to withstand high levels of peak current while allowing a circuit breaker to trip or a fuse blow before

shorting. This will enable the user to reset the breaker or replace the fuse and continue operation. For this type operation, it is recommended that a sufficient mounting surface be used for dissipating the heat generated by the Transient Voltage Suppressor during the transient or over-voltage condition.



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