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August 2014

ES2A - ES2D Fast Rectifiers

Features

- For Surface Mount Applications
- · Glass-Passivated Junction
- · Low-Profile Package
- · Easy Pick and Place
- · Built-in Strain Relief
- · Superfast Recovery Times for High Efficiency



Ordering Information

Part Number	Top Mark	Package	Packing Method
ES2A	ES2A	DO-214AA (SMB)	Tape and Reel
ES2B	ES2B	DO-214AA (SMB)	Tape and Reel
ES2C	ES2C	DO-214AA (SMB)	Tape and Reel
ES2D	ES2D	DO-214AA (SMB)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value				Unit
	i didilictei	ES2A	ES2B	ES2C	ES2D	Oilit
V_{RRM}	Maximum Repetitive Reverse Voltage	50	100	150	200	V
I _{F(AV)}	Average Rectified Forward Current, .375" Lead Length at T _L = 115°C		2	.0		Α
I _{FSM}	Non-Repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine Wave	50				Α
T _{STG}	Storage Temperature Range	-55 to +150			°C	
T_J	Operating Junction Temperature Range	-55 to +150			°C	

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
P _D	Power Dissipation	1.66	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽¹⁾	75	°C/W
$R_{\theta JL}$	Thermal Resistance, Junction to Lead ⁽¹⁾	20	°C/W

Note:

1. Device mounted on FR-4 PCB 0.013 mm.

Electrical Characteristics

Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Value			Unit	
Syllibol			ES2A	ES2B	ES2C	ES2D	- Onit
V_{F}	Maximum Forward Voltage	I _F = 2.0 A		0.	90		V
t _{rr}	Reverse Recovery Time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A},$ $I_{RR} = 0.25 \text{ A}$		2	0		ns
- 1-	Maximum Reverse Current	T _A = 25°C		1	0		μA
I _R	at Rated V _R	T _A = 100°C		3	50	•	μΑ
C _T	Total Capacitance	$V_R = 4.0 \text{ V}, f = 1.0 \text{ MHz}$		1	8		pF

Typical Performance Characteristics

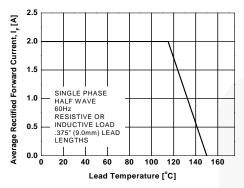


Figure 1. Forward Current Derating Curve

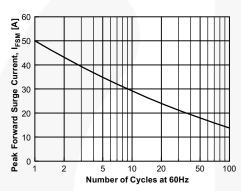


Figure 3. Non-Repetitive Surge Current

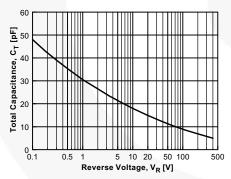
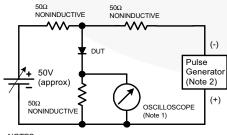
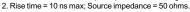


Figure 5. Total Capacitance



1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf. 2. Rise time = 10 ns max; Source impedance = 50 ohms.



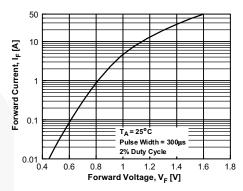


Figure 2. Foward Voltage Characteristics

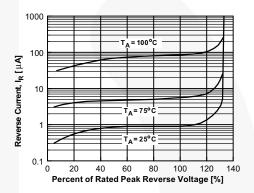


Figure 4. Reverse Current vs. Reverse Voltage

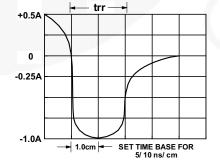


Figure 6. Reverse Recovery Time Characteristic and Test Circuit Diagram

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Physical Dimension

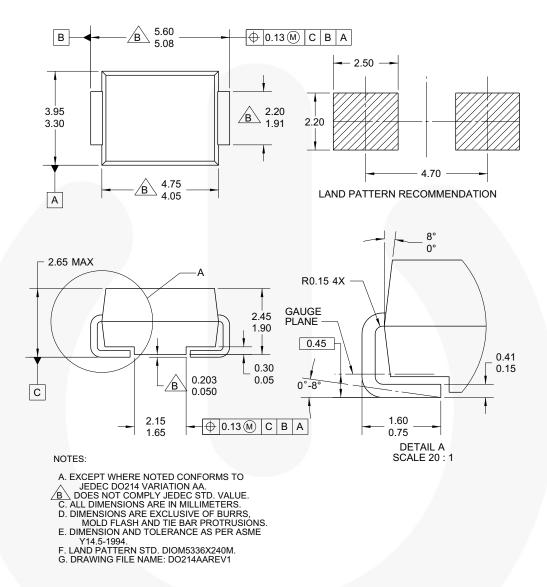


Figure 7. 2-LEAD, SMB, JEDEC DO-214, VARIATION AA (ACTIVE)

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Definition of Terms					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.			
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