

## P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
- 30	0.150 at $V_{GS} = - 10$ V	- 2.2
	0.260 at $V_{GS} = - 4.5$ V	- 1.6

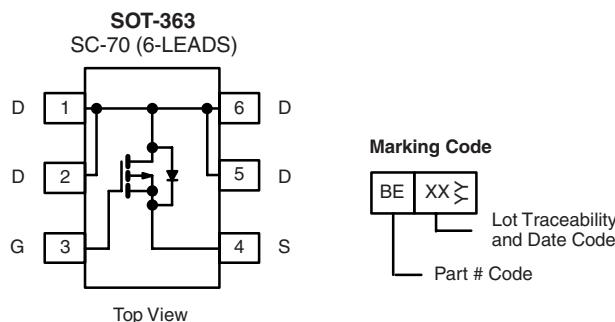
### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs: 1.8 V Rated
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



### APPLICATIONS

- Load Switches
  - Notebook PC
  - Servers



**Ordering Information:** Si1433DH-T1-E3 (Lead (Pb)-free)  
Si1433DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	- 30		V	
Gate-Source Voltage		$V_{GS}$	$\pm 20$			
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$T_A = 25$ °C	$I_D$	- 2.2	- 1.9	A	
	$T_A = 85$ °C		- 1.7	- 1.4		
Pulsed Drain Current		$I_{DM}$	- 8			
Continuous Diode Current (Diode Conduction) <sup>a</sup>		$I_S$	- 1.4	- 0.9		
Maximum Power Dissipation <sup>a</sup>	$T_A = 25$ °C	$P_D$	1.45	0.95	W	
	$T_A = 85$ °C		0.75	0.5		
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 5$ s	$R_{thJA}$	65	85	°C/W
	Steady State		105	130	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	38	48	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

**SPECIFICATIONS**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

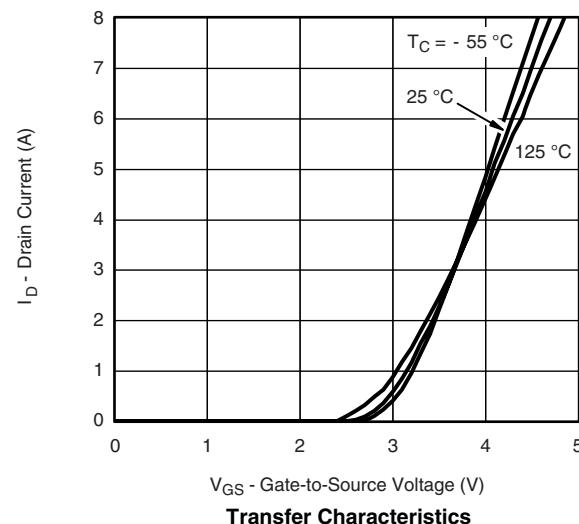
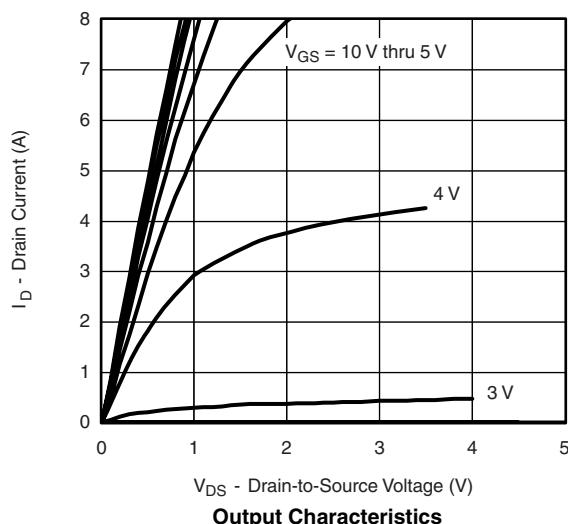
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$ , $I_D = -100\ \mu\text{A}$	- 1		- 3	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\ \text{V}$ , $V_{GS} = \pm 8\ \text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -16\ \text{V}$ , $V_{GS} = 0\ \text{V}$			- 1	$\mu\text{A}$
		$V_{DS} = -16\ \text{V}$ , $V_{GS} = 0\ \text{V}$ , $T_J = 85^\circ\text{C}$			- 5	
On-State Drain Current <sup>a</sup>	$I_{D(\text{on})}$	$V_{DS} = -5\ \text{V}$ , $V_{GS} = -4.5\ \text{V}$	- 4			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(\text{on})}$	$V_{GS} = -10\ \text{V}$ , $I_D = -2.2\ \text{A}$		0.120	0.150	$\Omega$
		$V_{GS} = -4.5\ \text{V}$ , $I_D = -1.6\ \text{A}$		0.210	0.260	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -10\ \text{V}$ , $I_D = -2.2\ \text{A}$		4		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -1.2\ \text{A}$ , $V_{GS} = 0\ \text{V}$		- 0.85	- 1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = -15\ \text{V}$ , $V_{GS} = -4.5\ \text{V}$ , $I_D = -2.2\ \text{A}$		3.1	5	nC
Gate-Source Charge	$Q_{gs}$			1.0		
Gate-Drain Charge	$Q_{gd}$			1.6		
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DD} = -15\ \text{V}$ , $R_L = 15\ \Omega$ $I_D \equiv -1\ \text{A}$ , $V_{GEN} = -10\ \text{V}$ , $R_g = 6\ \Omega$		11	17	ns
Rise Time	$t_r$			17	26	
Turn-Off Delay Time	$t_{d(\text{off})}$			18	27	
Fall Time	$t_f$			13	20	

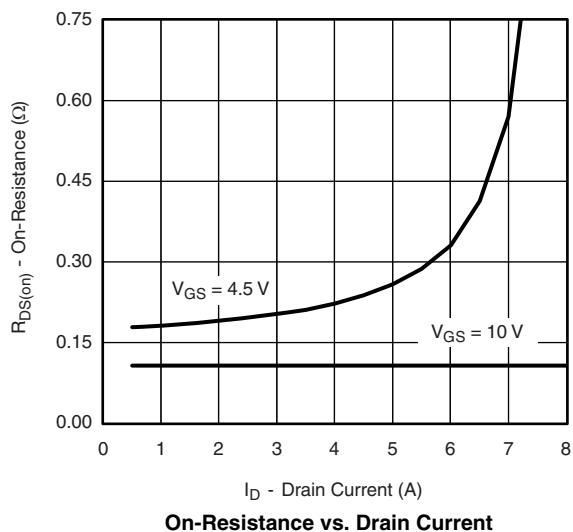
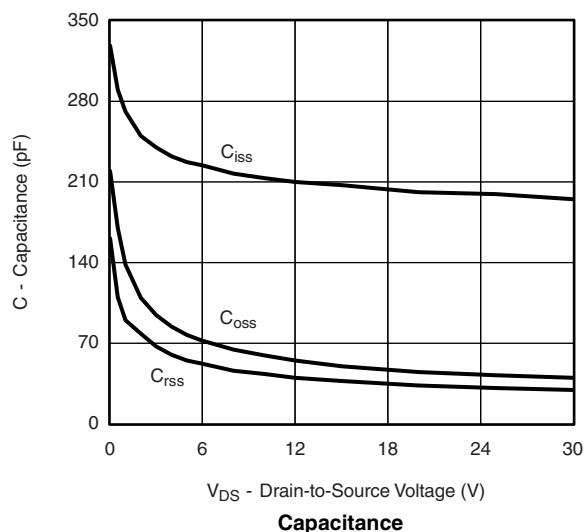
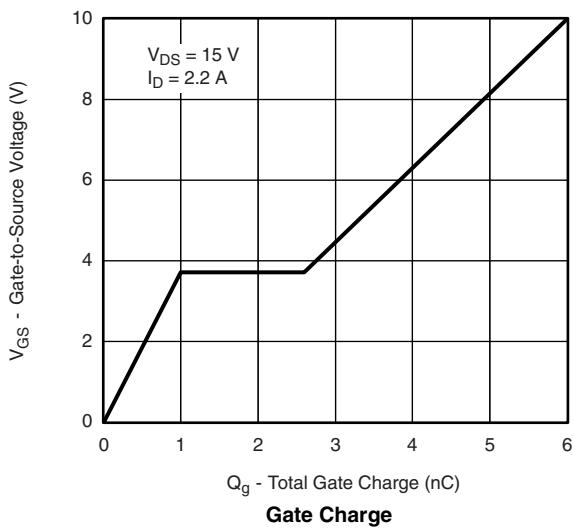
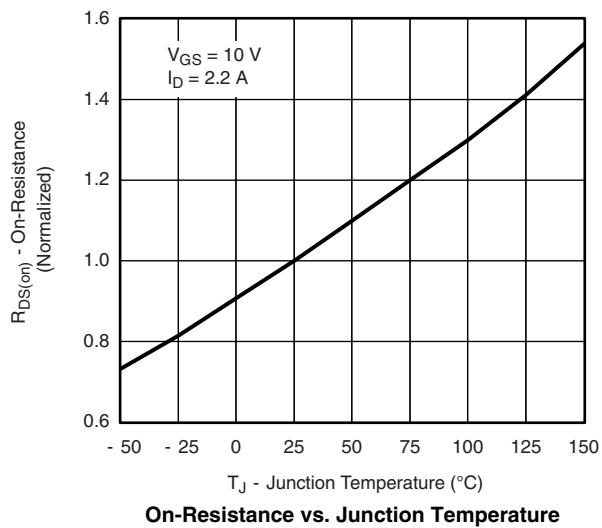
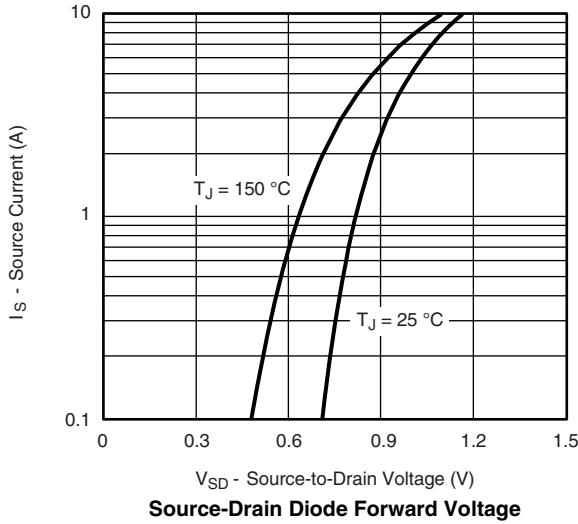
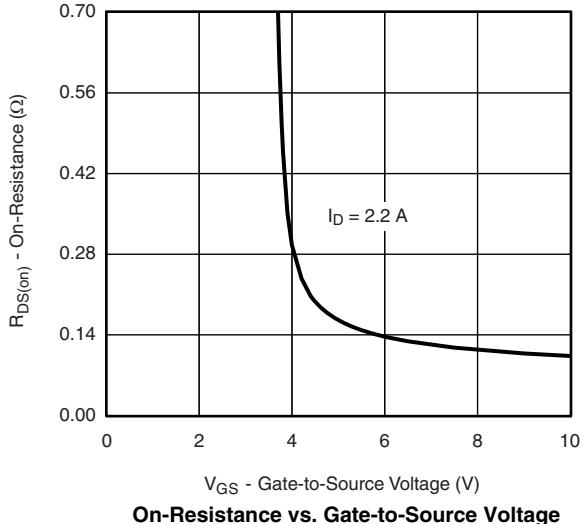
## Notes:

a. Pulse test; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\ \%$ .

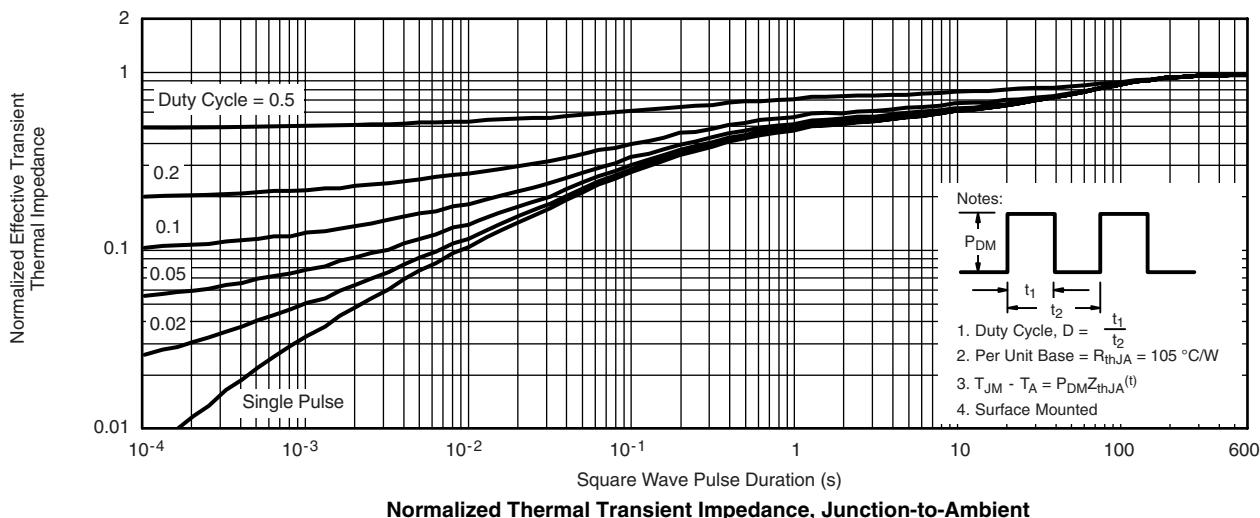
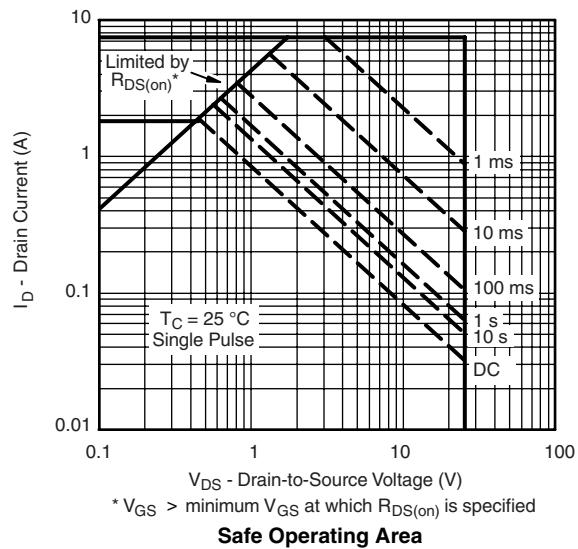
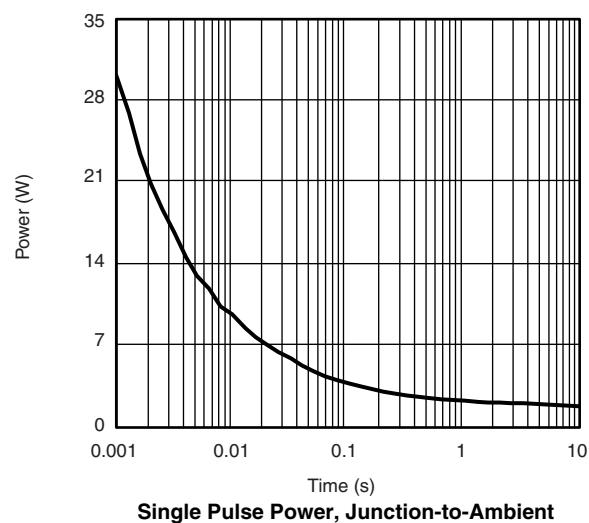
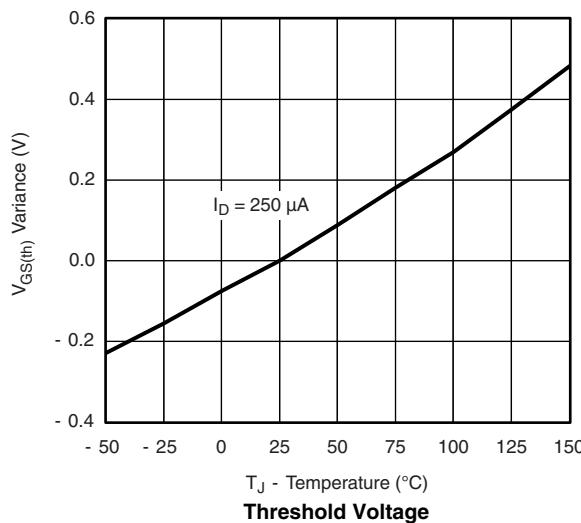
b. Guaranteed by design, not subject to production testing.

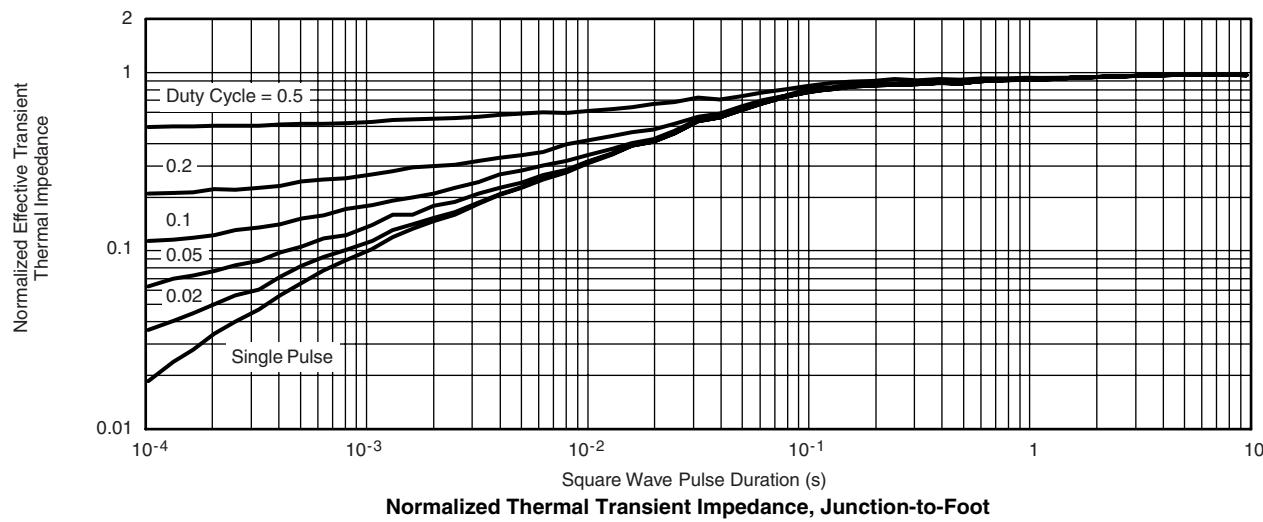
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS**  $25^\circ\text{C}$ , unless otherwise noted

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**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

**On-Resistance vs. Junction Temperature**

**Source-Drain Diode Forward Voltage**

**On-Resistance vs. Gate-to-Source Voltage**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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