

## 4-Bit Level Shifting Buffer/Transceiver with Configurable Dual Supply Voltage with Advanced Package Solution

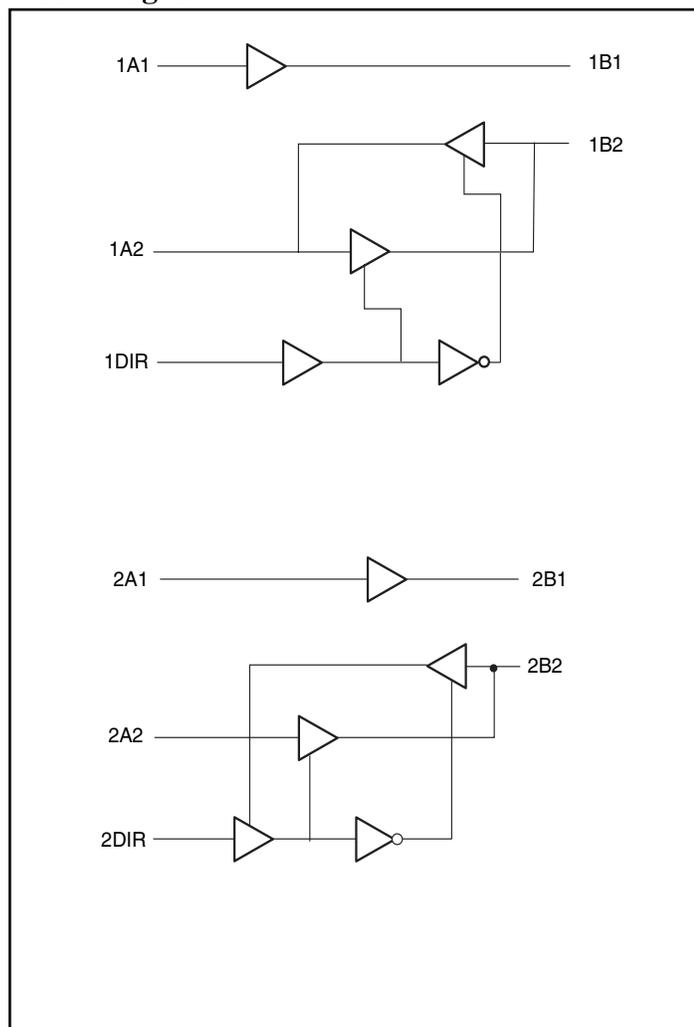
### Features

- Operation Voltage:  $1.65V \leq V_{CCA} \leq 3.6V$   
 $2.3V \leq V_{CCB} \leq 5.5V$   
 $V_{CCA} \leq V_{CCB}$
- High Speed:  $t_{pd} = 5ns$  typical into  $30pF$  @  $3V V_{CC}$
- Power down high-impedance inputs and outputs
- High output drive:  $\pm 12mA$  at  $3V V_{CCA}/V_{CCB}$   
 $\pm 24mA$  at  $5V V_{CCB}$
- Industrial operation at  $-40^{\circ}C$  to  $+85^{\circ}C$
- Packaging (Pb-free & Green available):  
 – 16-pin TDFN (ZJ)

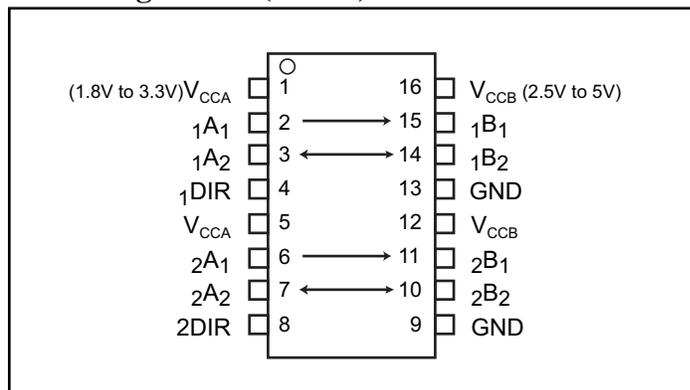
### Description

Pericom Semiconductor's PI74STX4G4245, a 4-bit noninverting buffer/transceiver, contains two separate supply rails: A port ( $V_{CCA}$ ), set to operate from 1.8V to 3.3V; B port ( $V_{CCB}$ ), set to operate from 2.5V to 5V. The xA1 to xB1 translation is configured as an unidirectional buffer; the translation between xA2 and xB2 is bidirectional and its direction is controlled by the xDIR pin. The xDIR pin is supplied by  $V_{CCB}$ .

### Block Diagram



### Pin Configuration (TDFN)



### Function Table

Input		Output
xDIR	DATA	
L	xB2	xA2
H	xA2	xB2
X	xA1	xB1

### Pin Description

Pin Name	Description
xDIR	Direction Control
xA1	Data Input
xB1	Data Output
xA2, xB2	Data Input/Output
$V_{CCA}, V_{CCB}$	Power Supply

### Maximum Ratings

(Absolute maximum ratings over operating free-air temperature range from  $V_{CCB}$  at 2.5V &  $V_{CCA}$  at 1.2V, unless otherwise noted)

Supply voltage range: $V_{CCA}$ .....	-0.5V to +6V	DC Input Diode Current, $I_{IK}$ ( $V_O < 0$ ).....	-50mA
$V_{CCB}$ .....	-0.5V to +6V	DC Output Diode Current, $I_{OK}$ ( $V_O < 0$ ).....	-50mA
Input Voltage Range, $V_I$ :		DC Output Current, ( $I_{OUT}$ ) .....	$\pm 50$ mA
Input xA1 <sup>(1)</sup> .....	-0.5V to +6V	DC $V_{CC}$ or GND Current ( $I_{CC}/I_{GND}$ ).....	$\pm 100$ mA
I/O xA2 <sup>(1,2)</sup> .....	-0.5V to $V_{CCA}+0.5V$	Storage temperature range, $T_{STG}$ .....	-65°C to 150°C
I/O xB2 <sup>(1,2)</sup> .....	-0.5V to $V_{CCB}+0.5V$	Junction Lead Temperature ( $I_{OS}$ ) .....	260°C
Output Voltage Range, $V_O$		Power Dissipation: MSOP .....	200mW
Output B1 <sup>(1,2)</sup> ...	-0.5V to $V_{CCB} +0.5V$		

#### Notes:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

1. The input negative voltage and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. This value is limited to 6V.

### Recommended Operating Conditions for A Port ( $V_{CCA}$ )<sup>(1)</sup>

Parameter	Description	Test Conditions	Min.	Max.	Units
$V_{CCA}$	Supply Voltage		1.65	3.6	V
$V_{IH}$	High-level input voltage	$V_{CCA} = 1.65V$ to $1.95V$	$0.7 \times V_{CCA}$		
		$V_{CCA} = 2.3V$ to $3.6V$	$0.7 \times V_{CCA}$		
$V_{IL}$	Low-level input voltage	$V_{CCA} = 1.65$ to $1.95V$		$0.3 \times V_{CCA}$	
		$V_{CCA} = 2.3V$ to $3.6V$		$0.3 \times V_{CCA}$	
$V_{IA}$	Input Voltage		0	$V_{CCA}$	
$V_{OA}$	Output Voltage		0	$V_{CCA}$	
$I_{OH}$	High-level Output Current	$V_{CCA} = 1.65 V$ to $1.95V$		-4	mA
		$V_{CCA} = 2.3V$ to $2.7V$		-8	
		$V_{CCA} = 3V$ to $3.6V$		-12	
$I_{OL}$	Low-level Output Current	$V_{CCA} = 1.65V$ to $1.95V$		4	
		$V_{CCA} = 2.3V$ to $2.7V$		8	
		$V_{CCA} = 3V$ to $3.6V$		12	
$\Delta t/\Delta V$	Input transition rise or fall rate	$V_{CCA} = 1.65V$ to $1.95V$		20	ns/V
		$V_{CCA} = 2.3V$ to $3.6V$		10	
$T_A$	Operation free-air temperature		-40	85	°C

#### Notes:

1. To ensure proper device operation, all unused inputs must be held at  $V_{CCA}$  or GND.

**Recommended Operating Conditions for B Port (  $V_{CCB}$  )<sup>(1)</sup>**

Parameters	Description	Test Conditions	Min.	Max.	Units
$V_{CCB}$	Supply Voltage		2.3	5.5	V
$V_{IH}$	High-level input voltage	Data Inputs	$V_{CCB} = 2.3V$ to $5.5V$	$0.7 \times V_{CCB}$	
		DIR pin			
$V_{IL}$	Low-level input voltage	Data Inputs	$V_{CCB} = 2.3V$ to $5.5V$	$0.3 \times V_{CCB}$	
		DIR pin			
$V_{IB}$	Input Voltage		0	$V_{CCB}$	
$V_{OB}$	Output Voltage		0	$V_{CCB}$	
$I_{OH}$	High-level Output Current	$V_{CCB} = 2.3V$ to $2.7V$		-8	mA
		$V_{CCB} = 3V$ to $3.6V$		-12	
		$V_{CCB} = 4.5V$ to $5.5V$		-24	
$I_{OL}$	Low-level Output Current	$V_{CCB} = 2.3V$ to $2.7V$		8	
		$V_{CCB} = 3V$ to $3.6V$		12	
		$V_{CCB} = 4.5V$ to $5.5V$		24	
$\Delta t/\Delta V$	Input transition rise or fall rate	$V_{CCB} = 2.3V$ to $5.5V$		10	m/V
$T_A$	Operation free-air temperature		-40	85	°C

**Notes:**

- To ensure proper device operation, all unused inputs must be held at  $V_{CCB}$  or GND.

**Electrical Characteristics** (Over recommended operating free-air temperature range)

Parameters		Test Conditions	V <sub>CCA</sub> <sup>(1)</sup>	V <sub>CCB</sub> <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units	
V <sub>OHA</sub> (B to A)		I <sub>OH</sub> = -100uA	1.65V to 3.6V	2.3V ≤ V <sub>CCB</sub> ≤ 5.5V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>	V <sub>CCB</sub> -0.1			V	
		I <sub>OH</sub> = -4mA	1.65V		1.3				
		I <sub>OH</sub> = -8mA	2.3V		1.8				
		I <sub>OH</sub> = -12mA	3.0V		2.4				
V <sub>OLA</sub> (B to A)		I <sub>OH</sub> = 100uA	1.65V to 3.6V						0.1
		I <sub>OH</sub> = 4mA	1.65V						0.24
		I <sub>OH</sub> = 8mA	2.3V						0.3
		I <sub>OH</sub> = 12mA	3.0V						0.4
V <sub>OHB</sub> (A to B)		I <sub>OH</sub> = -100uA	1.65V ≤ V <sub>CCA</sub> ≤ 3.6V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>	2.3V to 5.5V	V <sub>CCB</sub> -0.1				
		I <sub>OH</sub> = -8mA		2.3V	1.8				
		I <sub>OH</sub> = -12mA		3.0V	2.4				
		I <sub>OH</sub> = -24mA		4.5V	3.8				
V <sub>OLB</sub> (A to B)		I <sub>OH</sub> = 100uA			2.3V to 5.5V			0.1	
		I <sub>OH</sub> = 8mA			2.3V			0.3	
		I <sub>OH</sub> = 12mA			3.0V			0.4	
		I <sub>OH</sub> = 24mA			4.5V			0.55	
I <sub>I</sub> <sup>(3)</sup>	V <sub>I</sub> = V <sub>CCA</sub> /V <sub>CCB</sub> or GND	1.65V to 3.6V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>	2.3V to 5.5V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>				±5		
I <sub>OFF</sub>	V <sub>I</sub> or V <sub>O</sub> = V <sub>CCB</sub> or GND	0V	0 to 5.5V				±10		
	V <sub>I</sub> or V <sub>O</sub> = V <sub>CCA</sub> or GND	0 to 3.6V	0V				±10		
I <sub>CCA</sub>	V <sub>I</sub> = V <sub>CCA</sub> or GND, I <sub>O</sub> = 0	1.65V to 3.6V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>	2.3 to 5.5V and V <sub>CCB</sub> ≥ V <sub>CCA</sub>				10		
I <sub>CCB</sub>	V <sub>I</sub> = V <sub>CCB</sub> or GND, I <sub>O</sub> = 0						10		
C <sub>I</sub>	A1, DIR	V <sub>I</sub> = V <sub>CCA</sub> or GND	1.8V	3.3V		4.5			
C <sub>O</sub>	B1	V <sub>O</sub> = V <sub>CCB</sub> or GND	1.8V	3.3V		4.5			
C <sub>IO</sub>	A2, B2	V <sub>I</sub> or V <sub>O</sub> = V <sub>CCA</sub> /V <sub>CCB</sub> or GND	1.8V	3.3V		6.0			

**Notes:**

- When 1.65 ≤ V<sub>CCA</sub> ≤ 1.95V, V<sub>CCB</sub> can not be greater than 3.6V
- All Typical values are at T<sub>A</sub> = 25°C
- For I/O ports, the parameter I<sub>I</sub> includes the output leakage current.

**Switching Characteristics for  $V_{CCA} = 1.8V \pm 0.15V$**

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 2.5V \pm 0.2V$		$V_{CCB} = 3.3V \pm 0.3V$		Units
			Min.	Max.	Min.	Max.	
$t_{PHL}$	A	B	1.0	8	1.0	8	ns
$t_{PLH}$			1.0	8	1.0	8	
$t_{PHL}$	B	A	1.0	7	1.0	7	
$t_{PLH}$			1.0	7	1.0	7	
$t_{SK(O)}$				1.5		1	

**Switching Characteristics for  $V_{CCA} = 2.5V \pm 0.2V$**

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 2.5V \pm 0.2V$		$V_{CCB} = 3.3V \pm 0.3V$		$V_{CCB} = 5V \pm 0.5V$		Units
			Min.	Max.	Min.	Max.	Min.	Max.	
$t_{PHL}$	A	B	1.0	7	1.0	6.5	1.0	6	ns
$t_{PLH}$			1.0	7	1.0	6.5	1.0	6	
$t_{PHL}$	B	A	1.0	6	1.0	6	1.0	5	
$t_{PLH}$			1.0	6	1.0	6	1.0	5	
$t_{SK(O)}$				1.5		1		1	

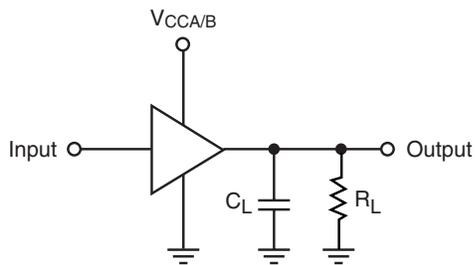
**Switching Characteristics for  $V_{CCA} = 3.3V \pm 0.3V$**

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 3.3V \pm 0.3V$		$V_{CCB} = 5V \pm 0.5V$		Units
			Min.	Max.	Min.	Max.	
$t_{PHL}$	A	B	1.0	5	1.0	5	ns
$t_{PLH}$			1.0	5	1.0	5	
$t_{PHL}$	B	A	1.0	5	1.0	5	
$t_{PLH}$			1.0	5	1.0	5	
$t_{SK(O)}$				1		1	

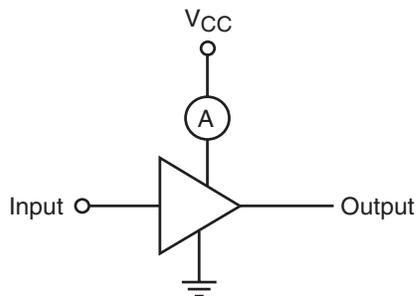
### AC Loading and Waveforms

$V_{CCA/B}$	$C_L$	$R_L$
$1.8 \pm 0.15V$	15pF	1K $\Omega$
$2.5V \pm 0.2V$	15pF	500 $\Omega$
3.0V to 3.6V	30pF	500 $\Omega$
$5V \pm 0.5V$	50pF	500 $\Omega$



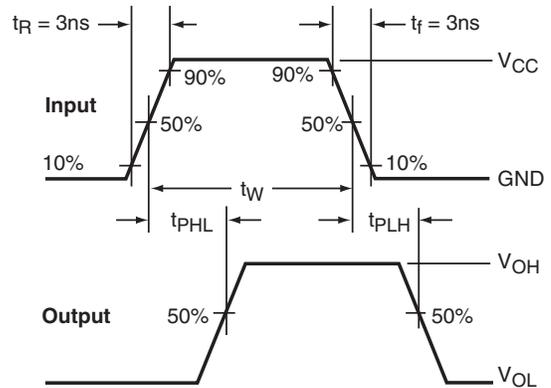
$C_L$  includes load and stray capacitance  
 Input PRR = 1.0 MHz;  $t_W = 500ns$

**Figure 1. AC Test Circuit**



Input = AC Waveform;  $t_r = t_f = 1.8ns$ ;  
 PRR = 10 MHz; Duty Cycle = 50%

**Figure 2. ICCD Test Circuit**



**Figure 3. AC Waveforms**

