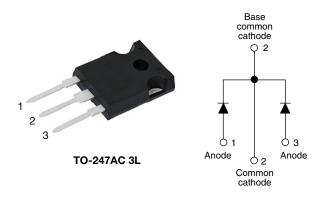
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RoHS

COMPLIANT

High Performance Schottky Rectifier, 2 x 20 A

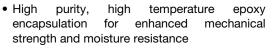


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PRIMARY CHARACTERISTICS								
I _{F(AV)} 2 x 20 A								
V _R	60 V							
V _F at I _F	0.62 V							
I _{RM} max.	100 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	13 mJ							
Package	TO-247AC 3L							
Circuit configuration	Common cathode							

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-MBR4060WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES U							
I _{F(AV)}	Rectangular waveform	40	А						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	1020	А						
V _F	20 A_{pk} , $T_J = 125 \ ^{\circ}C$ (per leg)	0.62	V						
ŢJ	Range	-55 to +150	°C						

VOLTAGE RATINGS						
PARAMETER	VS-MBR4060WT-N3	UNITS				
Maximum DC reverse voltage	V _R	<u>co</u>				
Maximum working peak reverse voltage	V _{RWM}	60	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS					UNITS		
Maximum averageper legforward currentper device			T_{C} = 108 °C, 50 % duty cycle, rectangular waveform		20			
		I _{F(AV)}			40			
Maximum peak one cycle non-repetitive surge current per leg		I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated	1020	A		
			10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	265			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1.5 A, L = 11.5 mH		13	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.5	А		

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VS-MBR4060WT-N3



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ELECTR	ICAL SPEC	CIFICATIONS

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.72	V			
	VFM (")	20 A	T _J = 125 °C	0.62	v			
Maximum instantaneous reverse current	I _{RM}	T _J = 25 °C	Rated DC voltage	1.0	mA			
		T _J = 125 °C	haled DC vollage	100	ШA			
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} , (test signal range 100 kHz to 1 MHz) 25 °C		720	pF			
Typical series inductance	Ls	Measured from top of terminal to mounting plane		7.5	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	°C		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	2.20			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	1.10	°C/W		
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	50			
Approximate weight				6	g		
Approximate weight				0.21	oz.		
Mounting torque	minimum			6 (5)	kgf ⋅ cm		
	maximum			12 (10)	(lbf · in)		
Marking device			Case style TO-247AC 3L	MBR40	060WT		



VS-MBR4060WT-N3

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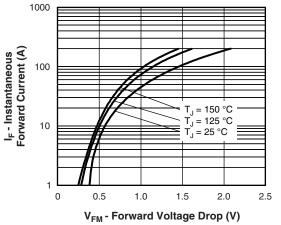


Fig. 1 - Maximum Forward Voltage Drop Characteristics

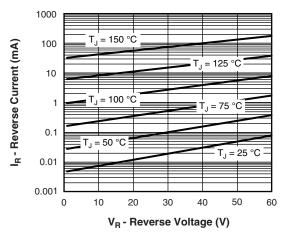


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

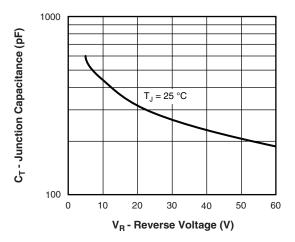


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

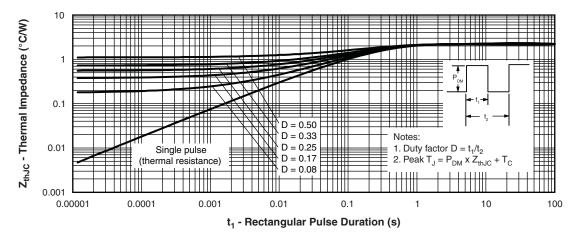


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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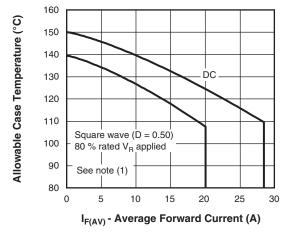


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

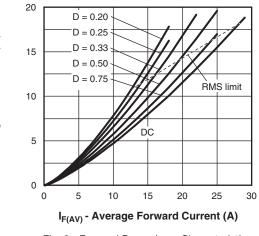
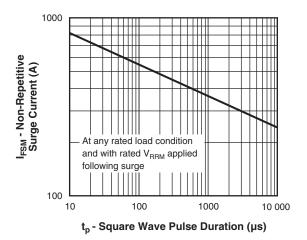
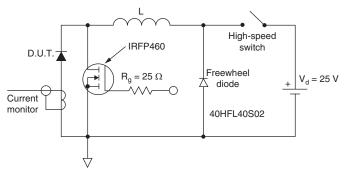


Fig. 6 - Forward Power Loss Characteristics



Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current





Note

 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ Pd_{REV} = \mbox{inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = 80 \ \% \mbox{ rated } V_R \end{array}$

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ORDERING INFORMATION TABLE

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-MBR4060WT-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?96138						
Part marking information	www.vishay.com/doc?95007					



Vishay Semiconductors

TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDOL	MIN.	MAX.	MIN.	MAX.	NUTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension Q

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