

BGU6005/N2

Low Noise Amplifier MMIC for GPS, GLONASS, Galileo and Compass

Rev. 2 — 31 January 2017

Preliminary data sheet

1. Product profile

1.1 General description

The BGU6005/N2, also known as the GPS1001M, is a Low Noise Amplifier (LNA) for GNSS receiver applications in a plastic leadless 6-pin, extremely small SOT886 package. The BGU6005/N2 requires only one external matching inductor and one external decoupling capacitor.

1.2 Features and benefits

- Covers full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure (NF) = 0.85 dB
- Gain = 17.5 dB
- High input 1 dB compression point P_{i(1dB)} of –6 dBm
- High out of band IP3_i of 6 dBm
- Supply voltage from 1.5 V to 3.1 V
- Power-down mode current consumption < 2 μA
- Optimized performance at low supply current of 5.2 mA
- Integrated matching for the output
- Requires only one input matching inductor and one supply decoupling capacitor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated temperature stabilized bias for easy design
- Small 6-pin leadless package 1 mm × 1.45 mm × 0.5 mm

1.3 Applications

LNA for GPS, GLONASS, Galileo and Compass (BeiDou) in smart phones, feature phones, tablet PCs, personal navigation devices, digital still cameras, digital video cameras, RF front end modules, complete GPS chipset modules and theft protection (laptop, ATM).



1.4 Quick reference data

Table 1. Quick reference data

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; $V_{I(ENABLE)} \ge 0.9$ V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CC}	supply voltage	RF input AC coupled	-	1.8	-	V
I _{CC}	supply current		-	5.2	-	mΑ
Gp	power gain	no jammer	-	17	-	dB
NF	noise figure	no jammer [1]	-	0.85	-	dB
P _{i(1dB)}	input power at 1 dB	f = 1575 MHz				
	gain compression	V _{CC} = 1.8 V	-	-9	-	dBm
		V _{CC} = 2.85 V	-	-6	-	dBm
IP3 _i	input third-order intercept point	f = 1575 MHz				
		V _{CC} = 1.8 V	-	3	-	dBm
		V _{CC} = 2.85 V [2]	-	6	-	dBm

^[1] PCB losses are subtracted.

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	GND		
2	GND	6 5 4	4 5
3	RF_IN		3 — 6
4	V _{CC}		
5	ENABLE		2 1 sym129
6	RF_OUT	1 2 3 Transparent top view	
		top view	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BGU6005/N2	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886

4. Marking

Table 4. Marking codes

Type number	Marking code
BGU6005/N2	D1

^[2] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_i = -20 \text{ dBm at } f_1$; $P_i = -65 \text{ dBm at } f_2$.

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage	RF input AC coupled	-0.5	+5.0	V
V _{I(ENABLE)}	input voltage on pin ENABLE	$V_{I(ENABLE)} < V_{CC} + 0.6$	0.5	+5.0	V
$V_{I(RF_IN)}$	input voltage on pin RF_IN	DC; $V_{I(RF_IN)} < V_{CC} + 0.6$	-0.5	+5.0	V
V _{I(RF_OUT)}	input voltage on pin RF_OUT	DC; $V_{I(RF_OUT)} < V_{CC} + 0.6$	-0.5	+5.0	V
Pi	input power		-	10	dBm
P _{tot}	total power dissipation	$T_{sp} \le 130 ^{\circ}C$	1 -	55	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to ANSI/ESDA/JEDEC standard JS-001	-	±4	kV
		Charged Device Model (CDM) According to JEDEC standard JESD22-C101	-	±1	kV

^[1] Warning: due to internal ESD diode protection, the applied DC voltage should not exceed V_{CC} + 0.6 and shall not exceed 5.0 V in order to avoid excess current.

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		225	K/W

7. Characteristics

Table 7. Characteristics at $V_{CC} = 1.8 \text{ V}$

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; $V_{I(ENABLE)} \ge 0.9$ V; $P_i < -40$ dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter Conditions		Min	Тур	Max	Unit
V _{CC}	supply voltage	RF input AC coupled	-	1.8	-	V
I _{CC}	supply current	$V_{I(ENABLE)} \ge 0.9 \text{ V}$	-	5.2	-	mΑ
		$V_{I(ENABLE)} \leq 0.3 \ V$	-	-	2	μΑ
G _p	power gain	no jammer	-	17	-	dB
RLin	input return loss		-	8	-	dB
RLout	output return loss		-	14	-	dB
ISL	isolation		-	24	-	dB
NF	noise figure	no jammer [1]	-	0.85	-	dB
P _{i(1dB)}	input power at 1 dB gain compression	f = 1575 MHz	-	-9	-	dBm
IP3 _i	input third-order intercept point	f = 1575 MHz [2]	-	3	-	dBm

^[2] The RF input and RF output are AC coupled through internal DC blocking capacitor.

^[3] T_{sp} is the temperature at the soldering point of the emitter lead.

Table 7. Characteristics at V_{CC} = 1.8 V ...continued

f = 1559 MHz to 1610 MHz; V_{CC} = 1.8 V; $V_{I(ENABLE)} \ge 0.9$ V; $P_i < -40$ dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
t _{on}	turn-on time	[3]	-	-	2	μS
t _{off}	turn-off time	[3]	-	-	1	μS
K	Rollett stability factor		1	-	-	

- [1] PCB losses are subtracted.
- [2] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_i = -20 \text{ dBm at } f_1$; $P_i = -65 \text{ dBm at } f_2$.
- [3] Within 10 % of the final gain.

Table 8. Characteristics at $V_{CC} = 2.85 \text{ V}$

f = 1559 MHz to 1610 MHz; V_{CC} = 2.85 V; $V_{I(ENABLE)} \ge 0.9$ V; P_i < -40 dBm; T_{amb} = 25 °C; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CC}	supply voltage	RF input AC coupled	-	2.85	-	V
I _{CC}	supply current	VI(_{ENABLE}) ≥ 0.9 V	-	5.7	-	mΑ
		VI(_{ENABLE}) ≤ 0.3 V	-	-	2	μΑ
Gp	power gain	no jammer	-	17.5	-	dB
RLin	input return loss		-	8	-	dB
RLout	output return loss		-	15	-	dB
ISL	isolation		-	25	-	dB
NF	noise figure	no jammer [1]	-	0.85	-	dB
P _{i(1dB)}	input power at 1 dB gain compression	f = 1575 MHz	-	-6	-	dBm
IP3 _i	input third-order intercept point	f = 1575 MHz [2]	-	6	-	dBm
t _{on}	turn-on time	[3]	-	-	2	μS
t _{off}	turn-off time	[3]	-	-	1	μS
K	Rollett stability factor		1	-	-	

- [1] PCB losses are subtracted.
- [2] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_i = -20 \text{ dBm at } f_1$; $P_i = -65 \text{ dBm at } f_2$.
- [3] Within 10 % of the final gain.

Table 9. ENABLE (pin 5)

 $-40 \text{ °C} \le T_{amb} \le +85 \text{ °C}; 1.5 \text{ V} \le V_{CC} \le 3.1 \text{ V}$

V _{I(ENABLE)} (V)	State
≤ 0.3	OFF
≥ 0.9	ON

8. Application information

8.1 GNSS LNA

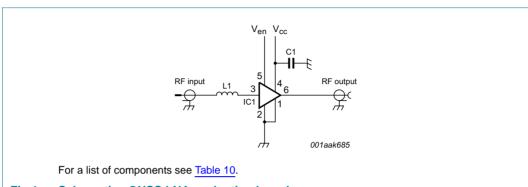


Fig 1. Schematics GNSS LNA evaluation board

Table 10. List of components For schematics see Figure 1.

Component	Description	Value	Supplier	Remarks
C1	decoupling capacitor	1 nF	various	
IC1	BGU6005/N2	-	NXP	
L1	high quality matching inductor	5.6 nH	Murata LQW15A	

9. Package outline

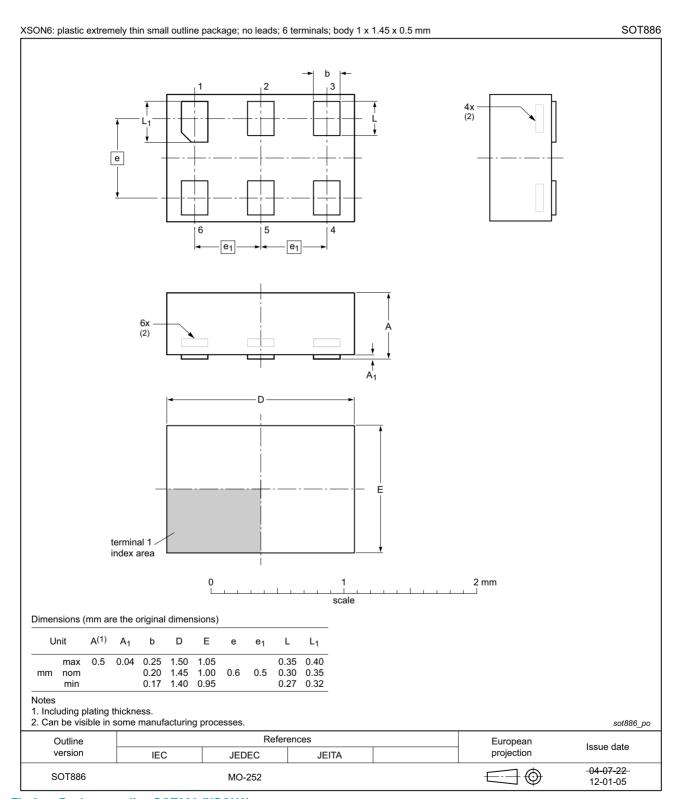


Fig 2. Package outline SOT886 (XSON6)

10. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

11. Abbreviations

Table 11. Abbreviations

Acronym	Description
ATM	Automated Teller Machine (cash dispenser)
ESD	ElectroStatic Discharge
GLONASS	GLObal NAvigation Satellite System
GNSS	Global Navigation Satellite System
GPS	Global Positioning System
НВМ	Human Body Model
MMIC	Monolithic Microwave Integrated Circuit
PCB	Printed Circuit Board

12. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGU6005_N2 v.2	20170131	Preliminary data sheet	-	BGU6005_N2 v.1
Modifications:	Section 1: added GPS1001M according to our new naming convention			
BGU6005_N2 v.1	20140324	Preliminary data sheet	-	-

NXP Semiconductors BGU6005/N2

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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15. Contents

Product profile	1
General description	1
Features and benefits	
Quick reference data	2
Pinning information	2
Ordering information	2
Marking	2
Limiting values	3
Thermal characteristics	3
Characteristics	3
Application information	5
GNSS LNA	5
Package outline	6
Handling information	7
Abbreviations	7
Revision history	7
Legal information	8
Data sheet status	8
Definitions	8
Disclaimers	8
Trademarks	9
Contact information	9
Contents	0
	General description Features and benefits Applications Quick reference data Pinning information Ordering information Marking Limiting values Thermal characteristics Characteristics Characteristics Application information GNSS LNA Package outline Handling information Abbreviations Revision history Legal information Data sheet status Definitions Disclaimers Trademarks Contact information.

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