





#### **FEATURES**

- RoHS compliant
- Typical efficiency from 83%
- Power density 1.9W/cm³
- Wide temperature performance at full 3 Watt load, −40°C to 85°C
- UL 94V-0 package material
- No heatsink required
- Industry standard pinout
- 3kVDC isolation (1 minute)
- 5V & 12V input
- 5V, 9V, 12V, & 15V output
- Fully encapsulated with toroidal magnetics
- No external components required
- No electrolytic or tantalum capacitors
- UL 60950 recognition pending

#### **PRODUCT OVERVIEW**

The MEV3 series is a new range of high performance 3W DC/DC converters, offering 3W of available output power in a previously rated 2W package capable of operation over the full industrial temperature range of -40°C to 85°C. Available in an industry standard SIP package, with a pin compatible power upgrade path from the 1W NMV/MEV and 2W NMK series', they are ideally suited for providing local supplies on control system boards with the added benefit of 3kVDC galvanic isolation.





SELECTION GUIDE												
Order Code	Nominal Input Voltage	Output Voltage	Output Current	Input Current at Rated Load	Load Regulation (Typ)	Load Regulation (Max)	Ripple & Noise (Typ)¹	Ripple & Noise (Max)¹	Efficiency (Min.)	Efficiency (Typ.)	Isolation Capacitance	MTTF <sup>2</sup>
	V	V	mA	mA	9	6	mV	р-р	%	%	pF	kHrs
MEV3S0505SC	5	5	600	700	6.8	8	39	50	81.5	83	28	6029
MEV3S0509SC	5	9	333	680	5.5	7	24	35	85.3	87	43	5163
MEV3S0512SC	5	12	250	700	5	7	20	35	81.5	85.5	32	5995
MEV3S0515SC	5	15	200	700	5	6	18	30	85	87.5	40	5426
MEV3S1205SC	12	5	600	300	4.5	6	34	50	81.5	84	34	6063
MEV3S1209SC	12	9	333	280	3.6	5	22	35	85.5	87.5	50	6056
MEV3S1212SC	12	12	250	280	3.2	4.5	16	30	86	88	63	6572
MEV3S1215SC	12	15	200	280	2.8	4	13	30	87	89	70	5754

INPUT CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Voltano vonno	Continuous operation, 5V input types	4.5	5	5.5	V			
Voltage range	Continuous operation, 12V input types	10.8	12	13.2	V			
Reflected ripple current	5V input types		8	18	m A n n			
	12V input types		6.5	15	mA p-p			

OUTPUT CHARACTERISTICS							
Parameter	Conditions	Min.	Тур.	Max.	Units		
Rated Power	T <sub>A</sub> =-40°C to 85°C			3	W		
Voltage Set Point Accuracy	See tolerance envelope						
Line regulation	High V <sub>IN</sub> to low V <sub>IN</sub>		1.01	1.1	%/%		

ISOLATION CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Isolation test voltage	Flash tested for 1 minute	3000			VDC			
Resistance	Viso= 1000VDC	10			GΩ			

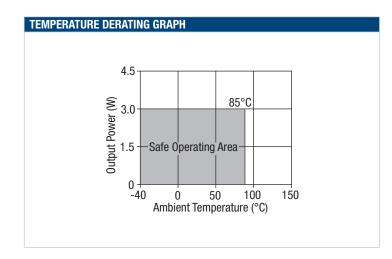
GENERAL CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Switching frequency			60		kHz			

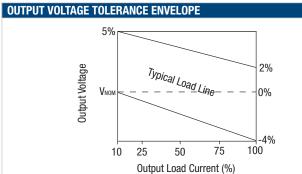
TEMPERATURE CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Specification	All output types	-40		85				
Storage		-50		125	°C			
Case Temperature rise	MEV3S0505SC			37				
above ambient	All other types			35				
Cooling	Free air convection							

ABSOLUTE MAXIMUM RATINGS	
Lead temperature 1.5mm from case for 10 seconds	260°C
Internal power dissipation	700mW
Input voltage V <sub>IN</sub> , MEV05 types	7V
Input voltage V <sub>IN</sub> , MEV12 types	15V

- 1. See Ripple & Noise characterisation method.
- 2. Calculated using MIL-HDBK-217F FN2 with nominal input voltage at full load.

All specifications typical at T<sub>A</sub>=25°C, nominal input voltage and rated output current unless otherwise specified.





The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.

#### **TECHNICAL NOTES**

#### **ISOLATION VOLTAGE**

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions MEV3 series of DC/DC converters are all 100% production tested at their stated isolation voltage. This is 3kVDC for 1 minute.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

For a part holding no specific agency approvals, such as the MEV3 series, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

#### REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The MEV3 series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enameled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognized parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

#### Rohs Compliant Information



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. They are backward compatible with Sn/Pb soldering systems. For further information, please visit www.murata-ps.com/rohs



#### **APPLICATION NOTES**

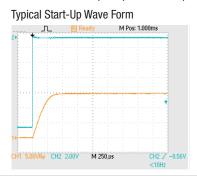
#### Minimum load

The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

#### Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of  $2.2\mu s$  and output capacitance of  $10\mu F$ , are shown in the table below. The product series will start into a capacitance of  $47\mu F$  with an increased start time, however, the maximum recommended output capacitance is  $10\mu F$ .

	Start-up time
	μs
MEV3S0505SC	165
MEV3S0509SC	300
MEV3S0512SC	650
MEV3S0515SC	1200
MEV3S1205SC	75
MEV3S1209SC	200
MEV3S1212SC	350
MEV3S1215SC	410

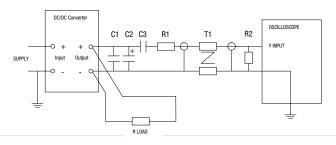


#### Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

C1	1μF X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC/DC converter
C2	$10\mu F$ tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC/DC converter with an ESR of less than $100 m\Omega$ at $100 \ kHz$
C3	100nF multilayer ceramic capacitor, general purpose
R1	$450\Omega$ resistor, carbon film, ±1% tolerance
R2	$50\Omega$ BNC termination
T1	3T of the coax cable through a ferrite toroid
RLOAD	Resistive load to the maximum power rating of the DC/DC converter. Connections should be made via twisted wires
Measured va	ues are multiplied by 10 to obtain the specified values.

#### Differential Mode Noise Test Schematic





#### **APPLICATION NOTES (continued)**

#### **Output Ripple Reduction**

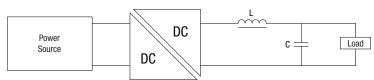
By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

#### Component selection

Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended.

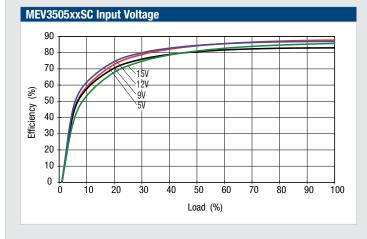
The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC/DC converter.

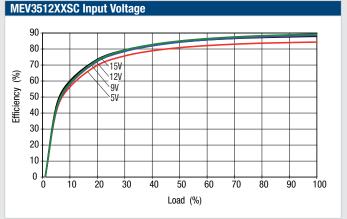
Inductor: The rated current of the inductor should not be less than that of the output of the DC/DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC/DC converter. The SRF (Self Resonant Frequency) should be >20MHz



			Capacitor	
	L, μH	SMD	Through Hole	C, µF
MEV3S0505SC	10	84103C	11R103C	4.7
MEV3S0509SC	22	84223C	11R223C	2.2
MEV3S0512SC	47	84473C	11R473C	1
MEV3S0515SC	47	84473C	11R473C	1
MEV3S1205SC	10	84103C	11R103C	4.7
MEV3S1209SC	22	84223C	11R223C	2.2
MEV3S1212SC	47	84473C	11R473C	1
MEV3S1215SC	47	84473C	11R473C	1

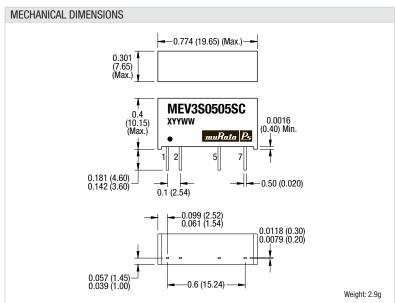
#### **EFFICIENCY VS LOAD**





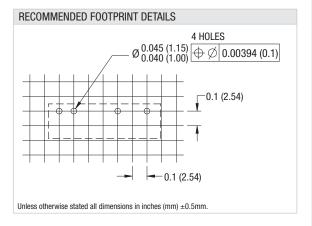


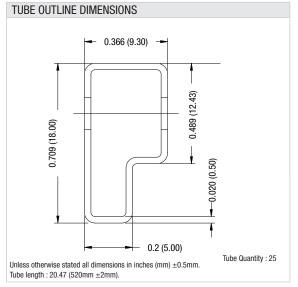
#### PACKAGE SPECIFICATIONS



Unless otherwise stated, all dimensions in inches  $\pm 0.01$  (mm  $\pm 0.25$ mm). All pins on a 0.1 (2.54) pitch and within  $\pm 0.01$  (0.25) of true position.

# PIN CONNECTIONS Pin Function 1 +V<sub>IN</sub> 2 -V<sub>IN</sub> 5 -V<sub>OUT</sub> 7 +V<sub>OUT</sub>





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