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June 2002 Revised March 2004

NC7SP125

TinyLogic® ULP Buffer with 3-STATE Output

General Description

The NC7SP125 is a single Buffer with 3-STATE output from Fairchild's Ultra Low Power (ULP) series of TinyLogic®. Ideal for applications where battery life is critical, this product is designed for ultra low power consumption within the V_{CC} operating range of 0.9V to 3.6V.

The internal circuit is composed of a minimum of inverter stages, including the output buffer, to enable ultra low static and dynamic power.

The NC7SP125, for lower drive requirements, is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve best in class speed operation while maintaining extremely low CMOS power dissipation.

Features

- 0.9V to 3.6V V_{CC} supply operation
- 3.6V overvoltage tolerant I/O's at V_{CC} from 0.9V to 3.6V
- t_{PC}

3 ns typ for 3.0V to 3.6V V_{CC}

4 ns typ for 2.3V to 2.7V V_{CC}

5 ns typ for 1.65V to 1.95V $\ensuremath{\text{V}_{\text{CC}}}$

6 ns typ for 1.40V to 1.60V $\rm V_{\rm CC}$

10 ns typ for 1.10V to 1.30V V_{CC}

26 ns typ for 0.90V $\rm V_{\rm CC}$

- Power-Off high impedance inputs and outputs
- Static Drive (I_{OH}/I_{OL})

±2.6 mA @ 3.00V V_{CC}

±2.1 mA @ 2.30V V_{CC}

±1.5 mA @ 1.65V V_{CC}

 \pm 1.0 mA @ 1.40V V_{CC}

 ± 0.5 mA @ 1.10V V_{CC}

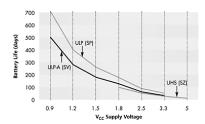
 $\pm 20~\mu A$ @ 0.9V V_{CC}

- Uses patented Quiet Series™ noise/EMI reduction circuitry
- Ultra small MicroPak™ leadfree packages
- Ultra low dynamic power

Ordering Code:

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As	
NC7SP125P5X	MAA05A	P25	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel	
NC7SP125L6X	MAC06A	L5	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel	

Battery Life vs. V_{CC} Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly.

Battery Life = (V_{battery} *I_{battery}*.9)/(P_{device})/24hrs/day

Where, $P_{device} = (I_{CC} * V_{CC}) + (C_{PD} + C_L) * V_{CC}^{2} * f$

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with $C_L = 15 \, pF$ load

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Quiet Series™ and MicroPak™ are trademarks of Fairchild Semiconductor Corporation.

Logic Symbol

IEEE/IEC



Pin Descriptions

Pin Names	Description
A, OE	Input
Y	Output
NC	No Connect

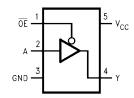
Function Table

In	out	Output
OE	In A	Out Y
L	L	L
L	Н	Н
Н	X	Z

- H = HIGH Logic Level
 L = LOW Logic Level
 X = HIGH or LOW Logic Level
 Z = HIGH Impedance State

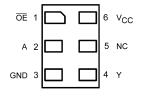
Connection Diagrams

Pin Assignments for SC70



(Top View)

Pad Assignments for MicroPak



(Top Thru View)

Absolute Maximum Ratings(Note 1)

Supply Voltage (V_{CC}) -0.5V to +4.6V DC Input Voltage (V_{IN}) -0.5V to +4.6V

-0.5V to V_{CC} +0.5V

-0.5V to 4.6V

±50 mA

-50 mA

+50 mA

 \pm 50 mA

DC Output Voltage (V_{OUT}) HIGH or LOW State (Note 2) $V_{CC} = 0V$

DC Input Diode Current (I_{IK}) $V_{IN} < 0V$ DC Output Diode Current (I_{OK})

 $V_{OUT} < 0V$ V_{OUT} > V_{CC} DC Output Source/Sink Current (I_{OH}/I_{OL})

DC V_{CC} or Ground Current per

Supply Pin (I_{CC} or Ground) \pm 50 mA Storage Temperature Range (T_{STG}) -65°C to +150°C

Recommended Operating Conditions (Note 3)

0.9V to 3.6V Supply Voltage 0V to 3.6V Input Voltage (V_{IN})

Output Voltage (V_{OUT})

HIGH or LOW State 0V to V_{CC} 0V to 3.6V $V_{CC} = 0V$

Output Current in I_{OH}/I_{OL}

 $V_{CC} = 3.0V$ to 3.6V±2.6 mA $V_{CC} = 2.3V \text{ to } 2.7V$ \pm 2.1 mA $V_{CC} = 1.65V$ to 1.95V \pm 1.5 mA

 $V_{CC} = 1.40V \text{ to } 1.60V$ \pm 1 mA $V_{CC} = 1.10V \text{ to } 1.30V$ ±0.5 mA $V_{CC} = 0.9V$ ±20 μA

Free Air Operating Temperature (T_A) -40°C to +85°C

Minimum Input Edge Rate (Δt/ΔV)

 $V_{IN} = 0.8V$ to 2.0V, $V_{CC} = 3.0V$ 10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: IO Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

Symbol Parameter		V _{CC}	$T_A = +25^{\circ}C$		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions
Symbol	Farameter	(V)	Min	Max	Min	Max	Units	Conditions
V_{IH}	HIGH Level	0.90	0.65 x V _{CC}		0.65 x V _{CC}			
	Input Voltage	$1.10 \le V_{CC} \le 1.30$	0.65 x V _{CC}		0.65 x V _{CC}			
		$1.40 \le V_{CC} \le 1.60$	0.65 x V _{CC}		0.65 x V _{CC}		V	
		$1.65 \leq V_{CC} \leq 1.95$	0.65 x V _{CC}		0.65 x V _{CC}		v	
		$2.30 \leq V_{CC} \leq 2.70$	1.6		1.6			
		$3.00 \leq V_{CC} \leq 3.60$	2.1		2.1			
V _{IL}	LOW Level	0.90		0.35 x V _{CC}		0.35 x V _{CC}		
	Input Voltage	$1.10 \leq V_{CC} \leq 1.30$		$0.35 \times V_{\rm CC}$		$0.35 \times V_{\rm CC}$		
		$1.40 \leq V_{CC} \leq 1.60$		$0.35 \times V_{\rm CC}$		$0.35 \times V_{\rm CC}$	V	
		$1.65 \leq V_{CC} \leq 1.95$		$0.35 \times V_{\rm CC}$		$0.35 \times V_{\rm CC}$	•	
		$2.30 \leq V_{CC} \leq 2.70$		0.7		0.7		
		$3.00 \leq V_{CC} \leq 3.60$		0.9		0.9		
V _{OH}	HIGH Level	0.90	V _{CC} - 0.1		V _{CC} - 0.1			
	Output Voltage	$1.10 \leq V_{CC} \leq 1.30$	V _{CC} - 0.1		V _{CC} - 0.1			
		$1.40 \leq V_{CC} \leq 1.60$	V _{CC} - 0.1		$V_{CC} - 0.1$			I _{OH} = -20 μA
		$1.65 \le V_{CC} \le 1.95$	V _{CC} - 0.1		V _{CC} - 0.1			ΙΟΗ - 20 μ/
		$2.30 \leq V_{CC} \leq 2.70$	V _{CC} - 0.1		V _{CC} - 0.1			
		$3.00 \leq V_{CC} \leq 3.60$			V _{CC} - 0.1		V	
		$1.10 \le V_{CC} \le 1.30$	0.75 x V _{CC}		0.70 x V _{CC}			$I_{OH} = -0.5 \text{ mA}$
		$1.40 \le V_{CC} \le 1.60$	1.07		0.99			$I_{OH} = -1 \text{ mA}$
		$1.65 \le V_{CC} \le 1.95$		•	1.22	•		$I_{OH} = -1.5 \text{ mA}$
		$2.30 \leq V_{CC} \leq 2.70$			1.87			$I_{OH} = -2.1 \text{ mA}$
		$3.00 \leq V_{CC} \leq 3.60$	2.61		2.55			$I_{OH} = -2.6 \text{ mA}$

DC Electrical Characteristics (Continued)

Symbol	Parameter	V _{CC}	T _A =	+25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions
Symbol	Farameter	(V)	Min	Max	Min	Max	Units	Conditions
V _{OL}	LOW Level	0.90		0.1		0.1		
	Output Voltage	$1.10 \leq V_{CC} \leq 1.30$		0.1		0.1		
		$1.40 \le V_{CC} \le 1.60$		0.1		0.1		1 204
		$1.65 \leq V_{CC} \leq 1.95$		0.1		0.1		$I_{OL} = 20 \mu A$
		$2.30 \leq V_{CC} \leq 2.70$		0.1		0.1		
		$3.00 \leq V_{CC} \leq 3.60$		0.1		0.1	V	
		$1.10 \le V_{CC} \le 1.30$		0.30 x V _{CC}		0.30 x V _{CC}		I _{OL} = 0.5 mA
		$1.40 \le V_{CC} \le 1.60$		0.31		0.37		I _{OL} = 1 mA
		$1.65 \le V_{CC} \le 1.95$		0.31		0.35		I _{OL} = 1.5 mA
		$2.30 \le V_{CC} \le 2.70$		0.31		0.33		I _{OL} = 2.1 mA
		$3.00 \le V_{CC} \le 3.60$		0.31		0.33		I _{OL} = 2.6 mA
I _{IN}	Input Leakage Current	0.90 to 3.60		±0.1		±0.5	μΑ	$0 \le V_I \le 3.6V$
I _{OZ}	3-STATE Output	0.90 to 3.60		±0.5		±0.5	μА	$V_I = V_{IH}$ or V_{IL}
	Leakage	0.90 to 3.00		±0.5		±0.5	μΑ	$0 \le V_O \le 3.6V$
I _{OFF}	Power Off Leakage Current	0		0.5		0.5	μΑ	$0 \le (V_I, V_O) \le 3.6V$
I _{CC}	Quiescent Supply Current	0.90 to 3.60		0.9		0.9	μΑ	$V_I = V_{CC}$ or GND

AC Electrical Characteristics

Symbol	Parameter	V _{CC}	T _A = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	Figure		
Symbol	Parameter	(V)	Min	Тур	Max	Min	Max	Units	Conditions	Number	
t _{PHL}	Propagation Delay	0.90		26							
t _{PLH}		$1.10 \leq V_{CC} \leq 1.30$	4.0	10	19.1	3.5	39.6				
		$1.40 \le V_{CC} \le 1.60$	2.0	6	11.2	1.5	14.5		C _L = 10 pF		
		$1.65 \le V_{CC} \le 1.95$	1.5	5	8.6	1.0	11.6	ns	$R_L = 1 M\Omega$		
		$2.30 \leq V_{CC} \leq 2.70$	1.0	4	6.3	0.8	8.2				
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	5.3	0.5	7.2				
t _{PZH}	Output	0.90		29					C _L = 10 pF		
t_{PZL}	Enable Time	$1.10 \le V_{CC} \le 1.30$	4.0	8	17.5	3.5	40.4		$R_U = 5000\Omega$		
		$1.40 \le V_{CC} \le 1.60$	2.0	6	11.9	1.5	14.8	ns	$R_D = 5000\Omega$		
		$1.65 \leq V_{CC} \leq 1.95$	1.5	5	9.7	1.0	12.3	115	$S_1 = GND \text{ for } t_{PZH}$		
		$2.30 \leq V_{CC} \leq 2.70$	1.0	4	7.7	0.8	10.5		$S_1 = V_I$ for t_{PZL}		
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	6.9	0.5	8.6		$V_I = 2 \times V_{CC}$		
t _{PHZ}	Output	0.90		28					C _L = 10 pF		
t_{PLZ}	Disable Time	$1.10 \leq V_{CC} \leq 1.30$	4.0	8	20.5	3.5	42.0		$R_U = 5000\Omega$		
		$1.40 \leq V_{CC} \leq 1.60$	2.0	6	15.3	1.5	18.0	ns	$R_D = 5000\Omega$		
		$1.65 \leq V_{CC} \leq 1.95$	1.5	5	14.7	1.0	17.8	115	$S_1 = GND \text{ for } t_{PHZ}$		
		$2.30 \leq V_{CC} \leq 2.70$	1.0	4	13.7	0.8	15.0		$S_1 = V_I \text{ for } t_{PLZ}$		
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	13.5	0.5	14.8		$V_I = 2 \times V_{CC}$		
t _{PHL}	Propagation Delay	0.90		28							
t_{PLH}		$1.10 \leq V_{CC} \leq 1.30$	5.0	10	20.5	4.5	42.5				
		$1.40 \leq V_{CC} \leq 1.60$	3.0	7	11.8	2.5	15.4	ns	C _L = 15 pF	Figures	
		$1.65 \leq V_{CC} \leq 1.95$	2.0	5	9.1	2.0	12.2	113	$R_L = 1 M\Omega$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.5	4	6.6	1.0	8.6				
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	5.6	0.5	7.5				
t _{PZH}	Output	0.90		31					C _L = 15 pF		
t_{PZL}	Enable Time	$1.10 \leq V_{CC} \leq 1.30$	5.0	11	18.2	4.5	43.3		$R_U = 5000\Omega$		
		$1.40 \leq V_{CC} \leq 1.60$	3.0	7	12.5	2.5	15.5	ns	$R_D = 5000\Omega$	Figures	
		$1.65 \leq V_{CC} \leq 1.95$	2.0	5	10.2	2.0	12.9	110	$S_1 = GND \text{ for } t_{PZH}$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.5	4	8.0	1.0	9.9		$S_1 = V_I \text{ for } t_{PLZ}$		
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	7.2	0.5	8.9		$V_I = 2 \times V_{CC}$		

AC Electrical Characteristics (Continued)

Symbol	Parameter	V _{cc}	T _A = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	Figure		
Symbol	Parameter	(V)	Min	Тур	Max	Min	Max	Units	Conditions	Number	
t _{PHZ}	Output	0.90		30					C _L = 15 pF		
t _{PLZ}	Disable Time	$1.10 \le V_{CC} \le 1.30$	5.0	11	21.6	4.5	44.9		$R_U = 5000\Omega$		
		$1.40 \le V_{CC} \le 1.60$	3.0	7	15.9	2.5	18.8	ns	$R_D = 5000\Omega$	Figures	
		$1.65 \leq V_{CC} \leq 1.95$	2.0	5	15.2	2.0	18.2	115	$S_1 = GND \text{ for } t_{PHZ}$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.5	4	14.1	1.0	15.4		$S_1 = V_I \text{ for } t_{PLZ}$		
		$3.00 \leq V_{CC} \leq 3.60$	1.0	3	13.9	0.5	15.1		$V_I = 2 \times V_{CC}$		
t _{PHL}	Propagation Delay	0.90		34							
t _{PLH}		$1.10 \le V_{CC} \le 1.30$	5.5	12	23.4	5.0	51.1				
		$1.40 \le V_{CC} \le 1.60$	4.0	8	13.8	3.0	17.7	ns	$C_L = 30 \text{ pF}\Omega$	Figures	
		$1.65 \le V_{CC} \le 1.95$	2.0	6	10.6	2.0	14.0	ns	$R_L=1M\Omega$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.0	5	7.6	1.0	9.9				
		$3.00 \le V_{CC} \le 3.60$	0.8	4	6.4	0.5	8.9				
t _{PZH}	Output	0.90		37					C _L = 30 pF		
t_{PZL}	Enable Time	$1.10 \le V_{CC} \le 1.30$	6.0	13	24.4	5.0	51.9		$R_U = 5000\Omega$		
		$1.40 \le V_{CC} \le 1.60$	4.0	8	14.5	3.0	17.9	ns	$R_D = 5000\Omega$	Figures	
		$1.65 \leq V_{CC} \leq 1.95$	2.0	6	11.7	2.0	14.7	115	$S_1 = GND \text{ for } t_{PZH}$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.0	5	9.1	1.0	11.1		$S_1 = V_I$ for t_{PZL}		
		$3.00 \leq V_{CC} \leq 3.60$	0.8	4	8.1	0.5	10.1		$V_I = 2 \times V_{CC}$		
t _{PHZ}	Output	0.90		36					C _L = 30 pF		
t_{PLZ}	Disable Time	$1.10 \le V_{CC} \le 1.30$	6.0	13	24.8	5.0	53.5		$R_U = 5000\Omega$		
		$1.40 \le V_{CC} \le 1.60$	4.0	8	17.1	3.0	21.1	ns	$R_D = 5000\Omega$	Figures	
		$1.65 \leq V_{CC} \leq 1.95$	2.0	6	16.5	2.0	20.5	115	$S_1 = GND \text{ for } t_{PHZ}$	1, 2	
		$2.30 \leq V_{CC} \leq 2.70$	1.0	5	15.2	1.0	16.7		$S_1 = V_I \text{ for } t_{PLZ}$		
		$3.00 \leq V_{CC} \leq 3.60$	8.0	4	14.8	0.5	16.3		$V_I = 2 \times V_{CC}$		
C _{IN}	Input Capacitance	0		2.0				pF			
C _{OUT}	Output Capacitance	0		4.0				pF			
C _{PD}	Power Dissipation Capacitance	0.9 to 3.60		8				pF	$V_I = 0V \text{ or } V_{CC},$ f = 10 MHz		

AC Loading and Waveforms

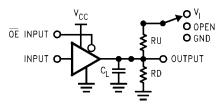
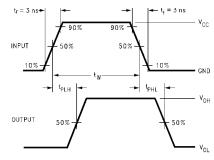


FIGURE 1. AC Test Circuit



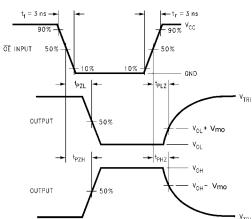


FIGURE 2. AC Waveforms

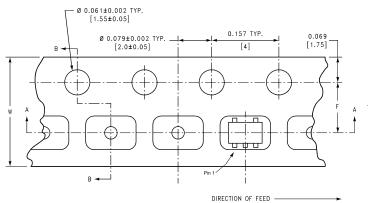
Symbol	V _{CC}								
5,25.	$3.3V \pm 0.3V$	$\textbf{2.5V} \pm \textbf{0.2V}$	1.8V \pm 0.15V	1.5V ± 0.10V	1.2V ± 0.10V	0.9V			
V _{mi}	1.5V	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2			
V _{mo}	0.3V	0.15	0.15	0.1	0.1	0.1			

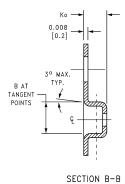
Tape and Reel Specification

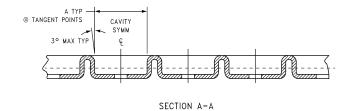
TAPE FORMAT for SC70

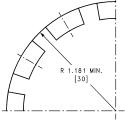
Package	Tape	Number	Cavity	Cover Tape	
Designator	Section	Cavities	Status	Status	
	Leader (Start End)	125 (typ)	Empty	Sealed	
P5X	Carrier	3000	Filled	Sealed	
	Trailer (Hub End)	75 (typ)	Empty	Sealed	

TAPE DIMENSIONS inches (millimeters)









BEND RADIUS NOT TO SCALE

Tape and Reel Specification (Continued) TAPE FORMAT for MicroPak Package Tape Number Cavity Cover Tape Designator Section Cavities Status Status Leader (Start End) Sealed 125 (typ) Empty L6X Carrier 5000 Filled Sealed Trailer (Hub End) 75 (typ) Empty Sealed TAPE DIMENSIONS inches (millimeters) 1.75±0.10 3.50±0.05 8.00 +0.30 -0.10 −ø 0.50 ±0.05 SECTION B-B SCALE:10X DIRECTION OF FEED-0.254±0.020 1.60±0.05 SECTION A-A SCALE:10X **REEL DIMENSIONS** inches (millimeters) TAPE SLOT DETAIL X **DETAIL X** SCALE: 3X Tape Size D N W1 W2 W3 В С 7.0 0.059 0.512 0.795 2.165 0.331 + 0.059/-0.000 0.567 W1 + 0.078/-0.039 8 mm (177.8) (1.50)(13.00)(20.20)(55.00) (8.40 + 1.50 / -0.00)(W1 + 2.00/-1.00)(14.40)

Physical Dimensions inches (millimeters) unless otherwise noted 0.65 1.9 B- 1.25±0.10 2.10±0.10 0.4 min 0.20 +0.10 0.25 ---LAND PATTERN RECOMMENDATION ◆ max 0.1 **②** SEE DETAIL A 0.9±.10 0.95±0.15 max 0.1 R0.14 GAGE PLANE R0.10 0.20 -- 0.425 NOMINAL DETAIL A

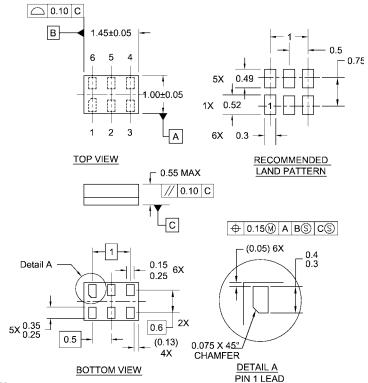
NOTES:

- A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A. B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- C. DIMENSIONS ARE IN MILLIMETERS.

MAA05ARevC

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Notes:

- 1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide Package Number MAC06A

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