

# Power Transistor (-60V, -3A)

#### 2SB1184 / 2SB1243

#### ●Features

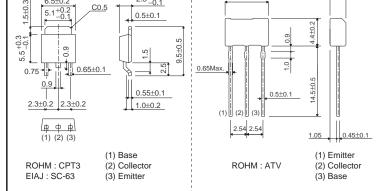
- 1) Low VCE(sat).  $V_{CE(sat)} = -0.5V (Typ.)$  $(I_C/I_B = -2A / -0.2A)$
- 2) Complements the 2SD1760 / 2SD1864.

#### Structure

Epitaxial planar type PNP silicon transistor

#### 2SB1184 2SB1243 2.5±0.2 $2.3^{+0.2}_{-0.1}$ 0.5±0.1

●Dimensions (Unit : mm)



#### ●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	-60	V	
Collector-emitter voltage		Vceo	-50	V	
Emitter-base voltage		VEBO	-5	V	
Collector current		Ic	-3	A (DC)	
Callagtar payer	2SB1184	Pc	1	W	
dissipation			15	W (Tc=25°C)	
dicolpation	2SB1243		1	W *1	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to 150	°C	

<sup>\*1</sup> Printed circuit board, 1.7mm thick, collector copper plating 100mm² or larger.

#### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	-60	_	_	V	Ic= -50μA	
Collector-emitter breakdown voltage	BVceo	-50	_	_	V	Ic=-1mA	
Emitter-base breakdown voltage	ВVево	-5	_	_	V	IE= -50μA	
Collector cutoff current	Ісво	_	_	-1	μΑ	V <sub>CB</sub> = -40V	
Emitter cutoff current	ІЕВО	-	_	-1	μΑ	V <sub>EB</sub> = -4V	
Collector-emitter saturation voltage	VCE(sat)	_	_	-1	V	Ic/I <sub>B</sub> = -2A/ -0.2A	*
DC current transfer ratio	hfe	120	_	390	_	Vc=-3V, Ic=-0.5A	*
Transition frequency	f⊤	_	70	_	MHz	Vce= -5V, Ie=0.5A, f=30MHz	
Output capacitance	Cob	_	50	_	pF	Vсв= −10V, IE=0A, f=1MHz	

<sup>\*</sup> Measured using pulse current.

2SB1184 / 2SB1243 Data Sheet

#### ●Packaging specifications and hFE

		Package Tap		ing
		Code	TL	TV2
Туре	hfe	Basic ordering unit (pieces)	2500	2500
2SB1184	QR		0	_
2SB1243	QR		_	0

#### hfe values are classified as follows:

Item	Q	R	
hfE	120 to 270	180 to 390	

#### •Electrical characteristic curves

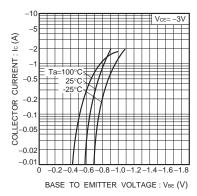


Fig.1 Grounded emitter propagation characteristics

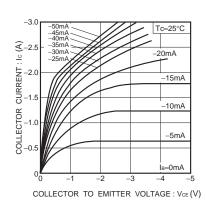


Fig.2 Grounded emitter output characteristics ( I )

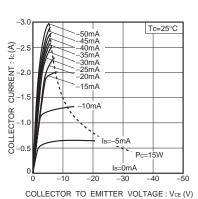


Fig.3 Grounded emitter output characteristics ( II )

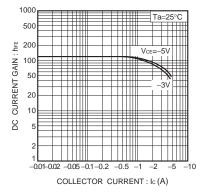


Fig.4 DC current gain vs. collector current ( I )

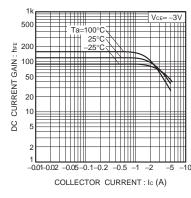


Fig.5 DC current gain vs. collector current ( II )

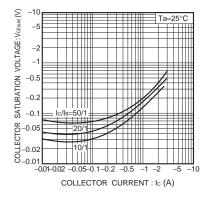


Fig.6 Collector-emitter saturation voltage vs.collector current

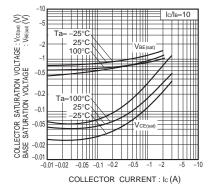


Fig.7 Collector-emitter saturation voltage vs. collector current
Base-emitter saturation voltage vs. collector current

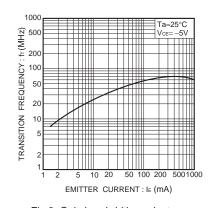


Fig.8 Gain bandwidth product vs. emitter current

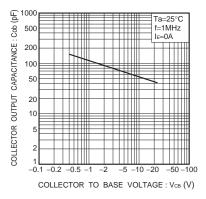


Fig.9 Collector output capacitance vs. collector base voltage

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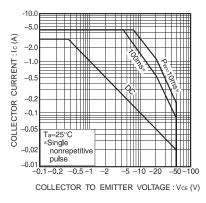


Fig.10 Safe operation area (2SB1184)

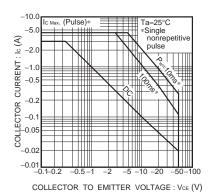


Fig.11 Safe operation area (2SB1243)

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