

# LM4121

*LM4121 Precision Micropower Low Dropout Voltage Reference*



Literature Number: SNVS073B

# LM4121

## Precision Micropower Low Dropout Voltage Reference

### General Description

The LM4121 is a precision bandgap voltage reference available in a fixed 1.25V and adjustable version with up to 5 mA current source and sink capability.

This series reference operates with input voltages as low as 1.8V and up to 12V consuming 160  $\mu$ A (Typ.) supply current. In power down mode, device current drops to less than 2  $\mu$ A.

The LM4121 comes in two grades A and Standard. The best grade devices (A) have an initial accuracy of 0.2%, while the standard have an initial accuracy of 0.5%, both with a tempco of 50ppm/ $^{\circ}$ C guaranteed from  $-40^{\circ}$ C to  $+125^{\circ}$ C.

The very low operating voltage, low supply current and power-down capability of the LM4121 makes this product an ideal choice for battery powered and portable applications.

The device performance is guaranteed over the industrial temperature range ( $-40^{\circ}$ C to  $+85^{\circ}$ C), while certain specs are guaranteed over the extended temperature range ( $-40^{\circ}$ C to  $+125^{\circ}$ C). Please contact National for full specifications over the extended temperature range. The LM4121 is available in a standard 5-pin SOT-23 package.

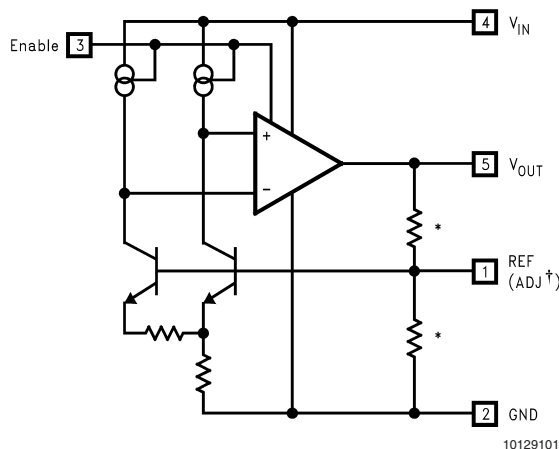
### Features (LM4121-1.2)

- Small SOT23-5 package
- Low voltage operation
- High output voltage accuracy: 0.2%
- Source and Sink current output:  $\pm 5$  mA
- Supply current: 160  $\mu$ A Typ.
- Low Temperature Coefficient: 50 ppm/ $^{\circ}$ C
- Enable pin
- Output voltages: 1.25V and Adjustable
- Industrial temperature Range:  $-40^{\circ}$ C to  $+85^{\circ}$ C
- (For extended temperature range,  $-40^{\circ}$ C to  $125^{\circ}$ C, contact National Semiconductor)

### Applications

- Portable, battery powered equipment
- Instrumentation and process control
- Automotive & Industrial
- Test equipment
- Data acquisition systems
- Precision regulators
- Battery chargers
- Base stations
- Communications
- Medical equipment

### Block Diagram

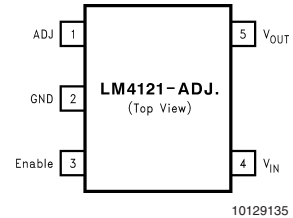
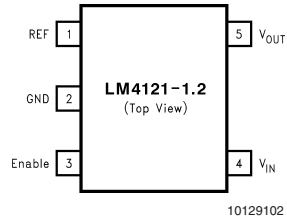


\* Resistors are removed on the LM4121-ADJ  
 †LM4121-ADJ only

LM4121-1.2 Block Diagram

10129101

## Connection Diagrams



Refer to the Ordering Information Table in this Data Sheet for Specific Part Number

SOT23-5 Surface Mount Package

## Ordering Information

Industrial Temperature Range ( $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ )

Initial Output Voltage Accuracy at $25^{\circ}\text{C}$ And Temperature Coefficient	LM4121 Supplied as 1000 Units, Tape and Reel	LM4121 Supplied as 3000 Units, Tape and Reel	Top Marking
0.2%, 50 ppm/ $^{\circ}\text{C}$ max (A grade)	LM4121AIM5-1.2	LM4121AIM5X-1.2	R19A
	LM4121AIM5-ADJ	LM4121AIM5X-ADJ	R20A
0.5%, 50 ppm/ $^{\circ}\text{C}$ max	LM4121IM5-1.2	LM4121IM5X-1.2	R19B
	LM4121IM5-ADJ	LM4121IM5X-ADJ	R20B

## SOT-23 Package Marking Information

Only four fields of marking are possible on the SOT-23's small surface. This table gives the meaning of the four fields.

Field Information
First Field: R = Reference Second and third Field: 19 = 1.250V Voltage Option 20 = Adjustable Fourth Field: A-B = Initial Reference Voltage Tolerance A = $\pm 0.2\%$ B = $\pm 0.5\%$

**Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Maximum Voltage on input or enable pins	-0.3V to 14V
Output Short-Circuit Duration	Indefinite
Power Dissipation ( $T_A = 25^\circ\text{C}$ ) (Note 2):	
MA05B package – $\theta_{JA}$	280°C/W
Power Dissipation	350 mW
ESD Susceptibility (Note 3)	
Human Body Model	2 kV
Machine Model	200V

Lead Temperature:

Soldering, (10 sec.)	+260°C
Vapor Phase (60 sec.)	+215°C
Infrared (15 sec.)	+220°C

**Operating Range** (Note 1)

Storage Temperature Range	-65°C to +150°C
Ambient Temperature Range	-40°C to +85°C
Junction Temperature Range	-40°C to +125°C

**Electrical Characteristics**

**LM4121-1.250V** Unless otherwise specified  $V_{IN} = 3.3\text{V}$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.01\mu\text{F}$ ,  $T_A = T_j = 25^\circ\text{C}$ . Limits with standard typeface are for  $T_j = 25^\circ\text{C}$ , and limits in **boldface type** apply over the  $-40^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$  temperature range.

Symbol	Parameter	Conditions	Min (Note 5)	Typ (Note 4)	Max (Note 5)	Units
$V_{OUT}$	Output Voltage Initial Accuracy LM4121A-1.250 LM4121-1.250			1.250	$\pm 0.2$  $\pm 0.5$	%
$TCV_{OUT}/^\circ\text{C}$	Temperature Coefficient	$-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$		14	50	ppm/°C
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$1.8\text{V} \leq V_{IN} \leq 12\text{V}$		0.0007	0.009 <b>0.012</b>	%/V
$\Delta V_{OUT}/\Delta I_{LOAD}$	Load Regulation	$0\text{ mA} \leq I_{LOAD} \leq 1\text{ mA}$		0.03	0.08 <b>0.17</b>	%mA
		$1\text{ mA} \leq I_{LOAD} \leq 5\text{ mA}$		0.01	0.04 <b>0.1</b>	
		$-1\text{ mA} \leq I_{LOAD} \leq 0\text{ mA}$		0.04	0.12	
		$-5\text{ mA} \leq I_{LOAD} \leq -1\text{ mA}$		0.01		
Min- $V_{IN}$	Minimum Operating Voltage	$I_{LOAD} = 5\text{mA}$		1.5	<b>1.8</b>	V
$V_N$	Output Noise Voltage	0.1 Hz to 10 Hz		20		$\mu\text{V}_{PP}$
		10 Hz to 10 kHz		30		$\mu\text{V}_{RMS}$
$I_S$	Supply Current			160	250 <b>275</b>	$\mu\text{A}$
$I_{SS}$	Power-down Supply Current	$V_{IN} = 12\text{V}$ Enable = 0.4V <b>Enable = 0.2V</b>			1 <b>2</b>	$\mu\text{A}$
$V_H$	Logic High Input Voltage		<b>1.6</b>	1.5		V
$V_L$	Logic Low Input Voltage			0.4	<b>0.2</b>	V
$I_H$	Logic High Input Current			7	<b>15</b>	$\mu\text{A}$
$I_L$	Logic Low Input Current			0.1		$\mu\text{A}$

## Electrical Characteristics

**LM4121-1.250V** Unless otherwise specified  $V_{IN} = 3.3V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.01\mu F$ ,  $T_A = T_j = 25^\circ C$ . Limits with standard typeface are for  $T_j = 25^\circ C$ , and limits in **boldface type** apply over the  $-40^\circ C \leq T_A \leq +85^\circ C$  temperature range. (Continued)

Symbol	Parameter	Conditions	Min (Note 5)	Typ (Note 4)	Max (Note 5)	Units
<b>0.2</b>						
$I_{SC}$	Short Circuit Current	$V_{IN} = 3.3V, V_{OUT} = 0$		15		mA
			<b>6</b>		<b>30</b>	
		$V_{IN} = 12V, V_{OUT} = 0$		17		
			<b>6</b>		<b>30</b>	
Hyst	Thermal Hysteresis (Note 8)	$-40^\circ C \leq T_A \leq 125^\circ C$		0.5		mV/V
$\Delta V_{OUT}$	Long Term Stability (Note 9)	1000 hrs. @ $25^\circ C$		100		ppm

## Electrical Characteristics

**LM4121-ADJ** Unless otherwise specified  $V_{IN} = 3.3V$ ,  $V_{OUT} = V_{REF}$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.01\mu F$ ,  $T_A = T_j = 25^\circ C$ . Limits with standard typeface are for  $T_j = 25^\circ C$ , and limits in **boldface type** apply over the  $-40^\circ C \leq T_A \leq +85^\circ C$  temperature range.

Symbol	Parameter	Conditions	Min (Note 5)	Typ (Note 4)	Max (Note 5)	Units
$V_{OUT} = V_{REF}$	Output Voltage Initial Accuracy LM4121A-ADJ LM4121-ADJ			1.216	$\pm 0.2$ $\pm 0.5$	%
$TCV_{REF}/^\circ C$	Temperature Coefficient	$-40^\circ C \leq T_A \leq +125^\circ C$		14	50	ppm/ $^\circ C$
$\Delta V_{REF}/\Delta V_{IN}$	Line Regulation	$1.8V \leq V_{IN} \leq 12V$		0.0007	0.009 <b>0.012</b>	%/V
$\Delta V_{OUT}/\Delta I_{LOAD}$	Load Regulation	$0 mA \leq I_{LOAD} \leq 1 mA$		0.03	0.08 <b>0.17</b>	%mA
		$1 mA \leq I_{LOAD} \leq 5 mA$		0.01	0.04 <b>0.1</b>	
		$-1 mA \leq I_{LOAD} \leq 0 mA$		0.04	0.12	
		$-5 mA \leq I_{LOAD} \leq -1 mA$		0.01		
Min- $V_{IN}$	Minimum Operating Voltage	$I_{LOAD} = 5 mA$		1.5	<b>1.8</b>	V
$V_N$	Output Noise Voltage (Note 6)	0.1 Hz to 10 Hz		20		$\mu V_{PP}$
		10 Hz to 10 kHz		30		$\mu V_{RMS}$
$I_S$	Supply Current			160	250 <b>275</b>	$\mu A$
$I_{SS}$	Power-down Supply Current	$V_{IN} = 12V$ Enable = 0.4V <b>Enable = 0.2V</b>			1 <b>2</b>	$\mu A$
$I_{BIAS}$	Reference Pin Bias Current	(Note 7)	15	<b>40</b>		nA
$V_H$	Logic High Input Voltage		<b>1.6</b>	1.5		V
$V_L$	Logic Low Input Voltage			0.4	<b>0.2</b>	V
$I_H$	Logic High Input Current			7	<b>15</b>	$\mu A$

## Electrical Characteristics

**LM4121-ADJ** Unless otherwise specified  $V_{IN} = 3.3V$ ,  $V_{OUT} = V_{REF}$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.01\mu F$ ,  $T_A = T_J = 25^\circ C$ . Limits with standard typeface are for  $T_J = 25^\circ C$ , and limits in **boldface type** apply over the  $-40^\circ C \leq T_A \leq +85^\circ C$  temperature range. (Continued)

Symbol	Parameter	Conditions	Min (Note 5)	Typ (Note 4)	Max (Note 5)	Units
$I_L$	Logic Low Input Current			0.1		$\mu A$
$I_{SC}$	Short Circuit Current	$V_{OUT} = 0$		15		mA
		$V_{IN} = 12V$ , $V_{OUT} = 0$	<b>6</b>		<b>30</b>	
			<b>6</b>		<b>30</b>	
Hyst	Thermal Hysteresis (Note 8)	$-40^\circ C \leq T_A \leq 125^\circ C$		0.5		mV/V
$\Delta V_{OUT}$	Long Term Stability (Note 9)	1000 hrs. @ $25^\circ C$		100		ppm

**Note 1:** "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

**Note 2:** Without PCB copper enhancements. The maximum power dissipation must be de-rated at elevated temperatures and is limited by  $T_{JMAX}$  (maximum junction temperature),  $\theta_{JA}$  (junction to ambient thermal resistance) and  $T_A$  (ambient temperature). The maximum power dissipation at any temperature is:  $PD_{DissMAX} = (T_{JMAX} - T_A)/\theta_{JA}$  up to the value listed in the Absolute Maximum Ratings.

**Note 3:** The human body model is a 100 pF capacitor discharged through a 1.5 k $\Omega$  resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

**Note 4:** Typical numbers are at  $25^\circ C$  and represent the most likely parametric norm.

**Note 5:** Limits are 100% production tested at  $25^\circ C$ . Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's Averaging Outgoing Quality Level (AOQL).

**Note 6:** Output noise for 1.25V option. Noise is proportional to  $V_{OUT}$ .

**Note 7:** Bias Current flows out of the Adjust pin.

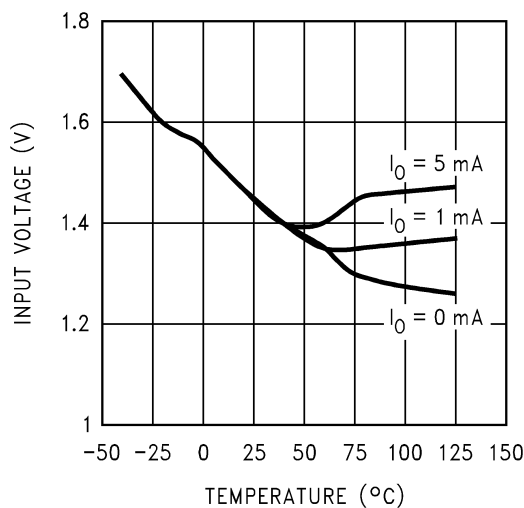
**Note 8:** Thermal hysteresis is defined as the change in  $+25^\circ C$  output voltage before and after exposing the device to temperature extremes.

**Note 9:** Long term stability is change in  $V_{REF}$  at  $25^\circ C$  measured continuously during 1000 hrs.

## LM4121- (All Options) Typical Operating Characteristics

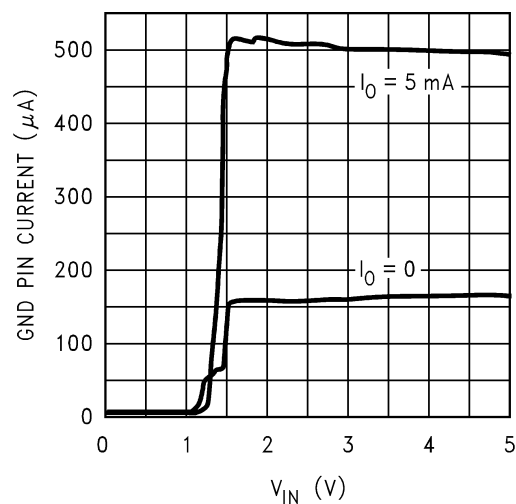
Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.25V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ .

Minimum Input Voltage vs Temperature



10129112

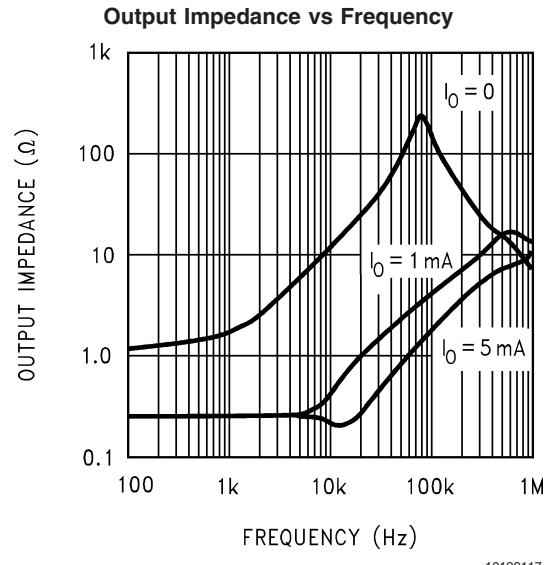
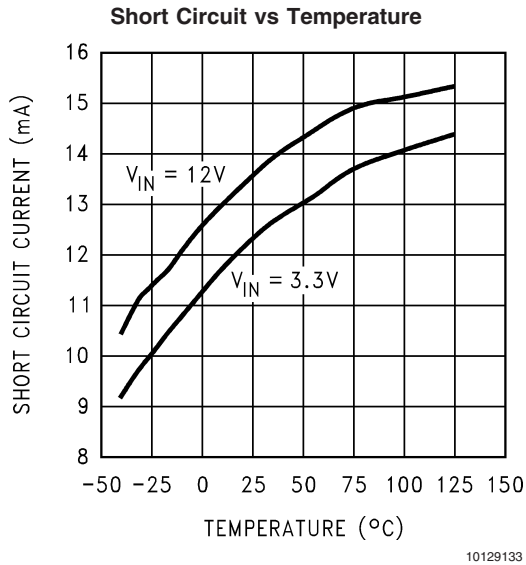
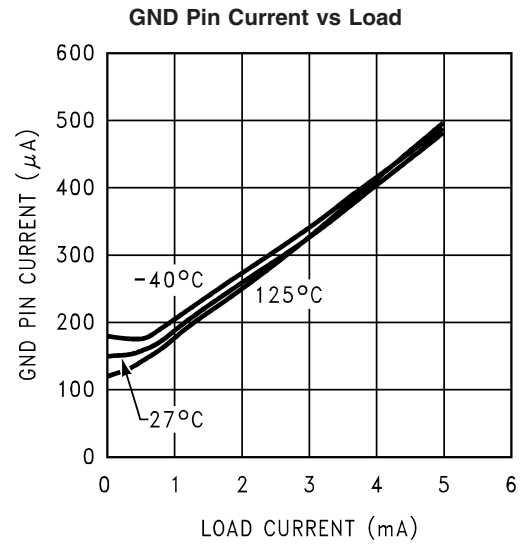
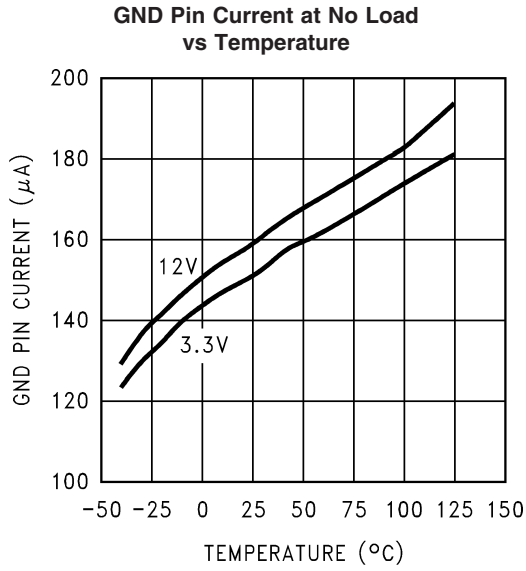
GND Pin Current vs  $V_{IN}$



10129113

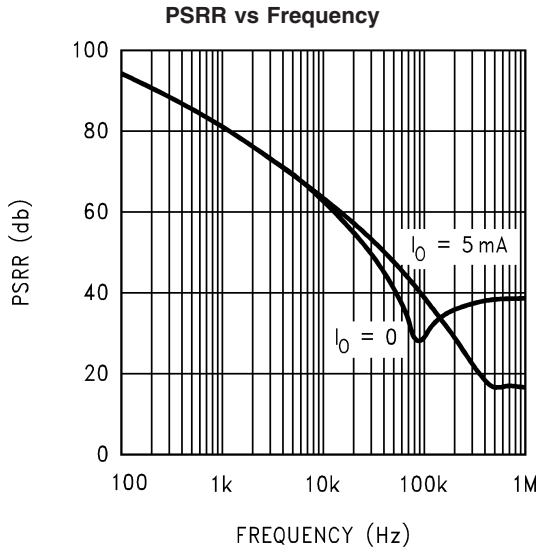
**LM4121- (All Options) Typical Operating Characteristics** Unless otherwise specified,

$V_{IN} = 3.3V$ ,  $V_{OUT} = 1.25V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ . (Continued)

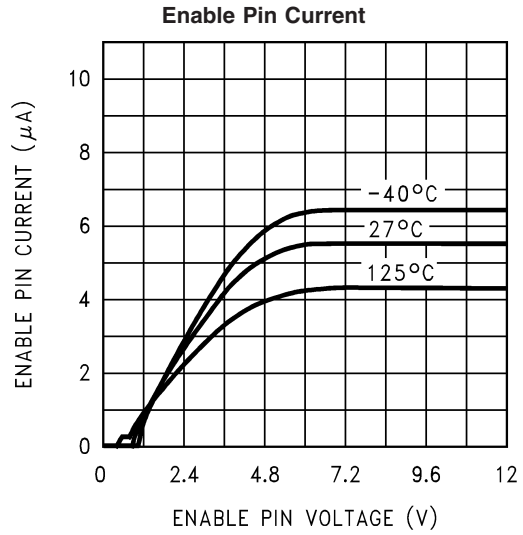


# LM4121- (All Options) Typical Operating Characteristics

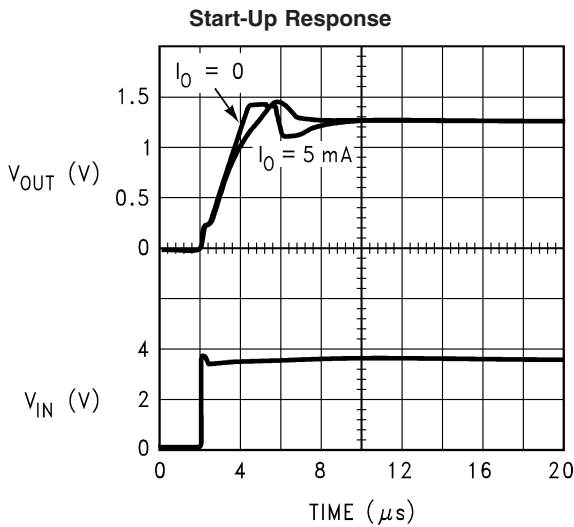
Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.25V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ . (Continued)



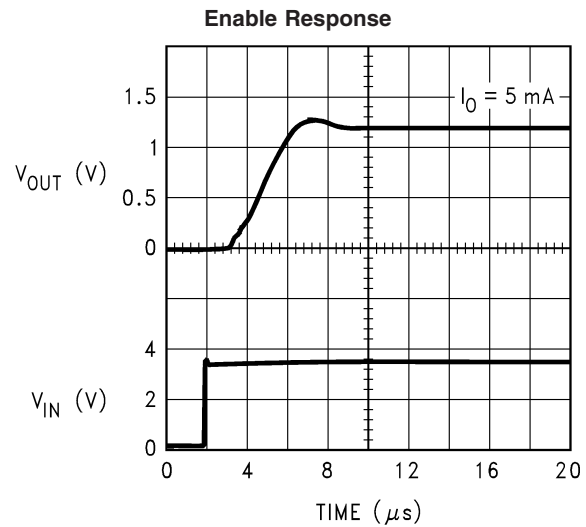
10129118



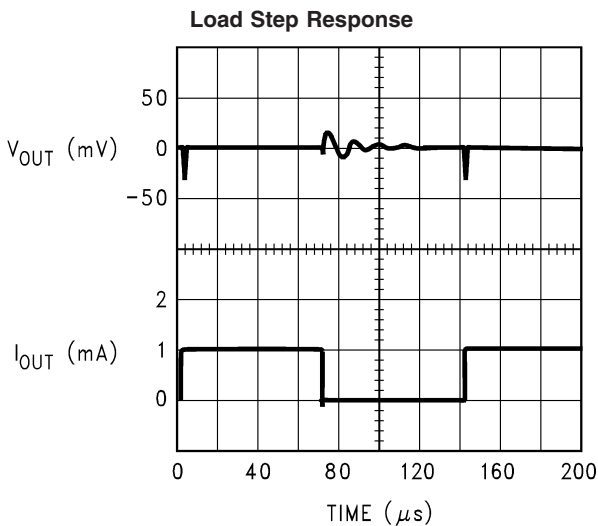
10129119



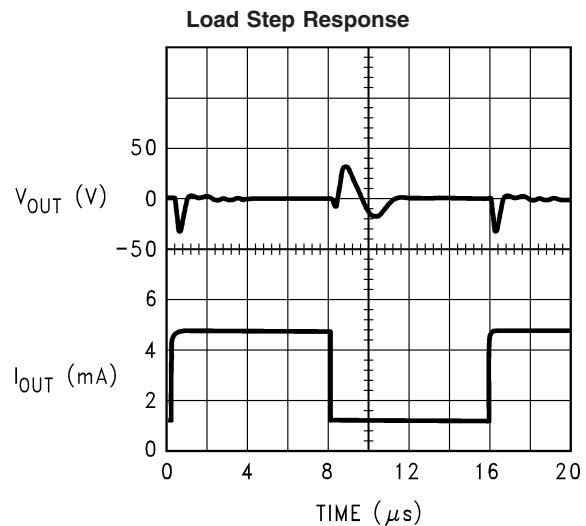
10129121



10129122



10129123



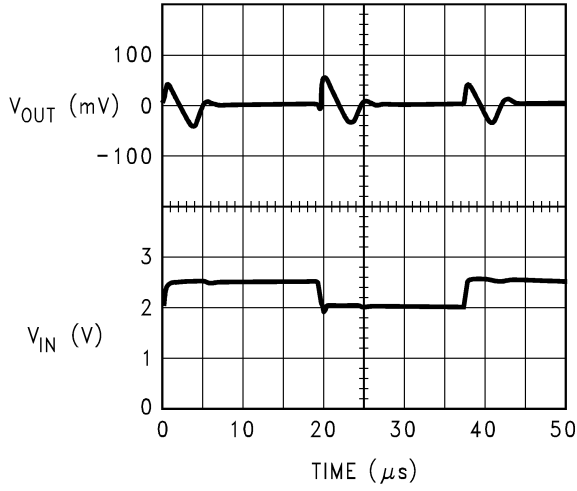
10129124



**LM4121- (All Options) Typical Operating Characteristics** Unless otherwise specified,

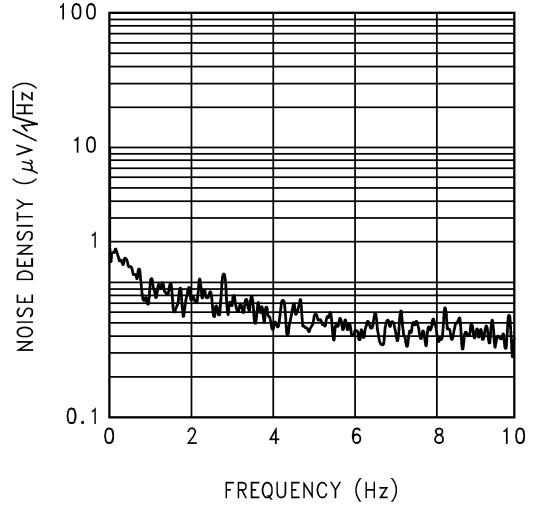
$V_{IN} = 3.3V$ ,  $V_{OUT} = 1.25V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ . (Continued)

**Line Step Response**



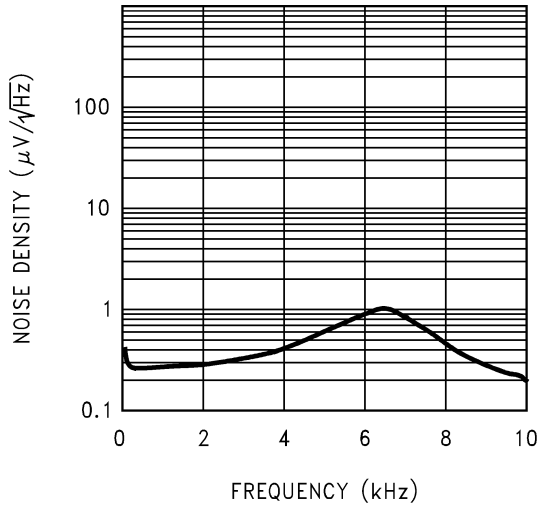
10129125

**Noise Spectral Density (0.1Hz-10Hz)**



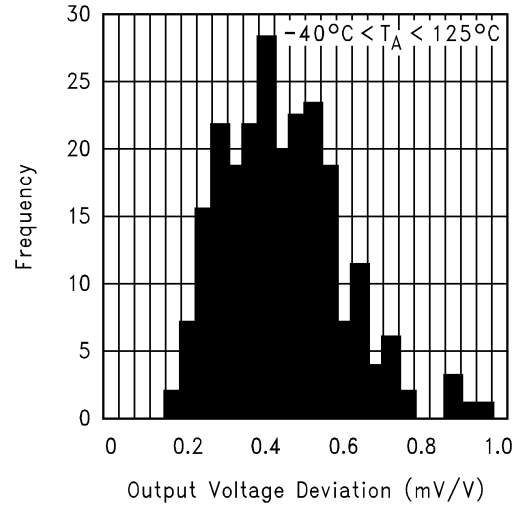
10129126

**Noise Spectral Density (10Hz-10kHz)**



10129127

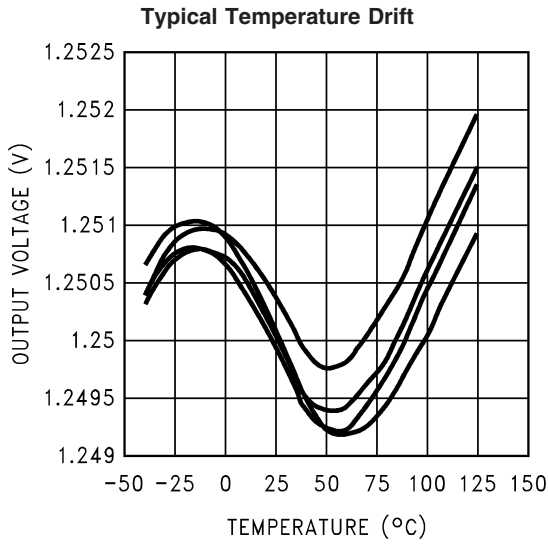
**Thermal Hysteresis**



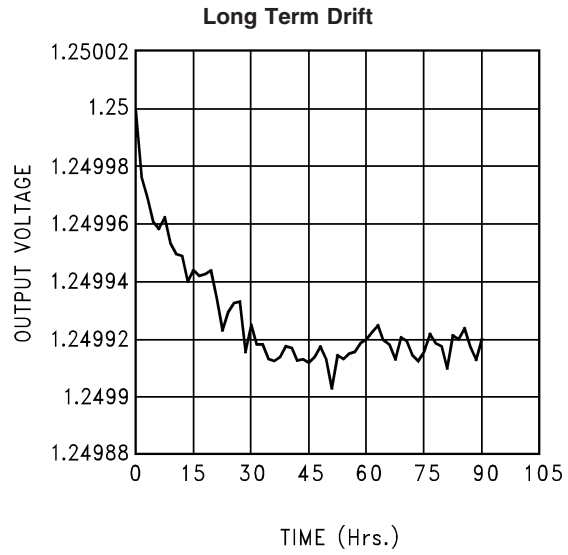
10129146

# LM4121-1.25 Typical Operating Characteristics

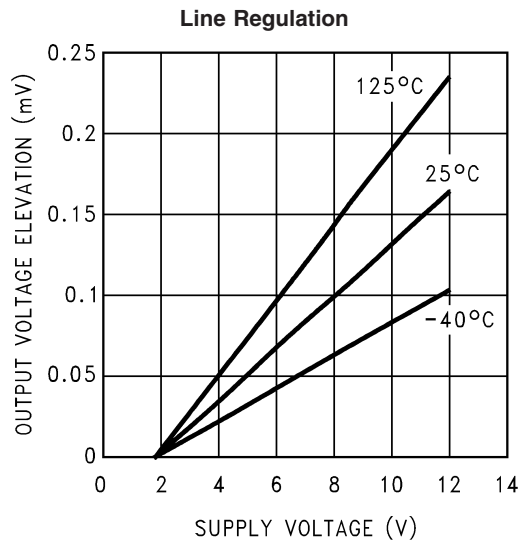
Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.25V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ .



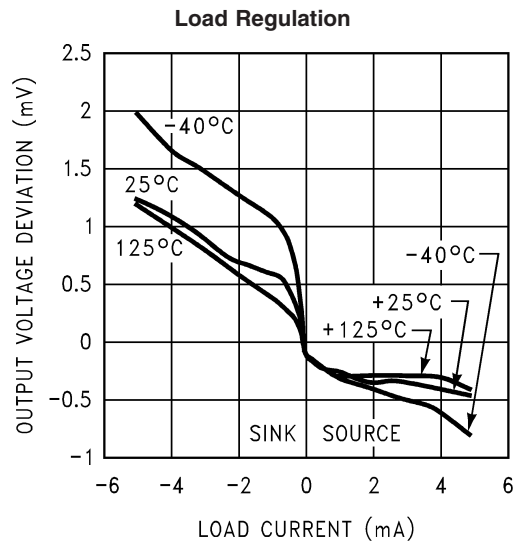
10129128



10129129



10129130

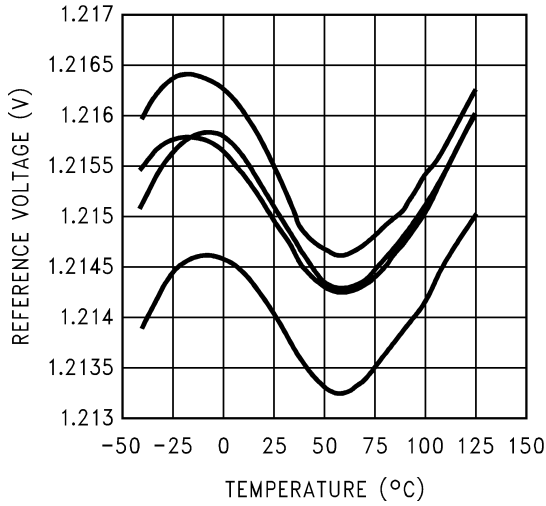


10129131

# LM4121-ADJ Typical Operating Characteristics

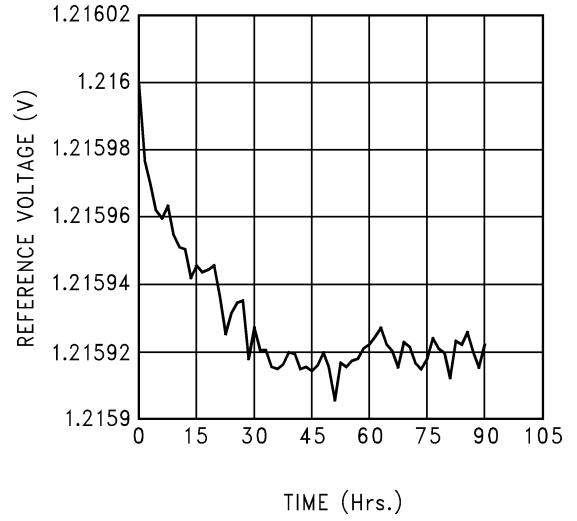
Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ .

**Typical Temperature Drift**



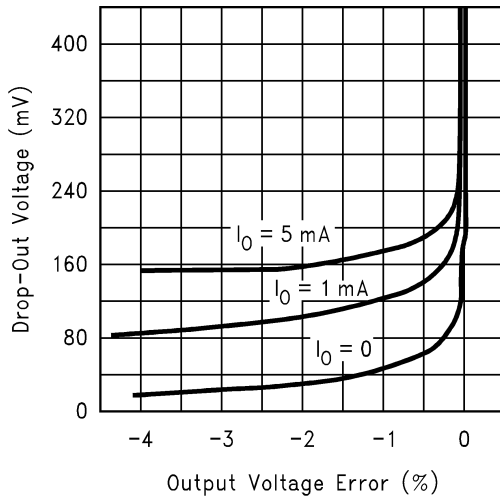
10129116

**Long Term Drift**



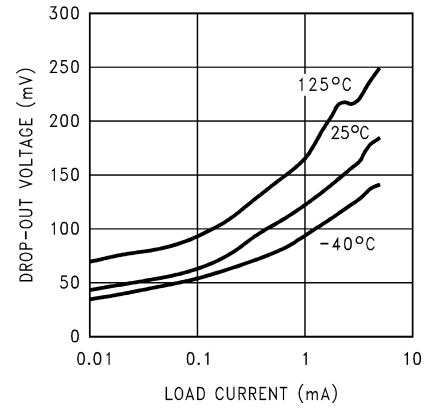
10129136

**Dropout Voltage vs Output Error**



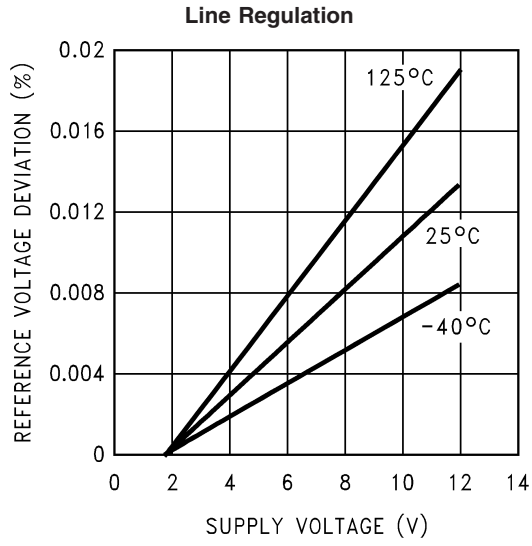
10129147

**Dropout Voltage vs Load Current**

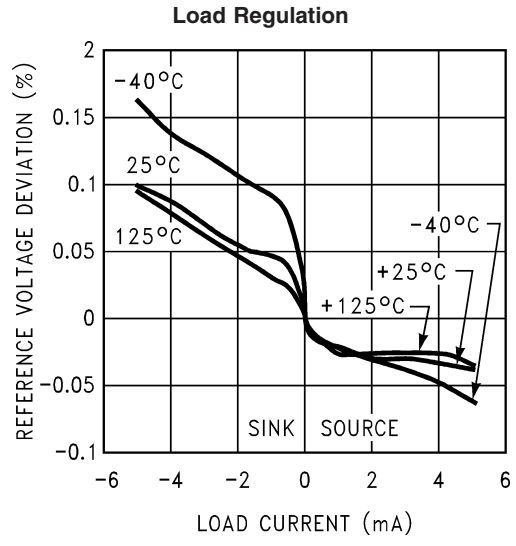


10129148

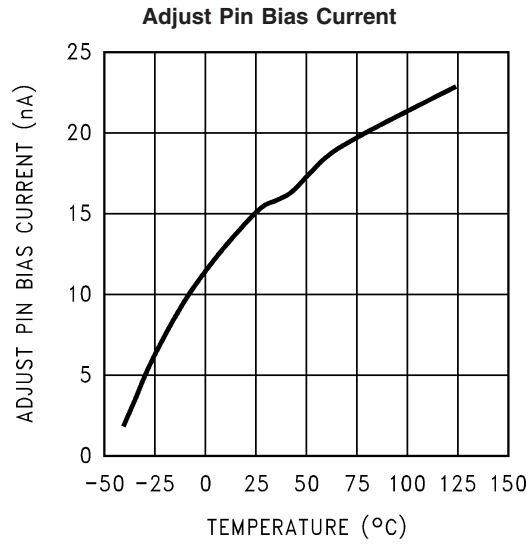
**LM4121-ADJ Typical Operating Characteristics** Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ . (Continued)



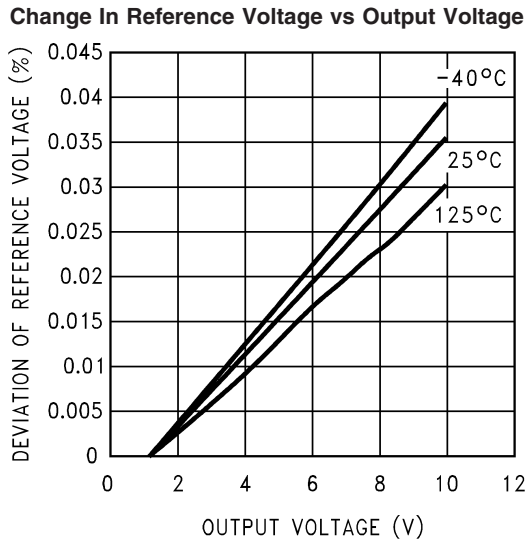
10129137



10129138

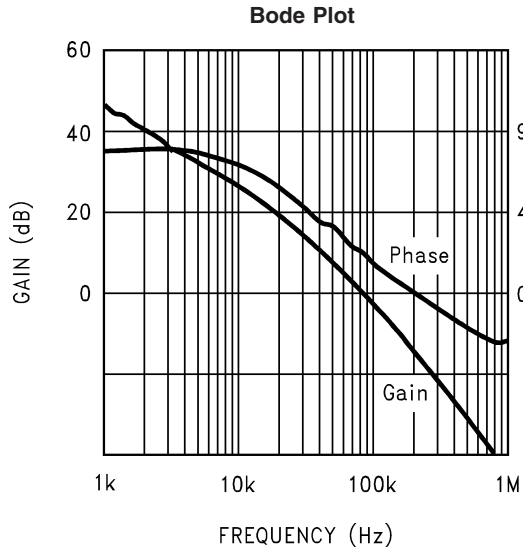


10129139

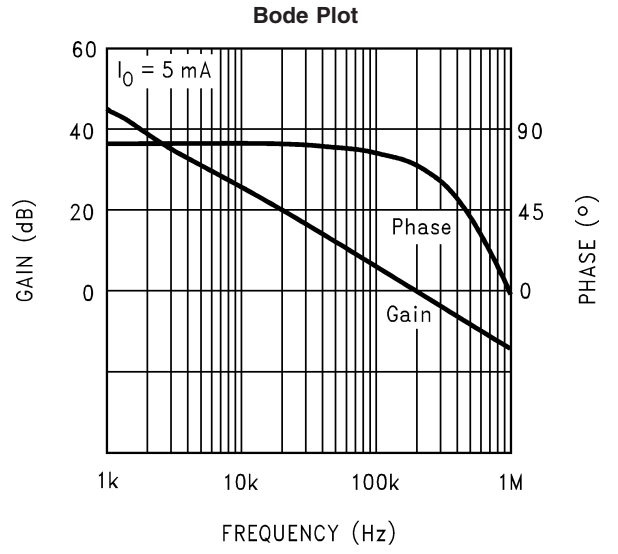


10129140

**LM4121-ADJ Typical Operating Characteristics** Unless otherwise specified,  $V_{IN} = 3.3V$ ,  $V_{OUT} = 1.2V$ ,  $I_{LOAD} = 0$ ,  $C_{OUT} = 0.022\mu F$ ,  $T_A = 25^\circ C$  and  $V_{EN} = V_{IN}$ . (Continued)



10129141



10129142

## Pin Functions

**Output (Pin 5):** Reference Output.

**Input (Pin 4):** Positive Supply.

**Ground (Pin 2):** Negative Supply or Ground Connection.

**Enable (Pin 3):** Pulled to input for normal operation. Forcing this pin to ground will turn-off the output.

**REF (Pin 1):** REF Pin (1.25V option only). This pin should be left unconnected for 1.25V option.

**Adj (Pin 1):**  $V_{OUT}$  Adj Pin (Adjustable option only). See Application Hints section.

## Application Hints

The standard application circuit for the LM4121 is shown in *Figure 1*. The output voltage is set with the two feedback resistors, according to the following formula:

$$V_{OUT} = [V_{ref}(1 + R1/R2)] - I_{bias} \cdot R1$$

Values for R1 and R2 should be chosen to be less than 1 M $\Omega$ .  $I_{bias}$  typically flows out of the adjust pin. Values for  $V_{ref}$  and  $I_{bias}$  are found in the Electrical Characteristics Spec. table. For best accuracy, be sure to take into account the variation of  $V_{REF}$  with input voltage, load and output voltage.

The LM4121 is designed to be stable with ceramic output capacitors in the range of 0.022 $\mu$ F to 0.047 $\mu$ F. Note that 0.022 $\mu$ F is the minimum required output capacitor. These capacitors typically have an ESR of about 0.1 to 0.5 $\Omega$ . Smaller ESR can be tolerated, however larger ESR can not. The output capacitor can be increased to improve load transient response, up to about 1 $\mu$ F. However, values above 0.047 $\mu$ F must be tantalum. With tantalum capacitors, in the 1 $\mu$ F range, a small capacitor between the output and the reference (Adj) pin is required. This capacitor will typically be in the 50pF range. Care must be taken when using output capacitors of 1 $\mu$ F or larger. These application must be thoroughly tested over temperature, line and load. Also, when the LM4121 is used as a controller, with external active components, each application must be carefully tested to ensure a stable design. The adjust pin is sensitive to noise and capacitive loading. The trace to this pin must be as short as possible and the feedback resistors should be close to this pin. Also, a single point ground to the LM4121 will help ensure good accuracy at high load currents.

An input capacitor is typically not required. However, a 0.1 $\mu$ F ceramic can be used to help prevent line transients from entering the LM4121. Larger input capacitors should be tantalum or aluminium.

The enable pin is an analog input with very little hysteresis. About 6 $\mu$ A into this pin is required to turn the part on, and it must be taken close to GND to turn the part off (see spec. table for thresholds). There is a *minimum* slew rate on this pin of about 0.003V/ $\mu$ S to prevent glitches on the output. All of these conditions can easily be met with ordinary CMOS or TTL logic. If the shutdown feature is not required, then this pin can safely be connected directly to the input supply. Floating this pin is not recommended.

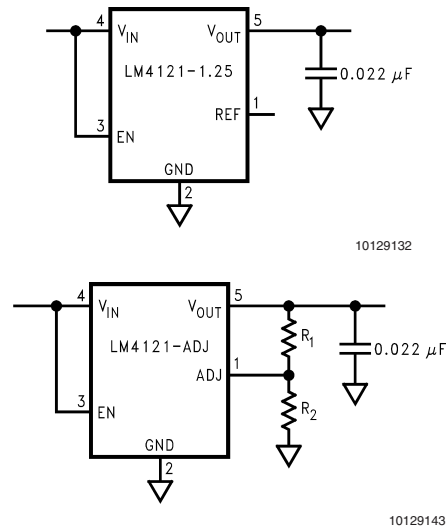


FIGURE 1. Standard Application Circuit

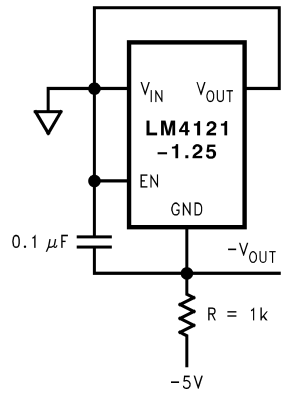
### PRINTED CIRCUIT BOARD LAYOUT CONSIDERATION

The mechanical stress due to PC board mounting can cause the output voltage to shift from its initial value. References in SOT packages are generally less prone to assembly stress than devices in Small Outline (SOIC) package.

To reduce the stress-related output voltage shifts, mount the reference on the low flex areas of the PC board such as near to the edge or the corner of the PC board.

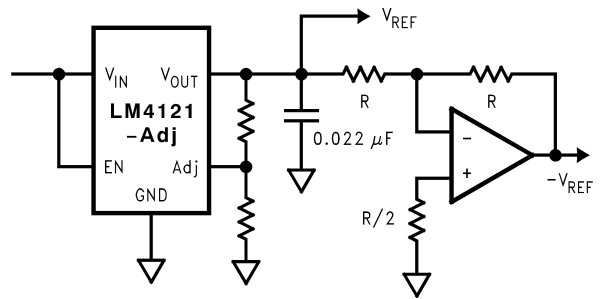
# Typical Application Circuits

Voltage Reference with Negative Output



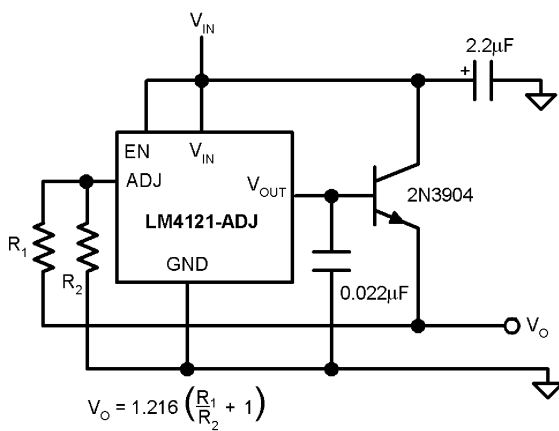
10129103

Voltage Reference with Complimentary Output



10129106

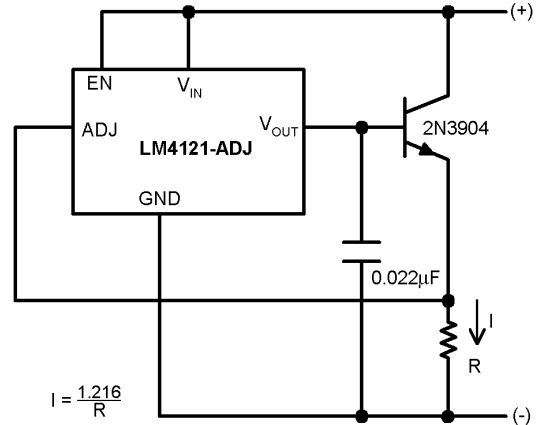
100mA Quasi-LDO Regulator



10129104

$$V_O = 1.216 \left( \frac{R_1}{R_2} + 1 \right)$$

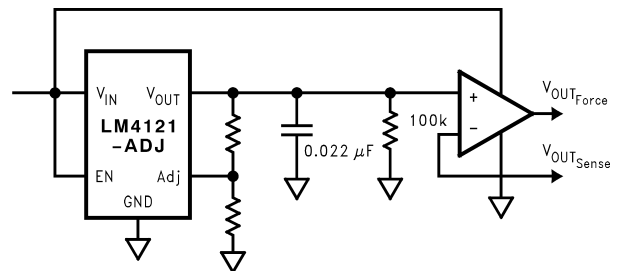
Two Terminal Constant Current Source



10129107

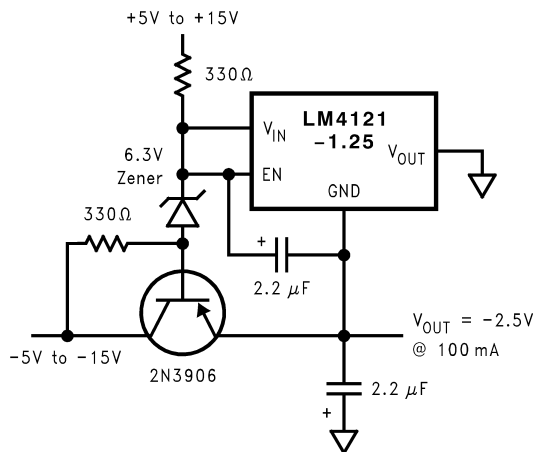
$$I = \frac{1.216}{R}$$

Precision Voltage Reference with Force and Sense Output



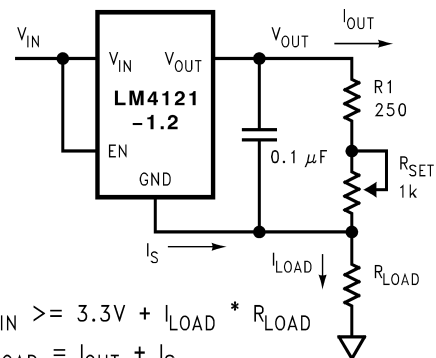
10129109

Boosted Output Current with Negative Voltage Reference



10129105

Programmable Current Source



$$V_{IN} \geq 3.3V + I_{LOAD} * R_{LOAD}$$

$$I_{LOAD} = I_{OUT} + I_S$$

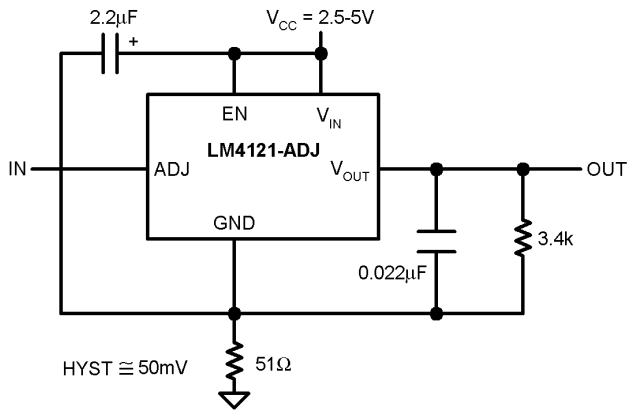
$$I_{OUT} = V_{OUT} / (R_1 + R_{SET})$$

10129110

# Typical Application Circuits

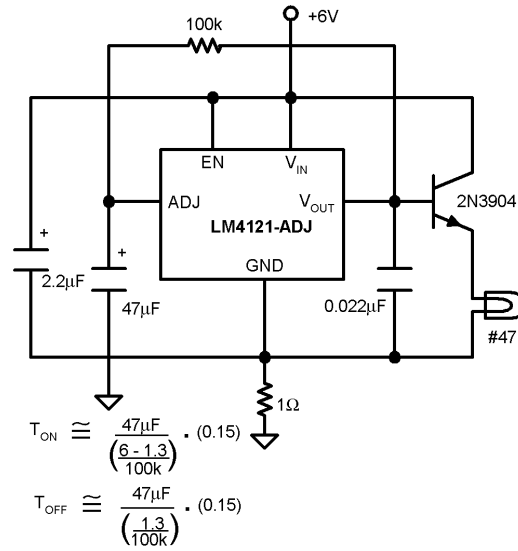
(Continued)

## Precision Comparator with Hysteresis



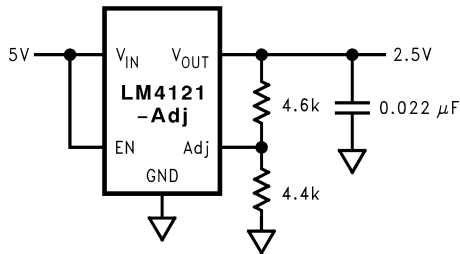
10129111

## Flasher Circuit



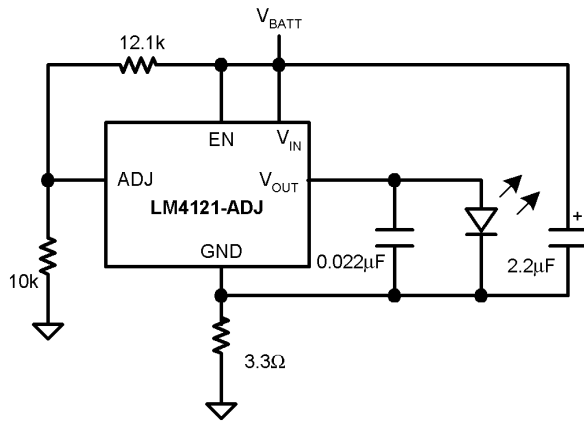
10129145

## Power Supply Splitter



10129120

## Li + Low Battery Detector



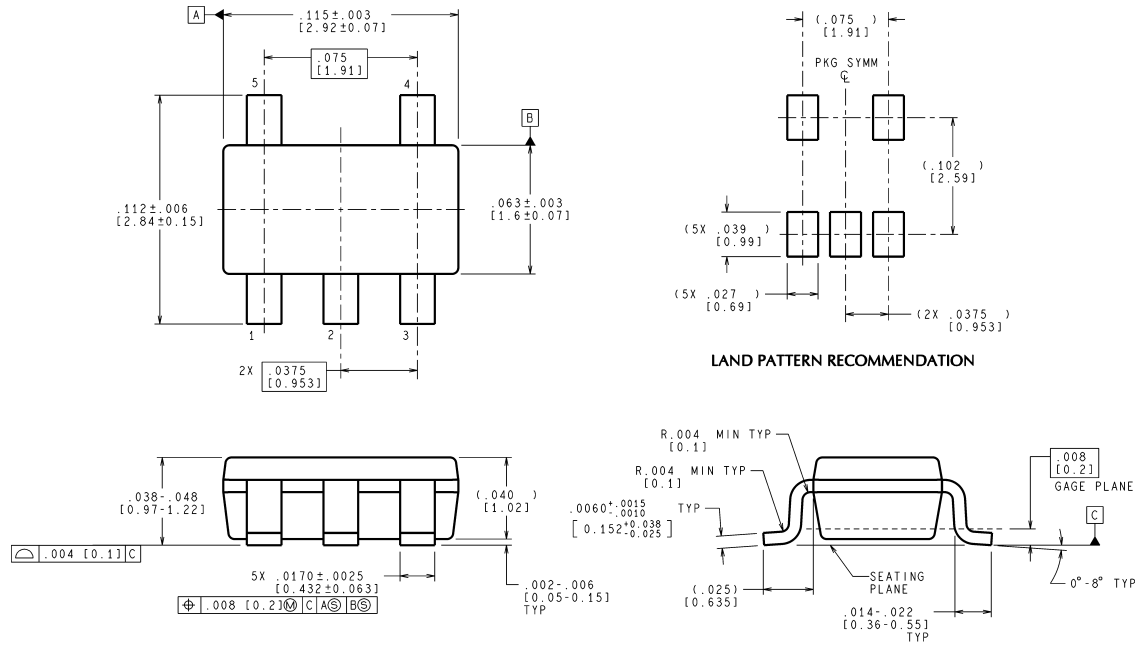
LED = ON @ ≅ 2.7V  
LED = OFF @ ≅ 2.8V

10129144



## Physical Dimensions inches (millimeters)

unless otherwise noted



CONTROLLING DIMENSION IS INCH  
VALUES IN [ ] ARE MILLIMETERS

MF05A (Rev B)

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications.

For the most current product information visit us at [www.national.com](http://www.national.com).

### LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### BANNED SUBSTANCE COMPLIANCE

National Semiconductor certifies that the products and packing materials meet the provisions of the Customer Products Stewardship Specification (CSP-9-111C2) and the Banned Substances and Materials of Interest Specification (CSP-9-111S2) and contain no "Banned Substances" as defined in CSP-9-111S2.



**National Semiconductor**  
Americas Customer  
Support Center  
Email: [new.feedback@nsc.com](mailto:new.feedback@nsc.com)  
Tel: 1-800-272-9959

**National Semiconductor**  
Europe Customer Support Center  
Fax: +49 (0) 180-530 85 86  
Email: [europe.support@nsc.com](mailto:europe.support@nsc.com)  
Deutsch Tel: +49 (0) 69 9508 6208  
English Tel: +44 (0) 870 24 0 2171  
Français Tel: +33 (0) 1 41 91 8790

**National Semiconductor**  
Asia Pacific Customer  
Support Center  
Email: [ap.support@nsc.com](mailto:ap.support@nsc.com)

**National Semiconductor**  
Japan Customer Support Center  
Fax: 81-3-5639-7507  
Email: [jpn.feedback@nsc.com](mailto:jpn.feedback@nsc.com)  
Tel: 81-3-5639-7560

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

### Products

Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>
DLP® Products	<a href="http://www.dlp.com">www.dlp.com</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>
Clocks and Timers	<a href="http://www.ti.com/clocks">www.ti.com/clocks</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>
RFID	<a href="http://www.ti-rfid.com">www.ti-rfid.com</a>
OMAP Mobile Processors	<a href="http://www.ti.com/omap">www.ti.com/omap</a>
Wireless Connectivity	<a href="http://www.ti.com/wirelessconnectivity">www.ti.com/wirelessconnectivity</a>

### Applications

Communications and Telecom	<a href="http://www.ti.com/communications">www.ti.com/communications</a>
Computers and Peripherals	<a href="http://www.ti.com/computers">www.ti.com/computers</a>
Consumer Electronics	<a href="http://www.ti.com/consumer-apps">www.ti.com/consumer-apps</a>
Energy and Lighting	<a href="http://www.ti.com/energy">www.ti.com/energy</a>
Industrial	<a href="http://www.ti.com/industrial">www.ti.com/industrial</a>
Medical	<a href="http://www.ti.com/medical">www.ti.com/medical</a>
Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Space, Avionics and Defense	<a href="http://www.ti.com/space-avionics-defense">www.ti.com/space-avionics-defense</a>
Transportation and Automotive	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
Video and Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>

TI E2E Community Home Page

[e2e.ti.com](http://e2e.ti.com)

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2011, Texas Instruments Incorporated