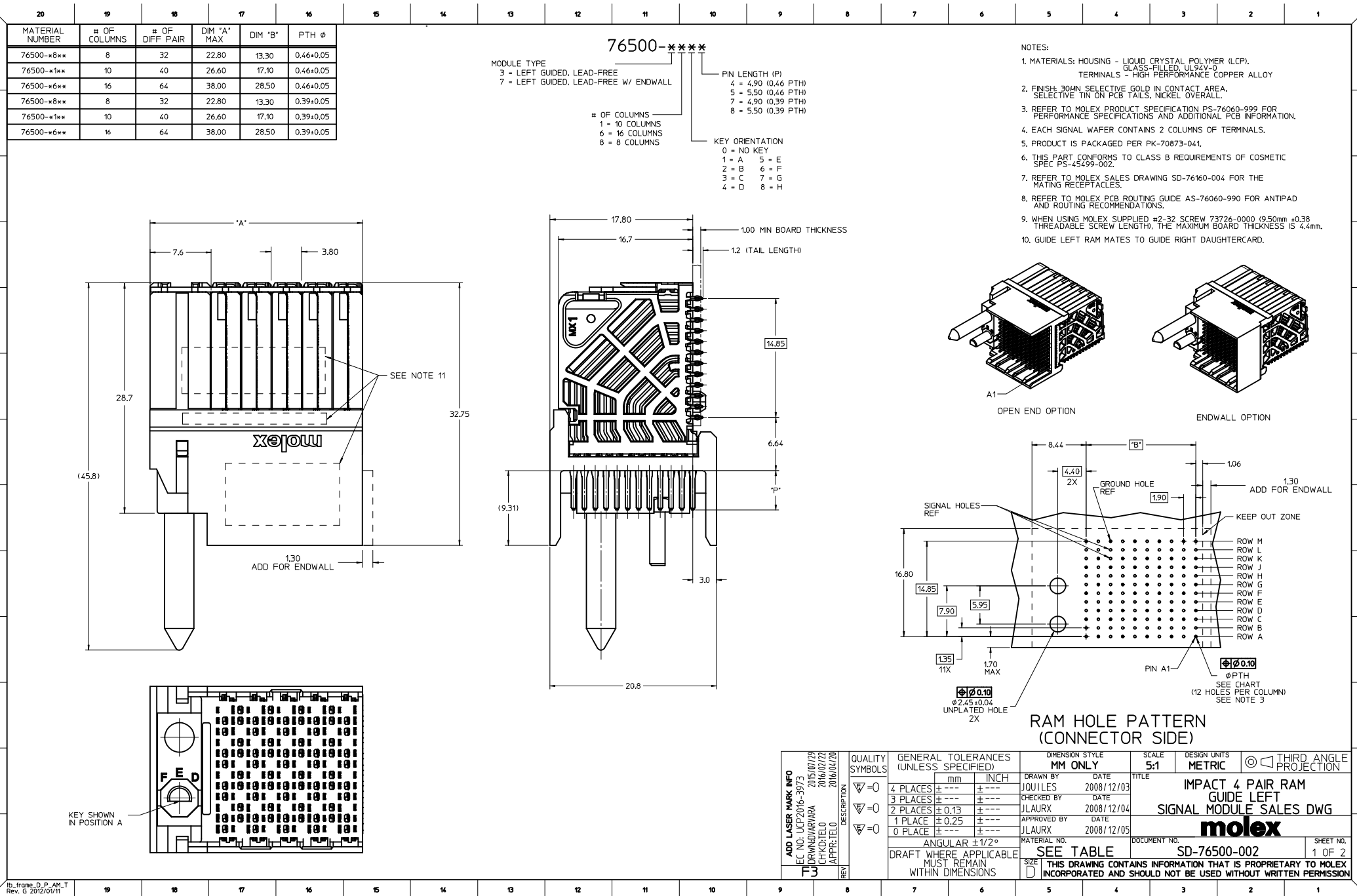
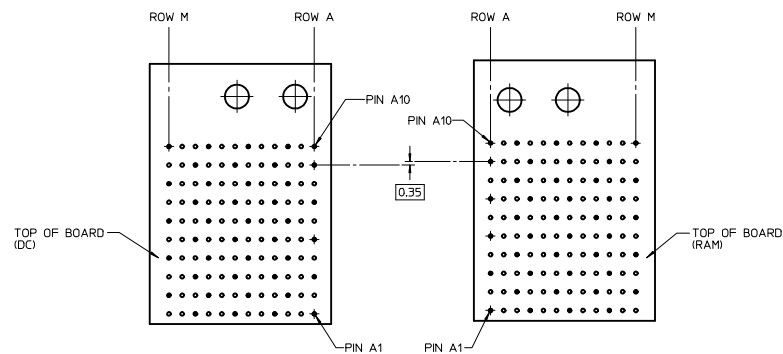
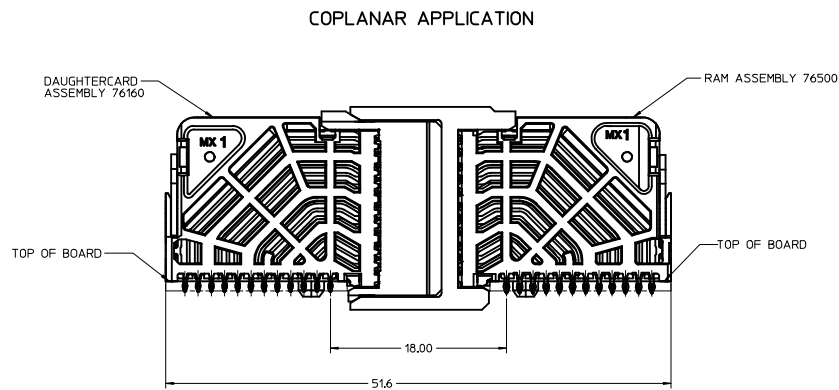


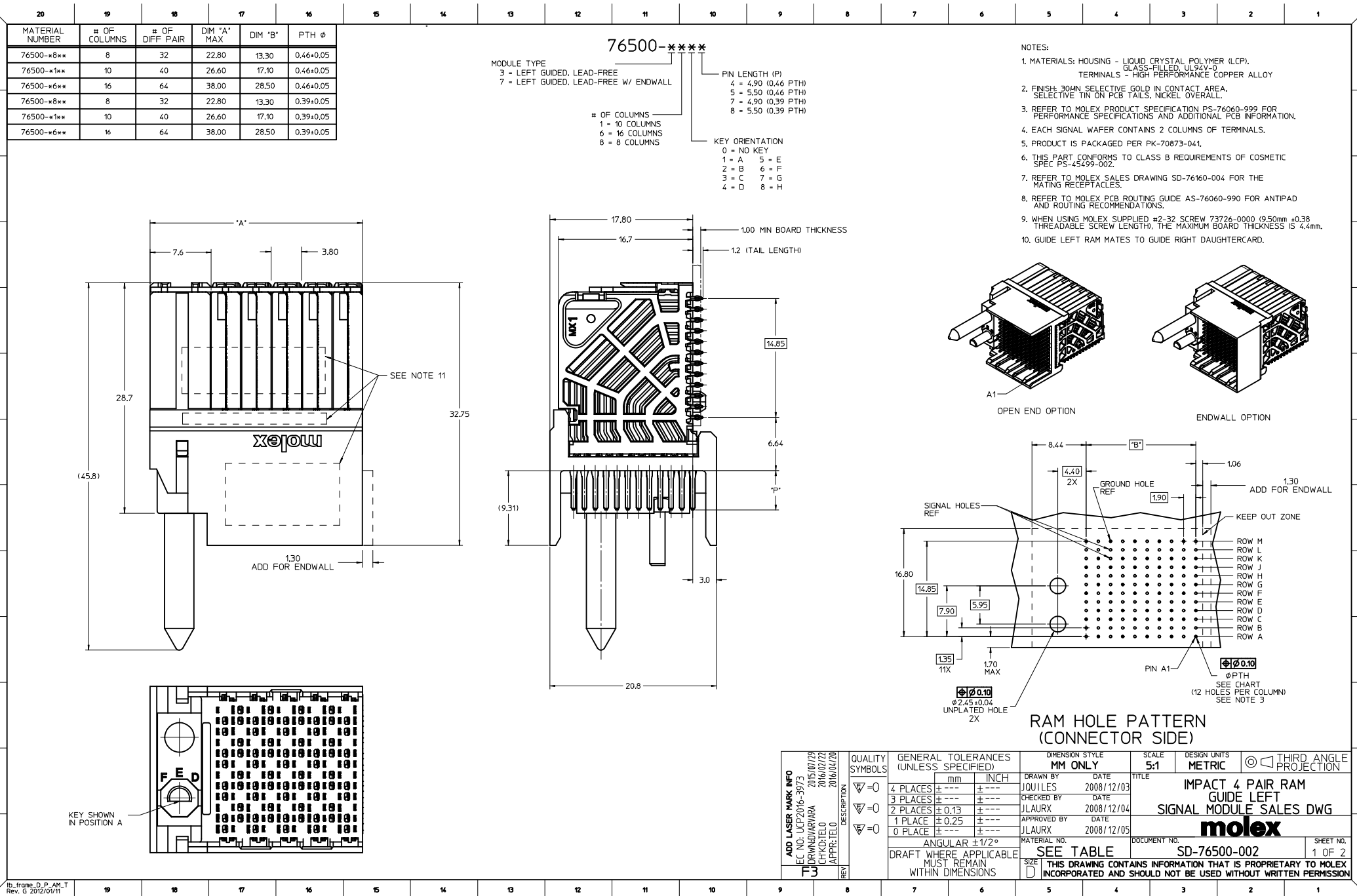


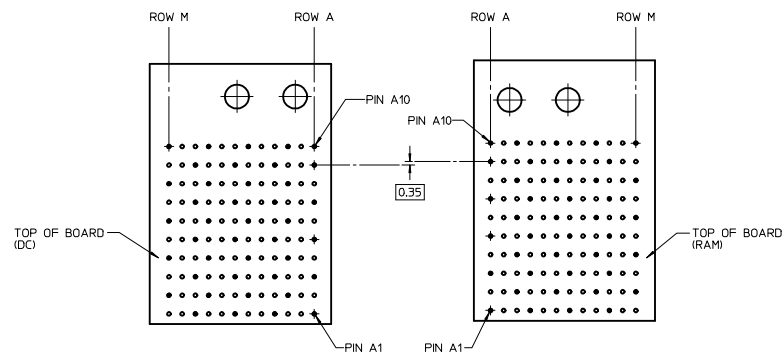
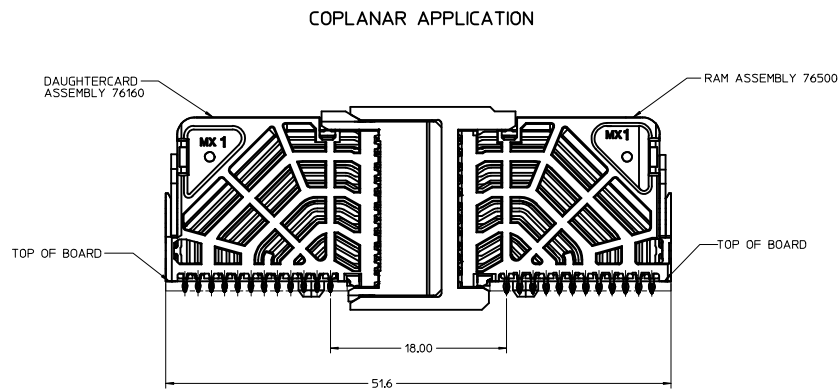


COPLANAR MATED SIGNAL PATHS

DC PIN A10-----RAM PIN A10
DC PIN A1-----RAM PIN A1

SEE SHEET 1 EEC NO: UCP2016-3973 DRAWN: VARARA CHKD: TEL APPR: TEL REV	QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE		SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	TITLE IMPACT 4 PAIR RAM GUIDE LEFT SIGNAL MODULE SALES DWG 	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-76500-002	SHEET NO. 2 OF 2
	$\nabla=0$ $\nabla=0$ $\nabla=0$		4 PLACES $\pm$ --- $\pm$ --- 3 PLACES $\pm$ --- $\pm$ --- 2 PLACES ± 0.13 $\pm$ --- 1 PLACE ± 0.25 $\pm$ --- 0 PLACE $\pm$ --- $\pm$ ---		MM ONLY mm INCH		4:1	METRIC					
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		ANGULAR $\pm 1/2^\circ$		DRAWN BY JQUILES		DATE 2008/12/03	DATE 2008/12/04					
					CHECKED BY JLAURX		DATE 2008/12/05	DATE 2008/12/05					





COPLANAR MATED SIGNAL PATHS

DC PIN A10-----RAM PIN A10
DC PIN A1-----RAM PIN A1

SEE SHEET 1 DEC NO: UCP2016-3973 DRAWN BY: ARVARRA CHKD BY: TELLO APPRO: TELLO REV:	QUALITY SYMBOLS	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE		SCALE	DESIGN UNITS	 THIRD ANGLE PROJECTION
					MM ONLY		4:1	METRIC	
							TITLE		
	▽=0		mm	INCH	DRAWN BY	DATE	IMPACT 4 PAIR RAM		
	▽=0		4 PLACES ± --- ± ---		JOUILLES	2008/12/03	GUIDE LEFT		
	▽=0		3 PLACES ± --- ± ---		CHECKED BY	DATE	SIGNAL MODULE SALES DWG		
	▽=0		2 PLACES ± 0.13 ± ---		JLAURX	2008/12/04			
	▽=0		1 PLACE ± 0.25 ± ---		APPROVED BY	DATE			
	▽=0		0 PLACE ± --- ± ---		JLAURX	2008/12/05			
			ANGULAR ±1/2°		MATERIAL NO.		DOCUMENT NO.		SHEET NO.
			DRAFT WHERE APPLICABLE		SEE TABLE		SD-76500-002		2 OF 2
			MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION