

DMN3065LW

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Package	I _{D max} T _A = +25°C
	52mΩ @ V _{GS} = 10V		
30V	65mΩ @ V _{GS} = 4.5V	SOT323	4A
	85mΩ @ V _{GS} = 2.5V		

- Applications
- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

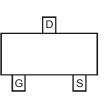
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)

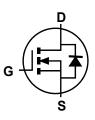




Top View



Pin Configuration Top View



Equivalent Circuitl

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3065LW-7	SOT323	3000/Tape & Reel
DMN3065LW-13	SOT323	10000/Tape & Reel

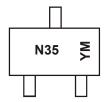
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



N35 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date	Code	Key
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Year	2012	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		А		В	()	D		E		F
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±12	V
Drain Current (Note 5)	ID	4	А
Body-Diode Continuous Current (Note 5)	I _S	1	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	770	mW
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{θJA}	162	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.) Characteristic Symbol Min Тур Max Unit **Test Condition OFF CHARACTERISTICS (Note 6)** Drain-Source Breakdown Voltage 30 V $V_{GS} = 0V, I_D = 250 \mu A$ BV_{DSS} Zero Gate Voltage Drain Current μA $V_{DS} = 30V, V_{GS} = 0V$ 1 I_{DSS} ____ ____ Gate-Body Leakage ±100 nA V_{GS} = ±12V, V_{DS} = 0V Igss **ON CHARACTERISTICS (Note 6)** Gate Threshold Voltage V_{GS(th)} 0.5 1.5 V $V_{DS} = V_{GS}, I_D = 250 \mu A$ ____ $V_{GS} = 10V, I_D = 4A$ 52 Static Drain-Source On-Resistance 65 mΩ V_{GS} = 4.5V, I_D = 3A RDS (ON) ____ _ 85 V_{GS} = 2.5V, I_D = 2A Source-Drain Diode Forward Voltage 1.2 V V_{GS} = 0V, I_S = 2.0A V_{SD} **DYNAMIC CHARACTERISTICS(7)** Input Capacitance Ciss ____ 465 ____ pF VDS = 15V, VGS = 0V, f = **Output Capacitance** Coss 49.5 pF 1.0MHz Reverse Transfer Capacitance Crss 43.8 pF ____ ____ Gate Resistance Rg 2.3 Ω V_{DS} =0V, V_{GS} = 0V, f = 1MHz Total Gate Charge (V_{GS}=10V) 11.7 nC V_{DS} = 15V, I_D = 4 A Qg ____ ____ Total Gate Charge (V_{GS}=4.5V) Qq 5.5 nC Gate-Source Charge Q_{gs} 1.1 nC V_{DS} = 15V, I_D = 4 A ____ ____ Gate-Drain Charge Q_{gd} ____ 1.8 nC ____ Turn-On Delay Time t_{D(on)} 1.9 ns ____ Turn-On Rise Time tr ____ 1.6 ____ ns $V_{DD} = 15V, V_{GEN} = 10V,$ Turn-Off Delay Time $R_{GEN} = 3\Omega, R_L = 3.75\Omega$ 10.3 ____ ns $t_{D(off)}$ ____ Turn-Off Fall Time tf 2.0 ____ ns

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout

6. Short duration pulse test used to minimize self-heating effect.

7. Guaranteed by design. Not subject to production testing.



DMN3065LW

__ = 25°C

-55°C

3

T_A = 150°C

 $T_A = 25^{\circ}C$

T_A = 8⁵°C

T_A = -55°C

16

V_{GS} = 4.5V I_D = 3A

V_{GS} = 10V I_D = 6A

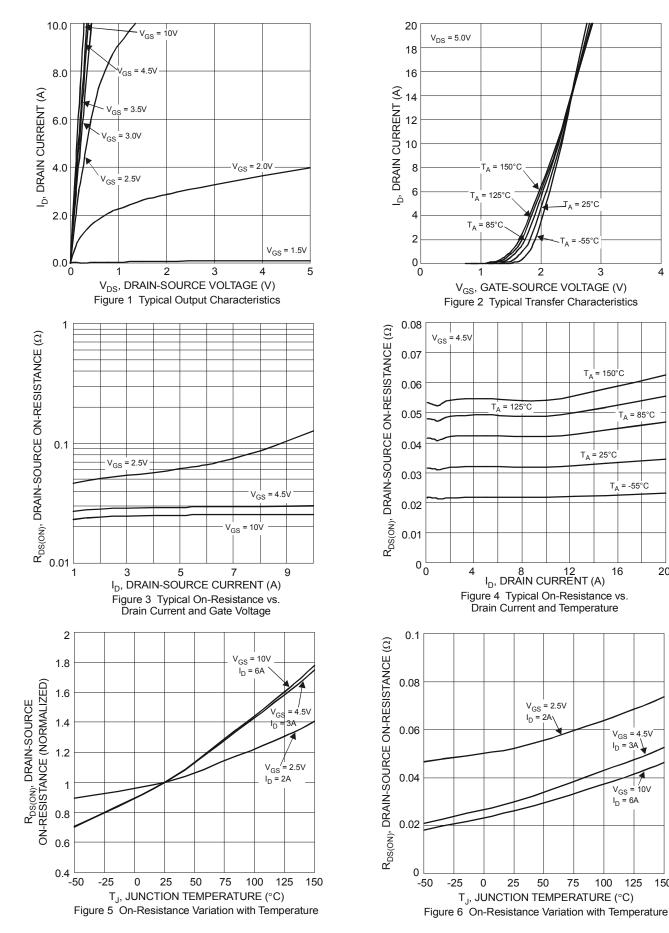
125

75

100

20

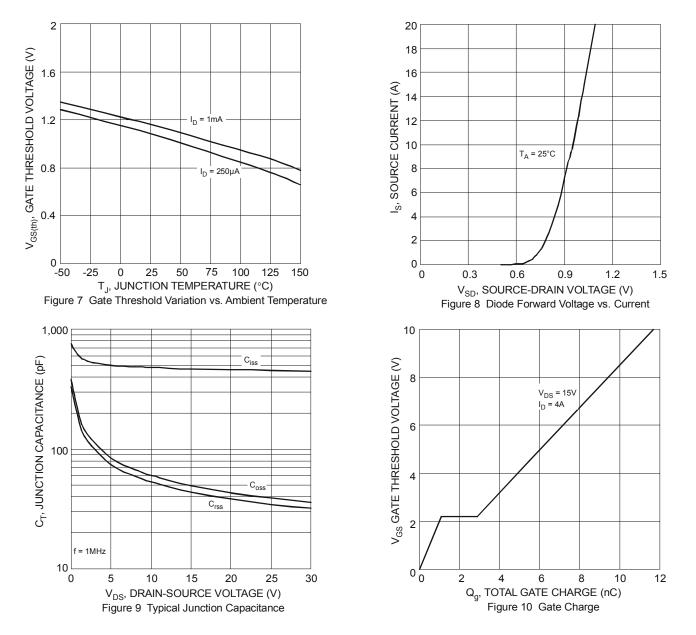
4



150

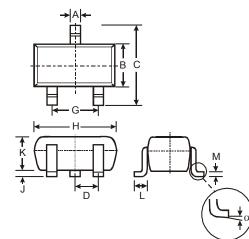


DMN3065LW



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

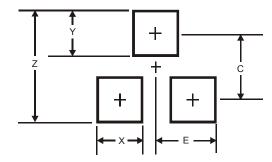


SOT323						
Dim	Min Max Typ					
Α	0.25	0.40	0.30			
В	1.15	1.35	1.30			
С	2.00	2.20	2.10			
D	-	-	0.65			
G	1.20	1.40	1.30			
Н	1.80	2.20	2.15			
J	0.0	0.10	0.05			
K	0.90	1.00	1.00			
L	0.25	0.40	0.30			
М	0.10	0.18	0.11			
α	0°	8°	-			
All Dimensions in mm						



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Y	0.9
С	1.9
E	1.0

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