SS12P4C

Vishay General Semiconductor

High Current Density Surface-Mount Schottky Barrier Rectifier



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SMPC (TO-277A)

K Anode 1 Cathode ◄ Anode 2

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 6.0 A			
V _{RRM}	40 V			
I _{FSM}	150 A			
E _{AS}	20 mJ			
V_F at $I_F = 6.0$ A	0.40 V			
T _J max.	125 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

FEATURES

- Very low profile typical height of 1.1 mm
- · Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency
- Low thermal impedance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3_X - halogen-free, RoHS-compliant and AEC-Q101 qualified

("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SS12P4C	UNIT			
Device marking code			S124C			
Maximum repetitive peak reverse voltage		V _{RRM}	40	V		
Maximum average forward rectified current (fig. 1) $^{(1)}$	total device	I _{F(AV)}	12	А		
	per diode		6.0			
Maximum average forward rectified current ⁽²⁾	total device	I _{F(AV)}	3.5	А		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	150	A		
Non-repetitive avalanche energy at T_J = 25 °C, L = 60 mH per diode		E _{AS}	20	mJ		
Peak repetitive reverse current at $t_p = 2 \ \mu s$, 1 kHz, at $T_J = 25 \ ^\circ C$ per diode		I _{RRM}	1.0	А		
Operating junction and storage temperature range		T _{J,} T _{STG}	-55 to +125	°C		

Notes

 $^{(1)}\,$ Mounted on 30 mm x 30 mm Al PCB with 50 mm x 25 mm x 100 mm fin heat sink

⁽²⁾ Free air, mounted on recommended copper pad area

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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 1 A	T _A = 25 °C	V _F (1)	0.34	-	V
	I _F = 3 A			0.40	-	
	I _F = 6 A			0.46	0.52	
	I _F = 1 A	T _A = 100 °C		0.24	-	
	I _F = 3 A			0.31	-	
	I _F = 6 A			0.40	0.45	
Reverse current per diode	Rated V _R	T _A = 25 °C T _A = 100 °C	I _R ⁽²⁾	129	500	μA
	naled VR			11.9	25	mA
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	400	-	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	SS12P4C	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	100	°C/W	
	R _{0JM} ⁽²⁾	3		

Notes

 $^{(1)}$ Free air, mounted on recommended copper pad area. Thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ Mounted on 30 mm x 30 mm AI PCB with 50 mm x 25 mm x 100 mm fin heat sink. Thermal resistance R_{0JM} - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SS12P4C-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel		
SS12P4C-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel		
SS12P4CHM3_A/H ⁽¹⁾	0.10	Н	1500	7" diameter plastic tape and reel		
SS12P4CHM3_A/I (1)	0.10	I	6500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

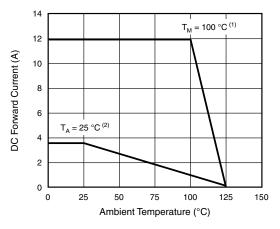


Fig. 1 - Maximum Forward Current Derating Curve

Notes

- Mounted on 30 mm x 30 mm Al PCB with 50 mm x 25 mm x 100 mm fin heat sink, T_M measured at the terminal of cathode band ($R_{\theta JM} = 3 \text{ °C/W}$)
- Free air, mounted on recommended copper pad area ($R_{\theta JA} = 100 \text{ °C/W}$)

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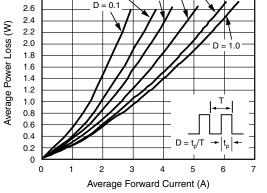


Fig. 2 - Forward Power Loss Characteristics Per Diode

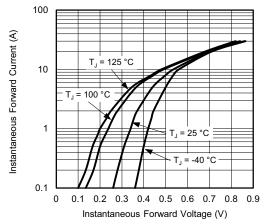
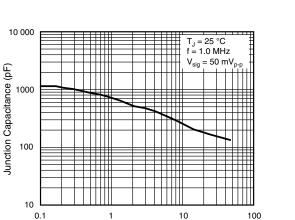


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode



Reverse Voltage (V) Fig. 5 - Typical Junction Capacitance Per Diode

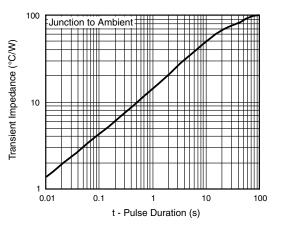
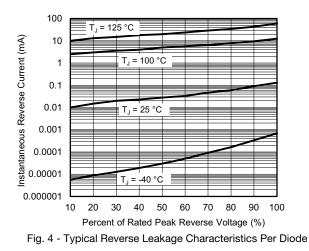


Fig. 6 - Typical Transient Thermal Impedance Per Diode



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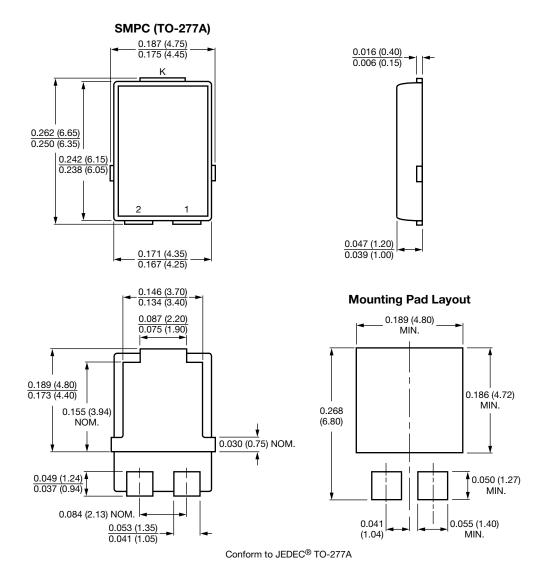
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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