LM120/LM320-N Series 3-Terminal Negative Regulators

Check for Samples: LM120, LM320-N

FEATURES

- **Preset Output Voltage Error Less than ±3%**
- **Preset Current Limit**
- **Internal Thermal Shutdown**
- **Operates with Input-Output Voltage Differential down to 1V**
- **Excellent Ripple Rejection**
- **Low Temperature Drift**
- **Easily Adjustable to Higher Output Voltage**

DESCRIPTION

The LM120 series are three-terminal negative regulators with a fixed output voltage of −5V, −12V, and −15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of −1.2V to −47V.

The LM120 need only one external component—a compensation capacitor at the output, making them easy to apply. Worst case specifications on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

Table 1. LM120 Series Packages and Power Capability

Device	Package	Rated Power Dissipation	Design Load Current
LM120/LM320-N	TO-3 (NDS)	20W	1.5A
	TO (NDT)	2W	0.5A
LM320-N	TO-220 (NDE)	15W	1.5A

Typical Applications

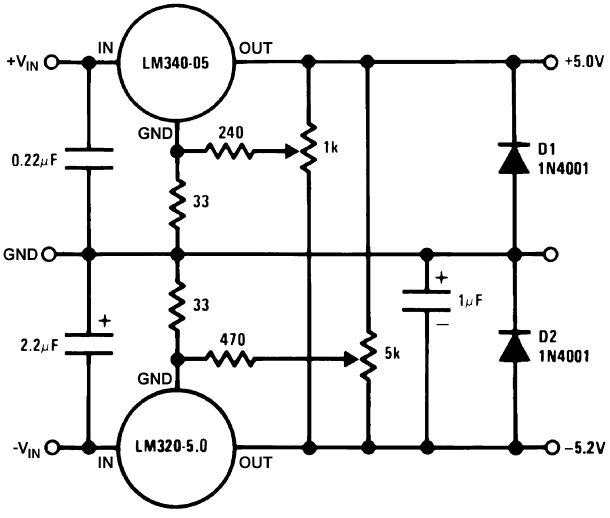
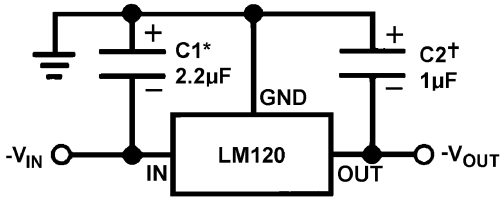


Figure 1. Dual Trimmed Supply



*Required if regulator is separated from filter capacitor by more than 3 inches. For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100 µF, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 2. Fixed Regulator

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS−5 VOLT REGULATORS(1)(2)(3)

Power Dissipation	Internally Limited
Input Voltage	−25V
Input-Output Voltage Differential	25V
Junction Temperatures	(4)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C
Plastic	260°C

(1) Refer to RETS120-5H drawing for LM120H-5.0 or RETS120-5K drawing for LM120-5K military specifications.
(2) For −5V 3 amp regulators, see LM145 data sheet.
(3) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
(4) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120K-5.0 AND LM320K-5.0 ELECTRICAL CHARACTERISTICS(1)

Order Numbers		Metal Can Package						Units
		LM120K-5.0 (TO-3)			LM320K-5.0 (TO-3)			
Design Output Current (I_D) Device Dissipation (P_D)		1.5A 20W						
Parameter	Conditions (2)	Min	Typ	Max	Min	Typ	Max	
Output Voltage	T_J = 25°C, V_{IN} =10V, I_{LOAD} = 5 mA	−5.1	−5	−4.9	−5.2	−5	−4.8	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		10	25		10	40	mV
Input Voltage		−25		−7	−25		−7	V
Ripple Rejection	f = 120 Hz	54	64		54	64		dB
Load Regulation, (3)	T_J = 25°C, V_{IN} = 10V, 5 mA ≤ I_{LOAD} ≤ I_D		50	75		60	100	mV
Output Voltage, (2)	−7.5V ≤ V_{IN} ≤ V_{MAX}, 5 mA ≤ I_{LOAD} ≤ I_D, P ≤ P_D	−5.20		−4.80	−5.25		−4.75	V
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2		1	2	mA
Quiescent Current	T_J = 25°C							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	5 mA ≤ I_{LOAD} ≤ I_D		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 10V, 10 Hz ≤ f ≤ 100 kHz		150			150		µV
Long Term Stability			5	50		5	50	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

(1) For −5V 3 amp regulators, see LM145 data sheet.
(2) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(3) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

LM120H-5.0 ELECTRICAL CHARACTERISTICS[1]

Order Numbers		Metal Can Package			Units
		LM120H-5.0 (TO)			
Design Output Current (I_D) Device Dissipation (P_D)		0.5A 2W			
Parameter	Conditions [2]	Min	Typ	Max	
Output Voltage	T_J = 25°C, V_{IN} =10V, I_{LOAD} = 5 mA	−5.1	−5	−4.9	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		10	25	mV
Input Voltage		−25		−7	V
Ripple Rejection	f = 120 Hz	54	64		dB
Load Regulation, [3]	T_J = 25°C, V_{IN} = 10V, 5 mA ≤ $I_{LOAD} \le I_D$		30	50	mV
Output Voltage, [4]	−7.5V ≤ $V_{IN} \le V_{MAX}$, 5 mA ≤ $I_{LOAD} \le I_D$, P ≤ P_D	−5.20		−4.80	V
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2	mA
Quiescent Current Change	T_J = 25°C				
	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA
	5 mA ≤ $I_{LOAD} \le I_D$		0.04	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 10V, 10 Hz ≤ f ≤ 100 kHz		150		µV
Long Term Stability			5		mV
Thermal Resistance					
Junction to Case				[5]	°C/W
Junction to Ambient				[5]	°C/W

(1) For −5V 3 amp regulators, see LM145 data sheet.
(2) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(3) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.
(4) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(5) Thermal resistance of typically 85°C/W (in 400 linear feet air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

ABSOLUTE MAXIMUM RATINGS−12 VOLT REGULATORS[1][2]

Power Dissipation	Internally Limited
Input Voltage	−35V
Input-Output Voltage Differential	30V
Junction Temperatures	[3]
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

(1) Refer to RETS120H-12 drawing for LM120H-12 or RETS120-12K drawing for LM120K-12 military specifications.
(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
(3) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120K-12 ELECTRICAL CHARACTERISTICS

Order Numbers		Metal Can Package			Units
		LM120K-12 (TO-3)			
Design Output Current (I_D)		1A			
Device Dissipation (P_D)		20W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
Output Voltage	T_J = 25°C, V_{IN} = 17V, I_{LOAD} = 5 mA	−12.3	−12	−11.7	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		4	10	mV
Input Voltage		−32		−14	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation, (2)	T_J = 25°C, V_{IN} = 17V, 5 mA ≤ $I_{LOAD} \le I_D$		30	80	mV
Output Voltage, (3)	14.5V ≤ $V_{IN} \le V_{MAX}$, 5 mA ≤ $I_{LOAD} \le I_D$, P ≤ P_D	−12.5		−11.5	V
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA
Quiescent Current Change	T_J = 25°C				
	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA
	5 mA ≤ $I_{LOAD} \le I_D$		0.1	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz		400		µV
Long Term Stability			12	120	mV
Thermal Resistance					
Junction to Case				3	°C/W
Junction to Ambient				35	°C/W

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.
(3) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120H-12 ELECTRICAL CHARACTERISTICS

Order Numbers		Metal Can Package			Units
		LM120H-12 (TO)			
Design Output Current (I_D)		0.2A			
Device Dissipation (P_D)		2W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
Output Voltage	T_J = 25°C, V_{IN} = 17V, I_{LOAD} = 5 mA	−12.3	−12	−11.7	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		4	10	mV
Input Voltage		−32		−14	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation,	T_J = 25°C, V_{IN} = 17V,		10	25	mV

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120H-12 ELECTRICAL CHARACTERISTICS (continued)

Order Numbers		Metal Can Package			Units
		LM120H-12 (TO)			
Design Output Current (I_D)		0.2A			
Device Dissipation (P_D)		2W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
(2)	5 mA ≤ I_{LOAD} ≤ I_D				
Output Voltage, (1)	14.5V ≤ V_{IN} ≤ V_{MAX}, 5 mA ≤ I_{LOAD} ≤ I_D, P ≤ P_D	−12.5		−11.5	V
Quiescent Current	V_{MIN} ≤ V_{IN} ≤ V_{MAX}		2	4	mA
Quiescent Current Change	T_J = 25°C				
	V_{MIN} ≤ V_{IN} ≤ V_{MAX}		0.05	0.4	mA
	5 mA ≤ I_{LOAD} ≤ I_D		0.03	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz		400		µV
Long Term Stability			12	120	mV
Thermal Resistance					
Junction to Case				(3)	°C/W
Junction to Ambient				(3)	°C/W

(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.
(3) Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

LM320T-12 ELECTRICAL CHARACTERISTICS

Order Numbers		Power Plastic Package			Units
		LM320T-12 (TO-220)			
Design Output Current (I_D)		1A			
Device Dissipation (P_D)		15W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
Output Voltage	T_J = 25°C, V_{IN} = 17V, I_{LOAD} = 5 mA	−12.4	−12	−11.6	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, V_{MIN} ≤ V_{IN} ≤ V_{MAX}		4	20	mV
Input Voltage		−32		−14.5	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation, (2)	T_J = 25°C, V_{IN} = 17V, 5 mA ≤ I_{LOAD} ≤ I_D		30	80	mV
Output Voltage, (1)	14.5V ≤ V_{IN} ≤ V_{MAX}, 5 mA ≤ I_{LOAD} ≤ I_D, P ≤ P_D	−12.6		−11.4	V
Quiescent Current	V_{MIN} ≤ V_{IN} ≤ V_{MAX}		2	4	mA
Quiescent Current Change	T_J = 25°C				
	V_{MIN} ≤ V_{IN} ≤ V_{MAX}		0.1	0.4	mA
	5 mA ≤ I_{LOAD} ≤ I_D		0.1	0.4	mA

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

LM320T-12 ELECTRICAL CHARACTERISTICS (continued)

Order Numbers		Power Plastic Package			Units
		LM320T-12 (TO-220)			
Design Output Current (I_D)		1A			
Device Dissipation (P_D)		15W			
Parameter	Conditions (1)	Min	Typ	Max	
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz		400		µV
Long Term Stability			24		mV
Thermal Resistance					
Junction to Case			4		°C/W
Junction to Ambient			50		°C/W

ABSOLUTE MAXIMUM RATINGS−15 VOLT REGULATORS(1)(2)

Power Dissipation	Internally Limited
Input Voltage	
LM120/LM320-N	−40V
LM320T	−35V
Input-Output Voltage Differential	30V
Junction Temperatures	(3)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

(1) Refer to RETS120-15H drawing for LM120H-15 or RETS120-15K drawing for LM120K-15 military specifications.
(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
(3) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS

Order Numbers		Metal Can Package						Units
		LM120K-15 (TO-3)			LM320K-15 (TO-3)			
Design Output Current (I_D)		1A						
Device Dissipation (P_D)		20W						
Parameter	Conditions (1)	Min	Typ	Max	Min	Typ	Max	
Output Voltage	T_J = 25°C, V_{IN} = 20V, I_{LOAD} = 5 mA	−15.3	−15	−14.7	−15.4	−15	−14.6	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} ≤ V_{IN} ≤ V_{MAX}$		5	10		5	20	mV
Input Voltage		−35		−17	−35		−17	V
Ripple Rejection	f = 120 Hz	56	80		56	80		dB
Load Regulation, (2)	T_J = 25°C, V_{IN} = 20V, 5 mA ≤ I_{LOAD} ≤ I_D		30	80		30	80	mV
Output Voltage, (1)	17.5V ≤ V_{IN} ≤ V_{MAX}, 5 mA ≤ I_{LOAD} ≤ I_D, P ≤ P_D	−15.5		−14.5	−15.6		−14.4	V
Quiescent Current	$V_{MIN} ≤ V_{IN} ≤ V_{MAX}$		2	4		2	4	mA

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS (continued)

Order Numbers		Metal Can Package						Units
		LM120K-15 (TO-3)			LM320K-15 (TO-3)			
Design Output Current (I_D)		1A						
Device Dissipation (P_D)		20W						
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	**Min**	**Typ**	**Max**	
Quiescent Current	T_J = 25°C							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	5 mA ≤ I_{LOAD} ≤ I_D		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 20V, 10 Hz ≤ f ≤ 100 kHz		400			400		µV
Long Term Stability			15	150		15	150	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

LM120H-15 ELECTRICAL CHARACTERISTICS

Order Numbers		Metal Can Package			Units
		LM120H-15 (TO)			
Design Output Current (I_D)		0.2A			
Device Dissipation (P_D)		2W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
Output Voltage	T_J = 25°C, V_{IN} = 20V, I_{LOAD} = 5 mA	−15.3	−15	−14.7	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		5	10	mV
Input Voltage		−35		−17	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation, (2)	T_J = 25°C, V_{IN} = 20V, 5 mA ≤ I_{LOAD} ≤ I_D		10	25	mV
Output Voltage, (1)	17.5V ≤ $V_{IN} \le V_{MAX}$, 5 mA ≤ I_{LOAD} ≤ I_D, P ≤ P_D	−15.5		−14.5	V
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA
Quiescent Current	T_J = 25°C				
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA
	5 mA ≤ I_{LOAD} ≤ I_D		0.03	0.4	mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 20V, 10 Hz ≤ f ≤ 100 kHz		400		µV
Long Term Stability			15	150	mV
Thermal Resistance					
Junction to Case				(3)	°C/W
Junction to Ambient				(3)	°C/W

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.
(3) Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

LM320T-15 ELECTRICAL CHARACTERISTICS

Order Numbers		Power Plastic Package			Units
		LM320T-15 (TO-220)			
Design Output Current (I_D)		1A			
Device Dissipation (P_D)		15W			
Parameter	**Conditions** (1)	**Min**	**Typ**	**Max**	
Output Voltage	T_J = 25°C, V_{IN} = 20V, I_{LOAD} = 5 mA	−15.5	−15	−14.5	V
Line Regulation	T_J = 25°C, I_{LOAD} = 5 mA, $V_{MIN} \le V_{IN} \le V_{MAX}$		5	20	mV
Input Voltage		−35		−17.5	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation, (2)	T_J = 25°C, V_{IN} = 20V, 5 mA ≤ $I_{LOAD} \le I_D$		30	80	mV
Output Voltage, (1)	17.5V ≤ $V_{IN} \le V_{MAX}$, 5 mA ≤ $I_{LOAD} \le I_D$, P ≤ P_D	−15.7		−14.3	V
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA
Quiescent Current Change	T_J = 25°C $V_{MIN} \le V_{IN} \le V_{MAX}$ 5 mA ≤ $I_{LOAD} \le I_D$		 0.1 0.1	 0.4 0.4	 mA mA
Output Noise Voltage	T_A = 25°C, C_L = 1 µF, I_L = 5 mA, V_{IN} = 20V, 10 Hz ≤ f ≤ 100 kHz		400		µV
Long Term Stability			30		mV
Thermal Resistance Junction to Case Junction to Ambient			 4 50		 °C/W °C/W

(1) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

TYPICAL PERFORMANCE CHARACTERISTICS

Output Voltage vs Temperature

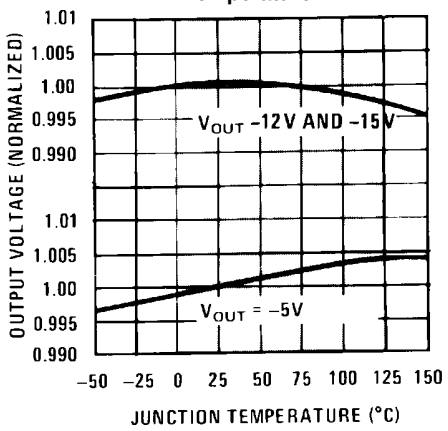


Figure 3.

Ripple Rejection (All Types)

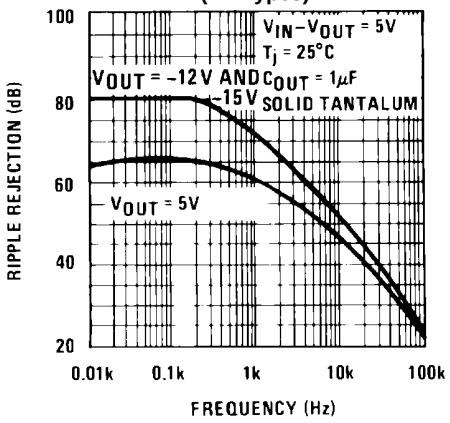


Figure 4.

Output Impedance TO-3 and TO-220 Packages

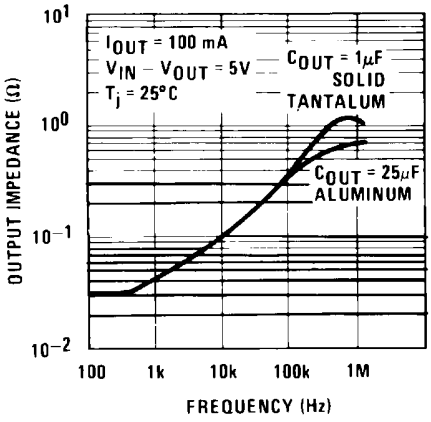


Figure 5.

Output Impedance TO-5 and TO-202 Packages

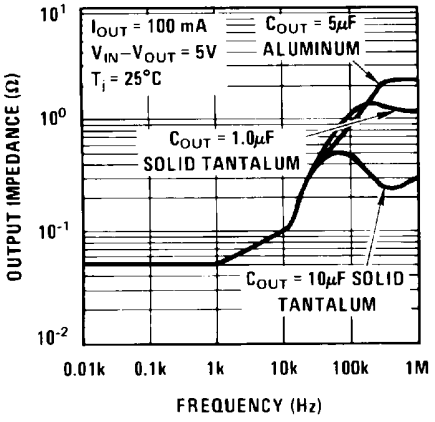


Figure 6.

Minimum Input-Output Differential TO-3 and TO-220 Packages

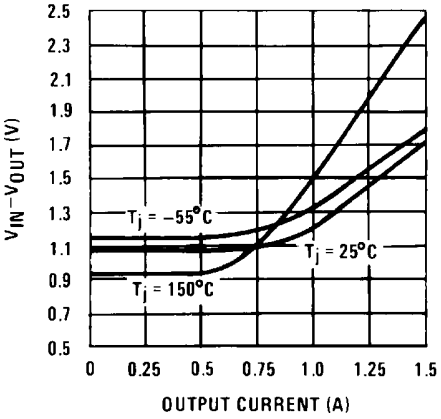


Figure 7.

Minimum Input-Output Differential TO-5 and TO-202 Packages

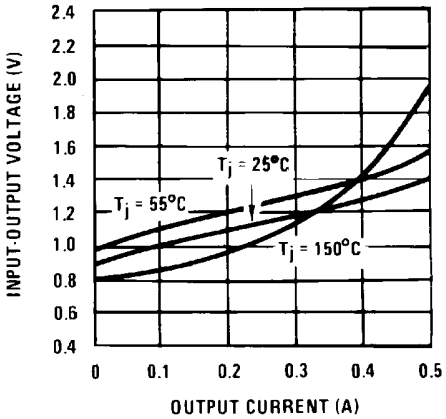


Figure 8.

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Quiescent Current vs Input Voltage

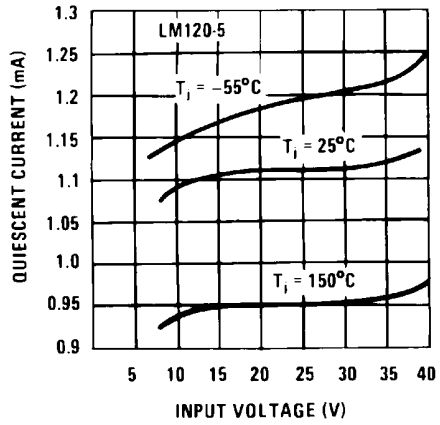


Figure 9.

Quiescent Current vs Load Current

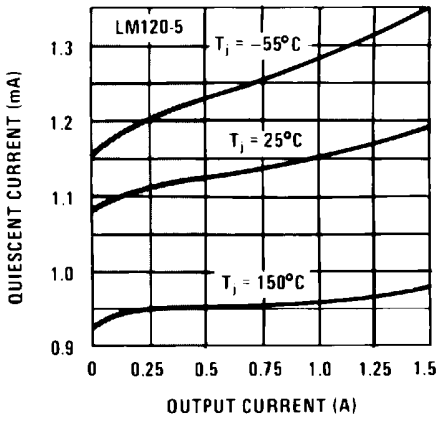
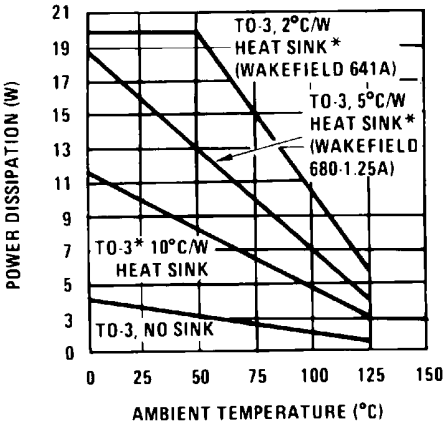


Figure 10.

Maximum Average Power Dissipation (TO-3)



*These curves for LM120. Derate 25°C further for LM320-N.

Figure 11.

Maximum Average Power Dissipation (TO-5)

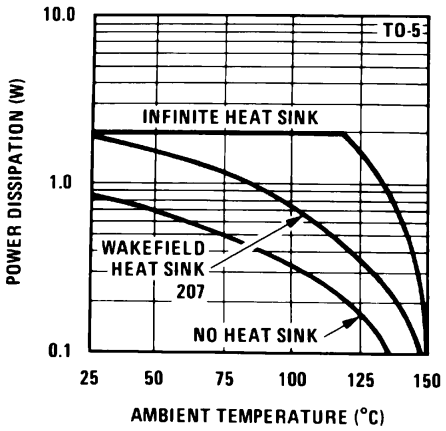


Figure 12.

Maximum Average Power Dissipation (TO-202)

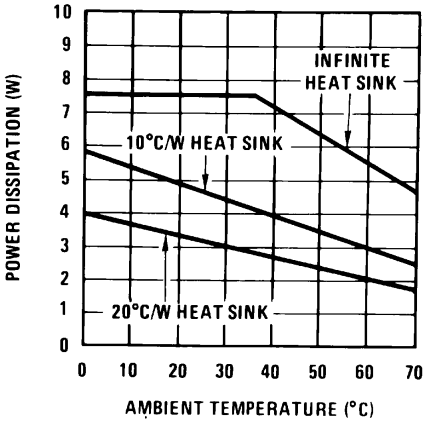


Figure 13.

Maximum Average Power Dissipation (TO-220)

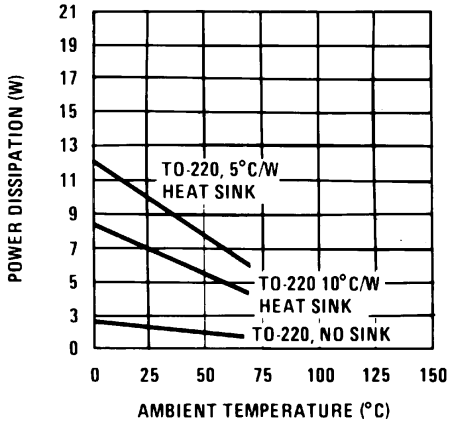


Figure 14.

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Short Circuit Current

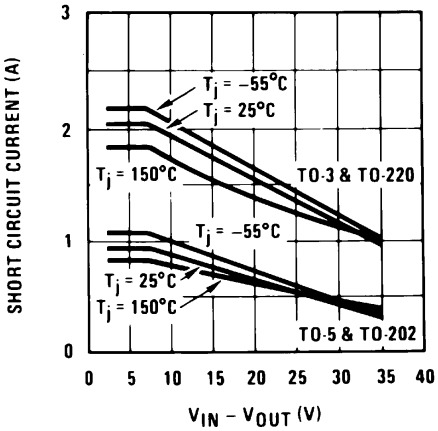
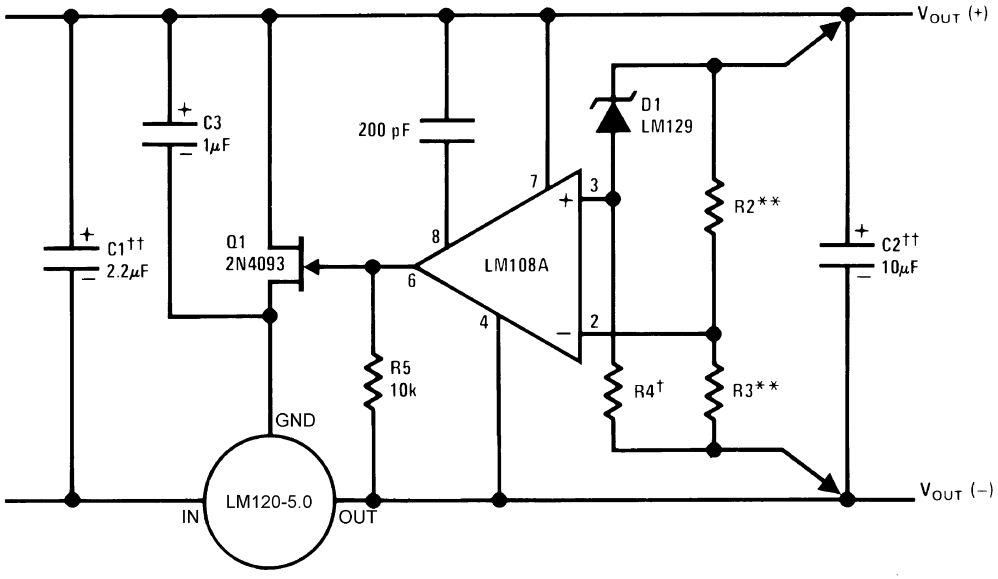


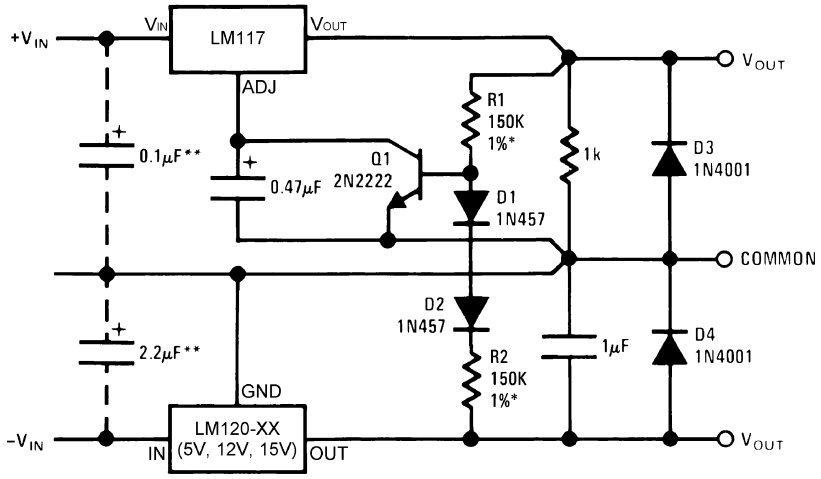
Figure 15.

TYPICAL APPLICATIONS



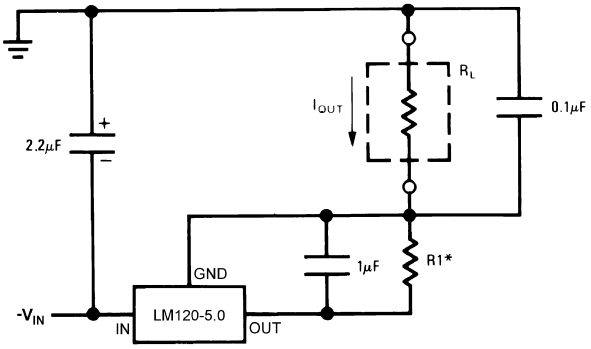
Lead and line regulation — 0.01% temperature stability — 0.2%
†Determines Zener current.
††Solid tantalum.
An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least −15V when using the LM120-12 and −18V for the LM120-15.
**Select resistors to set output voltage. 2 ppm/°C tracking suggested.

Figure 16. High Stability 1 Amp Regulator



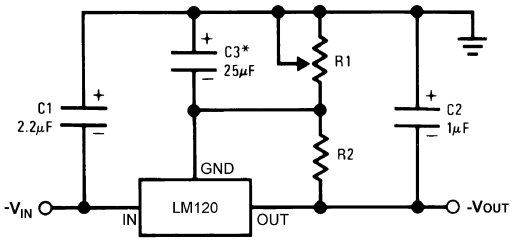
*Resistor tolerance of R1 and R2 determine matching of (+) and (−) inputs.
**Necessary only if raw supply capacitors are more than 3″ from regulators
An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.

Figure 17. Wide Range Tracking Regulator



$*I_{OUT} = 1\ mA + \frac{5.0V}{R1}$

Figure 18. Current Source



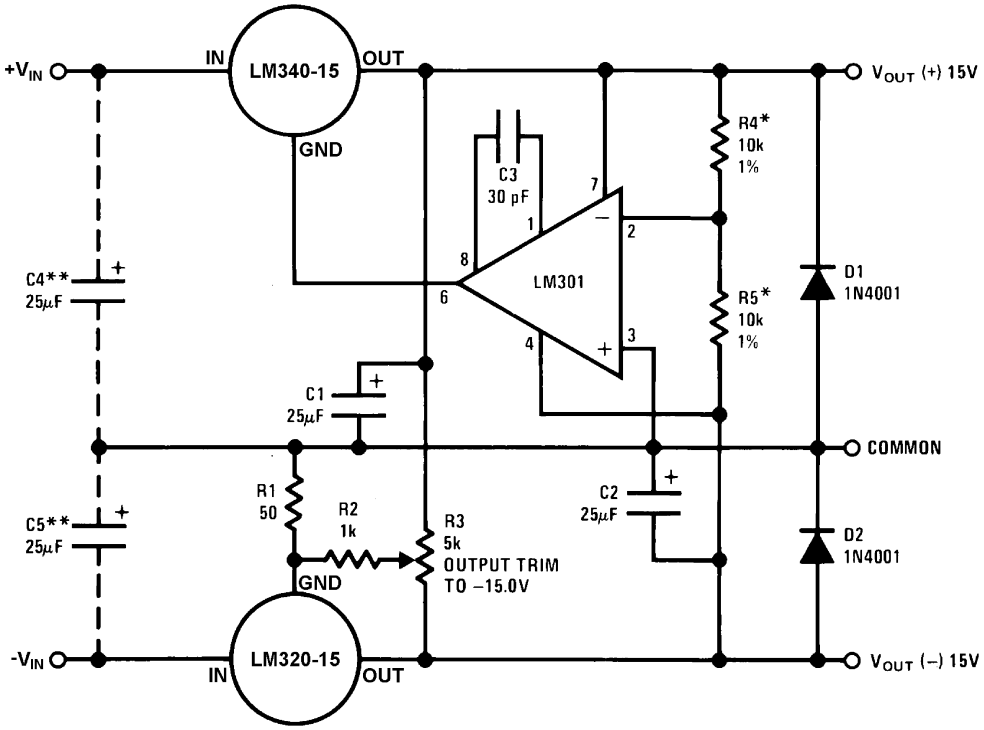
SELECT R2 AS FOLLOWS:

LM120-5	**300Ω**
LM120-12	**750Ω**
LM120-15	**1k**

$V_{OUT} = V_{SET}\frac{R1 + R2}{R2}$

*C3 optional. Improves transient response and ripple rejection.

Figure 19. Variable Output Current Source



See Performance (Typical)

*Resistor tolerance of R4 and R5 determine matching of (+) and (−) outputs.

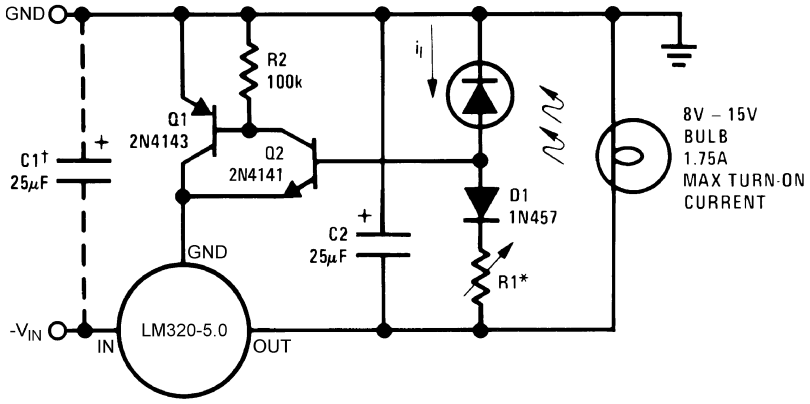
**Necessary only if raw supply filter capacitors are more than 2 inches from regulators.

Figure 20. ±15V, 1 Amp Tracking Regulators

Performance (Typical)

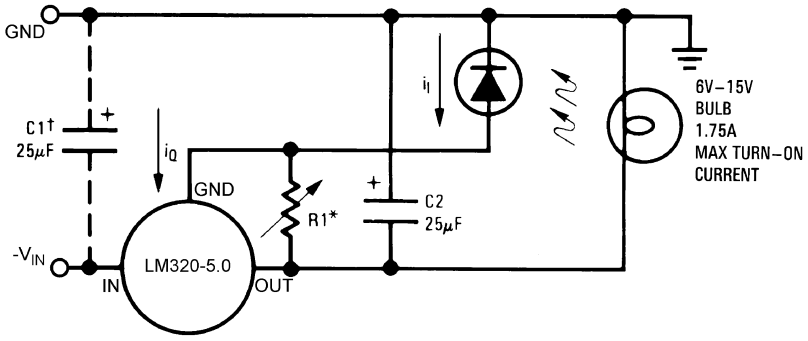
Load Regulation at ΔI_L = 1A	10 mV	1 mV
Output Ripple, C_{IN} = 3000 µF, I_L = 1A	100 µVrms	100 µVrms
Temperature Stability	+50 mV	+50 mV
Output Noise 10 Hz ≤ f ≤ 10 kHz	150 µVrms	150 µVrms

Light Controllers Using Silicon Photo Cells



*Lamp brightness increases until i_I = 5V/R1 (i_I can be set as low as 1 µA).

†Necessary only if raw supply filter capacitor is more than 2 inches from LM320MP.



*Lamp brightness increases until $i_I = i_Q$ (1 mA) + 5V/R1.
†Necessary only if raw supply filter capacitor is more than 2 inches from LM320-N.

Connection Diagram

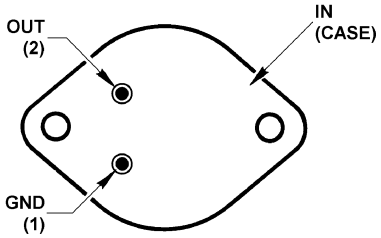


Figure 21. Steel Metal Can Package TO-3 (NDS)
(Bottom View)

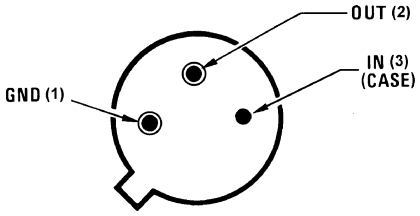


Figure 22. Metal Can Package TO (NDT)
(Bottom View)

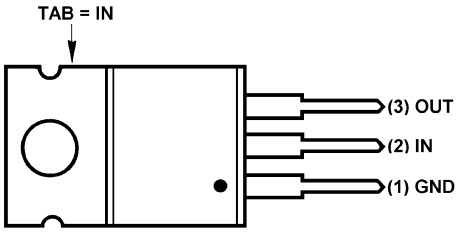


Figure 23. Power Package TO-220 (NDE)
(Front View)

Schematic Diagrams

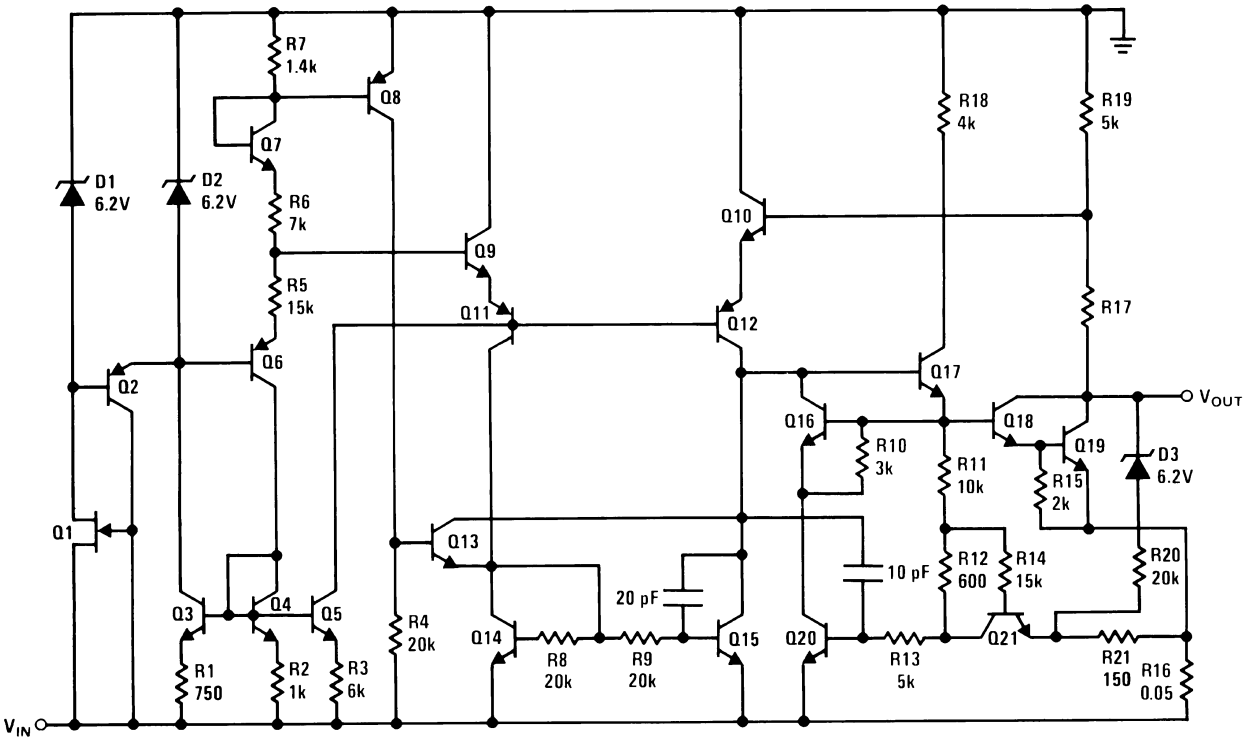


Figure 24. −5V

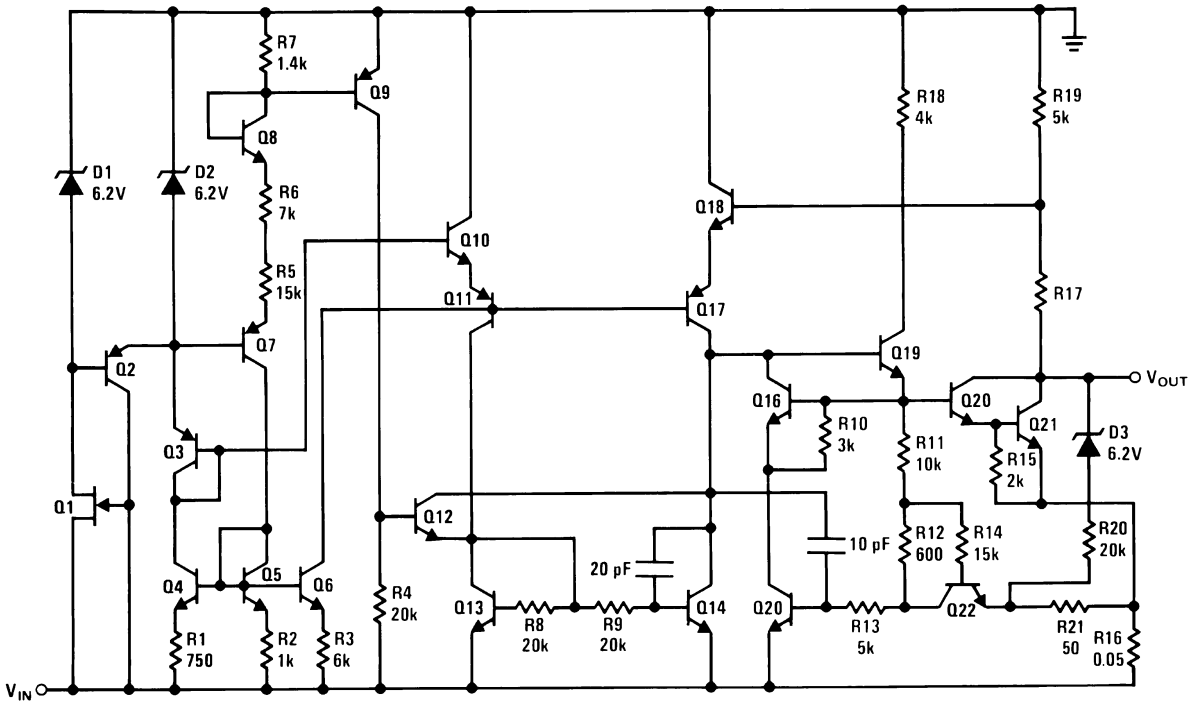


Figure 25. −12V and −15V

REVISION HISTORY

Changes from Revision B (April 2013) to Revision C	Page
• Changed layout of National Data Sheet to TI format	16

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM120H-12	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	AU | Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-12P+, LM120H-12P+)	Samples
LM120H-12/NOPB	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	AU | Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-12P+, LM120H-12P+)	Samples
LM120H-15	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-15P+, LM120H-15P+)	Samples
LM120H-15/NOPB	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	AU | Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-15P+, LM120H-15P+)	Samples
LM120H-5.0	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	AU | Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-5.0P+, LM120H-5.0P+)	Samples
LM120H-5.0/NOPB	ACTIVE	TO	NDT	3	500	Green (RoHS & no Sb/Br)	AU | Call TI	Level-1-NA-UNLIM	-55 to 150	(LM120H-5.0P+, LM120H-5.0P+)	Samples
LM320T-15	NRND	TO-220	NDE	3	45	TBD	Call TI	Call TI	0 to 125	LM320T -15 P+	
LM320T-15/NOPB	ACTIVE	TO-220	NDE	3	45	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 125	LM320T -15 P+	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

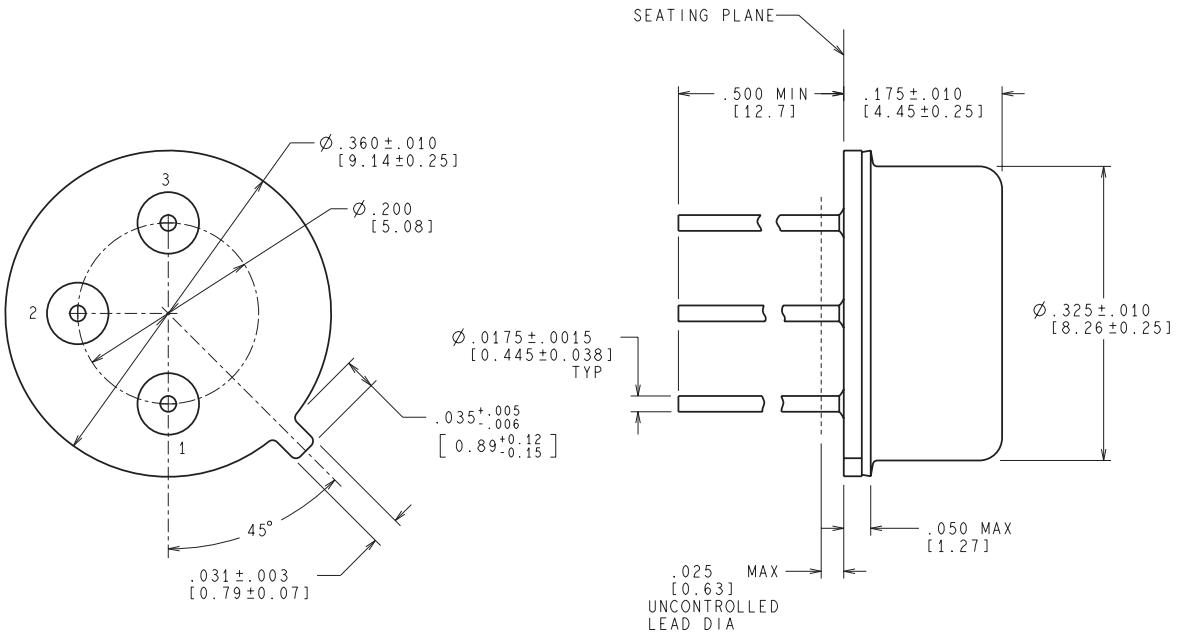
(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

NDT0003A

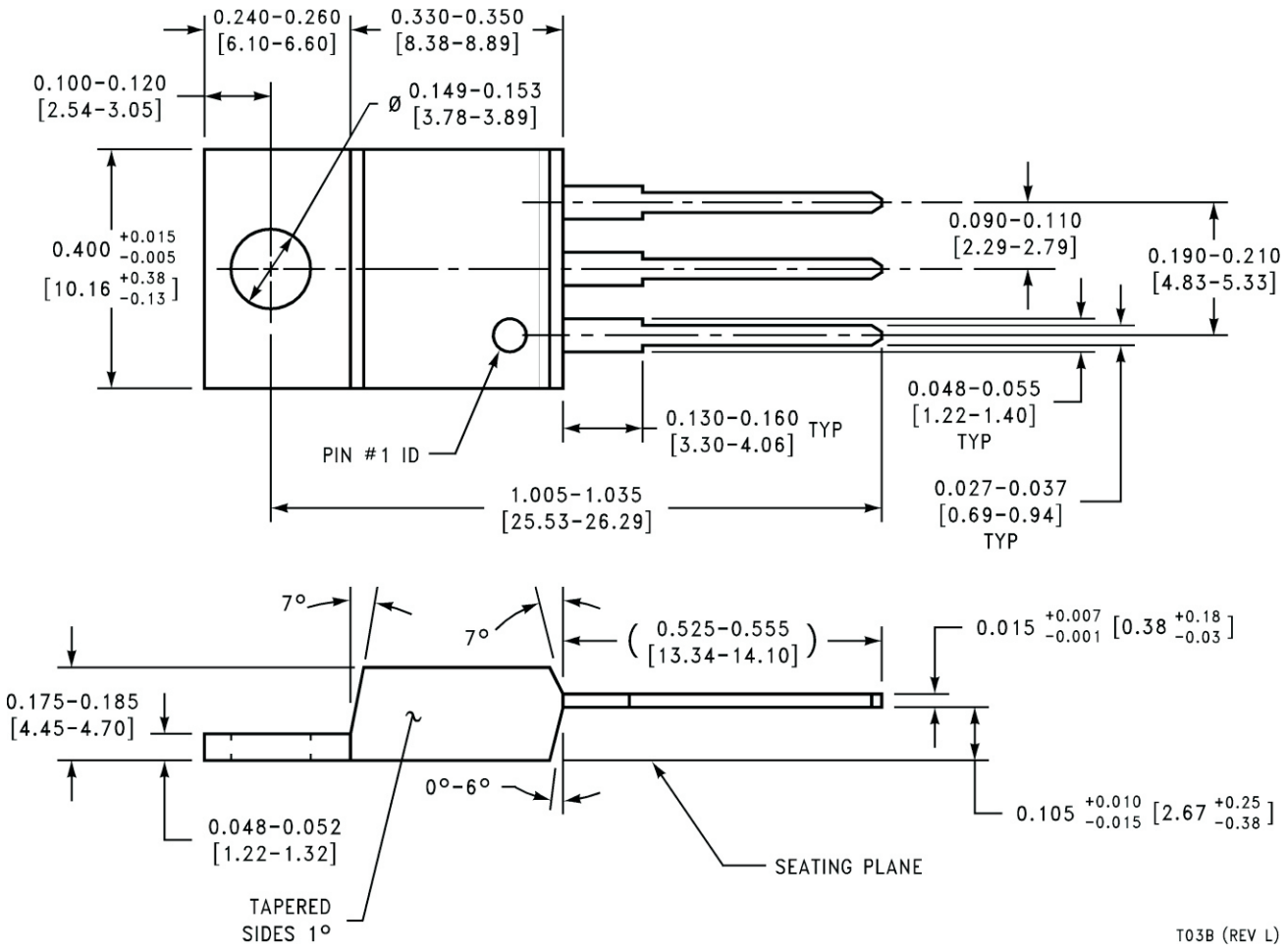


CONTROLLING DIMENSION IS INCH
VALUES IN [ ] ARE MILLIMETERS

MIL-PRF-38535
CONFIGURATION CONTROL

H03A (Rev D)

NDE0003B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[6.10-6.60]
0.330-0.350
[8.38-8.89]
0.100-0.120
[2.54-3.05]
Ø 0.149-0.153
[3.78-3.89]
0.400 +0.015 -0.005
[10.16 +0.38 -0.13]
0.090-0.110
[2.29-2.79]
0.190-0.210
[4.83-5.33]
PIN #1 ID
0.130-0.160 TYP
[3.30-4.06]
0.048-0.055
[1.22-1.40]
TYP
1.005-1.035
[25.53-26.29]
0.027-0.037
[0.69-0.94]
TYP
7°
7°
(0.525-0.555
[13.34-14.10])
0.015 +0.007 -0.001 [0.38 +0.18 -0.03]
0.175-0.185
[4.45-4.70]
0°-6°
0.105 +0.010 -0.015 [2.67 +0.25 -0.38]
0.048-0.052
[1.22-1.32]
SEATING PLANE
TAPERED
SIDES 1°
T03B (REV L)

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