Unit: mm

7.62 ± 0.25

0.25 +0.

.85~8.80

11-10C4

TOSHIBA Photocoupler GaAlAs Ired & Photo IC

6N137

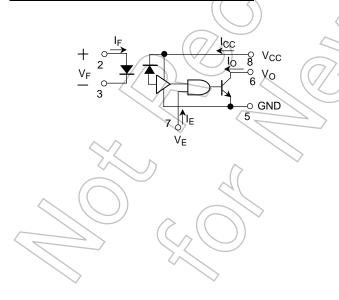
Digital Logic Isolation Tele-Communication Analog Data Equipment Control

The TOSHIBA 6N137 consist of a high emitting diode and a one chip photo IC. This unit is 8-lead DIP package.

- LSTTL / TTL compatible: 5V Supply •
- Ultra high speed: 10MBd •
- Guaranteed performance over temperature: 0°C to 70°C •
- High isolation voltage: 2500Vrms (min)
- UL recognized: UL1577, file no. E67349



Input	Enable	Output
Н	Н	L
L	Н	н
Н	L	н ((
L	L	H





긴 2 3

.65

9.66 ± 0.25

 2.54 ± 0.25

0.5 ±



- 1 : N.C.
- 2 : Anode 3 : Cathode
- 4 : N.C.
- 5 : GND
- 6 : Output(Open collector)
- 7 : Enable
- 8 : V_{CC}

Start of commercial production 1982/11

Absolute Maximum Ratings

	Characteristic	Symbol	Rating	Unit	
	Forward current	lF	20	mA	
	Pulse forward current (Note 1)	IFP	40	mA	
LED	Reverse voltage	VR	5	٧	
	Diode power dissipation	PD	40	mW	
	Input power dissipation derating (Ta \ge 70 °C)	∆PD/°C	-0.73	mW/°C	\sum
	Output current	lo	50	mA	
	Output voltage	Vo	7		
tor	Supply voltage (1 minute maximum)	Vcc	7	$\langle \rangle$	
Detector	Enable input voltage (not to exceed V _{CC} by more than 500mV)	VEH	5.5		
	Output collector power dissipation	Po	85	_ m₩	
	Output power dissipation derating $(T_a \ge 85 \text{ °C})$	∆Po/°C	-2.2	mW/°C	
Operating temperature range		Topr	0 to 70	°C	\bigcirc
Stora	ge temperature range	T _{stg}	-55 to 125	°Ç ⟨	(
Lead	solder temperature (10 s) (Note 2)	T _{sol}	260	ŝ	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

- Note 1: 50% duty cycle, 1ms pulse width.
- Note 2: Soldering portion of lead: Up to 2mm from the body of the device.

Recommended Operating Conditions

		/		
Characteristic	Symbol	Min	Max	Unit
Input current, low level each channel	IFL	0	250	μA
Input current, high level each channel	IFH	7	20	mA
High level enable voltage	VEH	2.0	Vcc	V
Low level enable voltage (output high)	VEL	0	0.8	V
Supply voltage, output*	Vcc	4.5	5.5	V
Fan out (TTL load)	Z	_	8	-
Operating temperature	Ta	0	70	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

*This item denotes operating ranges, not meaning of recommended operating conditions.

Precaution

Please be careful of the followings.

A ceramic capacitor $(0.1\mu F)$ should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching property. The total lead length between capacitor and coupler should not exceed 1cm.

Electrical Characteristics Over Recommended Temperature (Ta = 0 to 70°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min	(**)Typ.	Max	Unit
High level output current	ЮН	V _{CC} =5.5V, V _O =5.5V I _F =250μA, V _E = 2.0V	- <	1	250	μΑ
Low level output voltage	Vol	V _{CC} =5.5V, I _F =5mA V _{EH} =2.0V I _{OL} (sinking)=13mA		0.4	0.6	V
High level enable current	IEH	V _{CC} =5.5V, V _E =2.0V		_1.0	_	mA
Low level enable current	IEL	V _{CC} =5.5V, V _E =0.5V	$(\mathcal{O} \neq \mathfrak{I})$	-1.6	-2.0	mA
High level supply current	ICCH	V _{CC} =5.5V, I _F =0mA, V _E =0.5V		7	15	mA
Low level supply current	ICCL	V _{CC} =5.5V, I _F =10mA, V _E =0.5V	\sum	12	18	mA
Resistance (input-output) (Note 3)	R _{I-O}	V _{I−O} =500V, Ta=25°C R.H. ≤ 60%	シ_	10 ¹²	1	Ω
Capacitance (input-output) (Note 3)	CI-O	f=1MHz, Ta=25°C	_	0.6		pF
Input forward voltage	VF	IF=10mA, Ta=25°C	$\langle -$	1.65	1.75	V
Input reverse breakdown voltage	BVR	I _R =10μA, Ta≠25°C	5	- H	77-	V
Input capacitance	CIN	V _F =0V, f=1MHz	P	45	_	pF
Current transfer ratio	CTR	IF=5.0mA, RL=100Ω	Y	1000	-	%

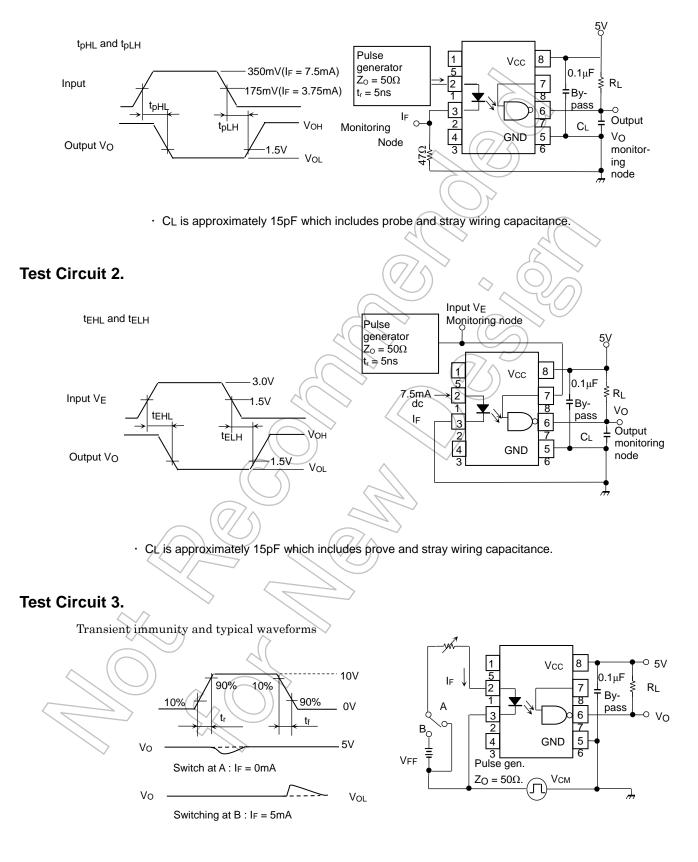
(**) All typical values are at VCC=5V, Ta=25°C

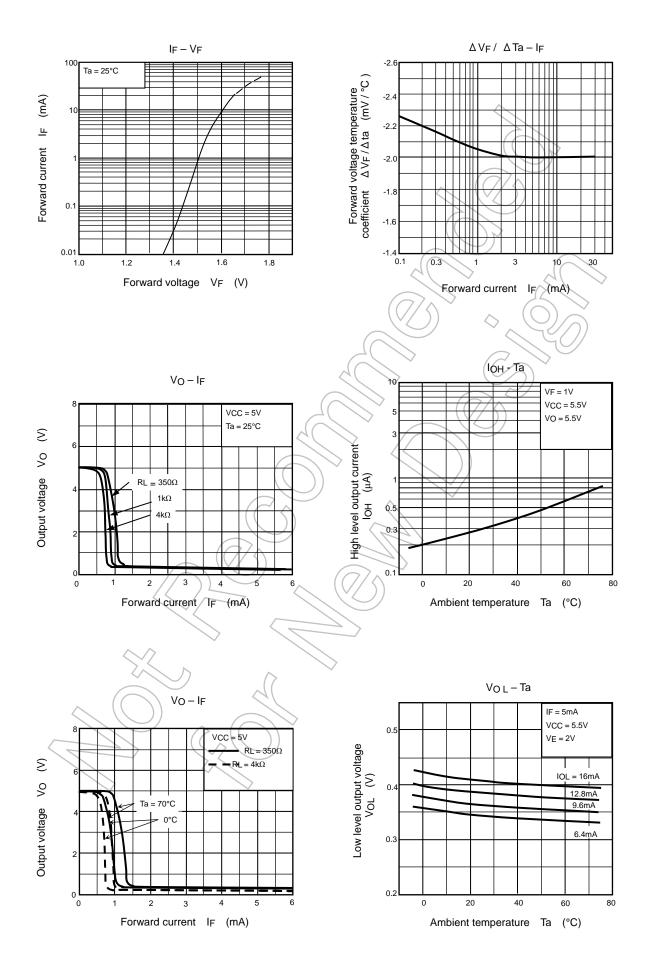
Note 3: Pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

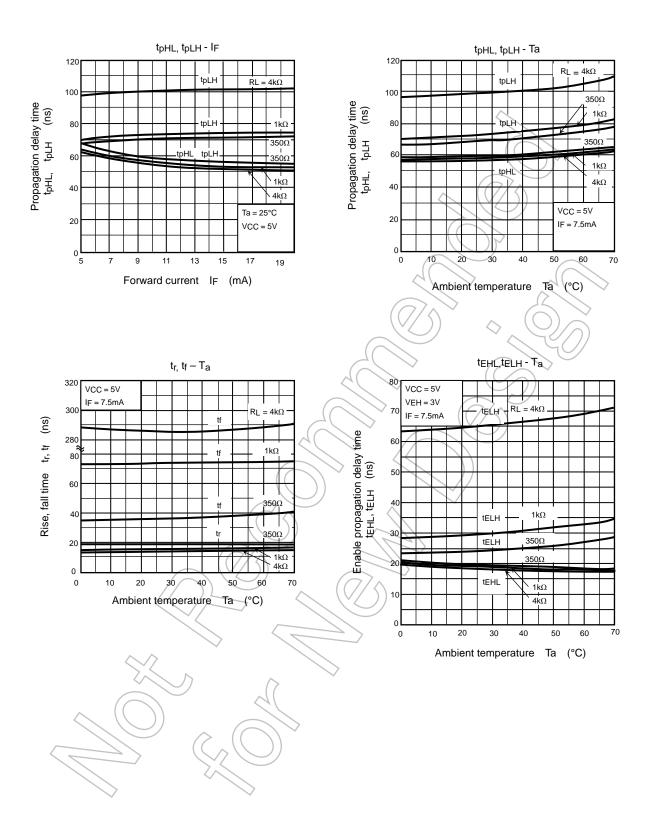
Switching Characteristics (Ta = 25°C, Vcc = 5V)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time to high output level	t _p LH	1	$R_L=350\Omega$, $C_L=15pF$ IF=7.5mA	_	60	75	ns
Propagation delay time to low output level	t _p HL	1	R _L =350Ω, C _L =15pF I _F =7.5mA	\searrow	60	75	ns
Output rise-fall time (10-90%)	t _r , t _f	_	R _L =350Ω, C _L =15pF I _F =7.5mA	-2	30	_	ns
Propagation delay time of enable from V _{EH} to V _{EL}	^t ELH	2	R _L =350Ω, C _L =15pF I _F =7.5mA V _{EH} =3.0V V _{EL} =0.5V	(75)	25	_	ns
Propagation delay time of enable from V _{EL} to V _{EH}	tEHL	2	RL=350Ω, CL=15pF IF=7.5mA VEH=3.0V VEL=0.5V		25	-	ns
Common mode transient immunity at logic high output level	CMH	3	V _{CM} =10V R _L =350Ω V _O (min)=2V I _F =0mA	_	200		V / μs
Common mode transient Immunity at logic low output level	CML	3	V _{CM} =10V R _L =350Ω V _{O(max)} =0.8V I _F =5mA		-500)	V / μs

Test Circuit 1.







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