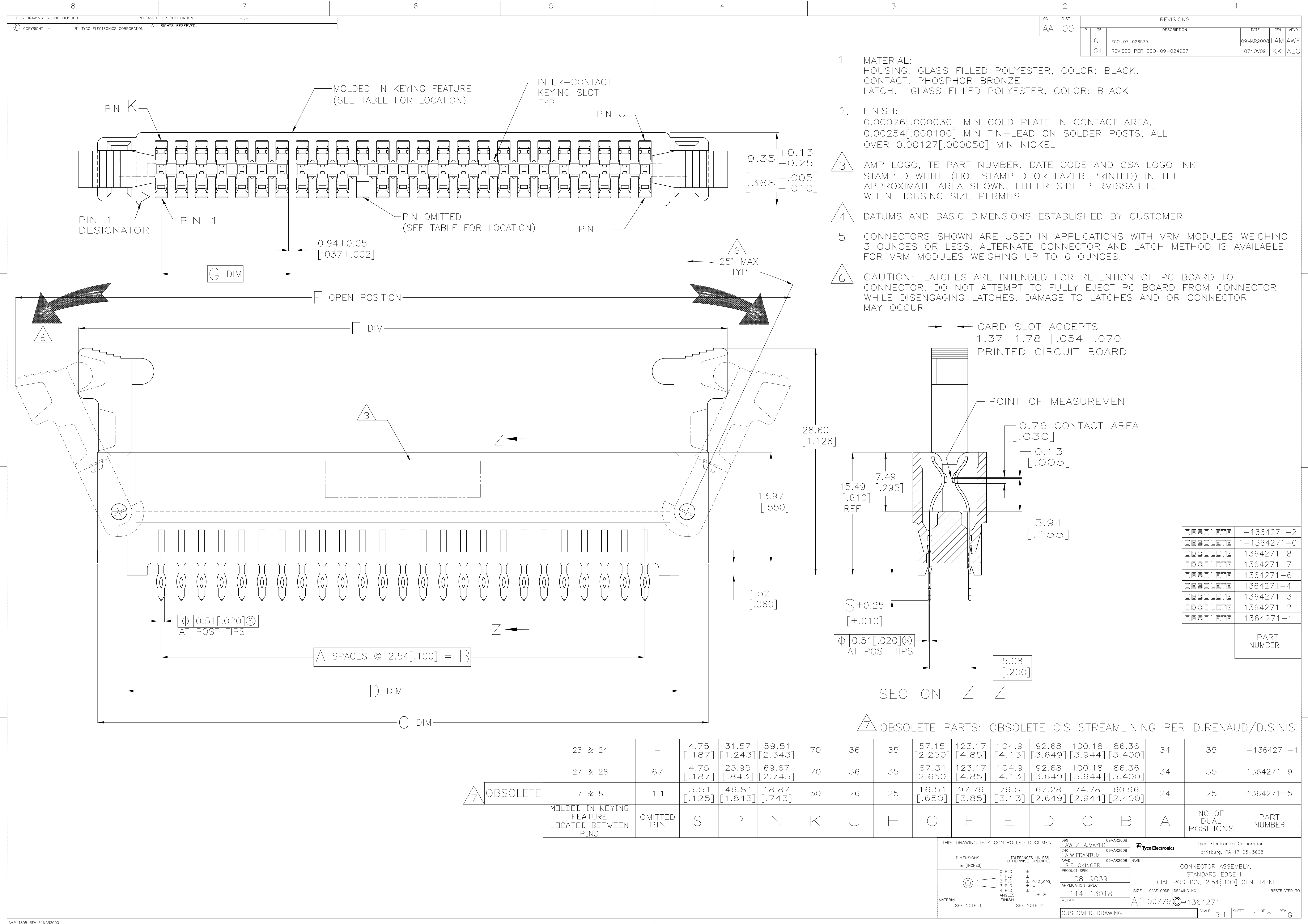




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LOC	DIST	REVISONS			
F	LTR	DESCRIPTION		DATE	DWN
G		ECO-07-026535		09MAR2008	AWF
G1		REVISED PER ECO-09-024927		07NOV09	KK

1. MATERIAL:
HOUSING: GLASS FILLED POLYESTER, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE
LATCH: GLASS FILLED POLYESTER, COLOR: BLACK

2. FINISH:
0.00076[.000030] MIN GOLD PLATE IN CONTACT AREA,
0.00254[.000100] MIN TIN-LEAD ON SOLDER POSTS, ALL
OVER 0.00127[.000050] MIN NICKEL

3. AMP LOGO, TE PART NUMBER, DATE CODE AND CSA LOGO INK STAMPED WHITE (HOT STAMPED OR LAZER PRINTED) IN THE APPROXIMATE AREA SHOWN, EITHER SIDE PERMISSABLE, WHEN HOUSING SIZE PERMITS

4. DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER

5. CONNECTORS SHOWN ARE USED IN APPLICATIONS WITH VRM MODULES WEIGHING 3 OUNCES OR LESS. ALTERNATE CONNECTOR AND LATCH METHOD IS AVAILABLE FOR VRM MODULES WEIGHING UP TO 6 OUNCES.

6. CAUTION: LATCHES ARE INTENDED FOR RETENTION OF PC BOARD TO CONNECTOR. DO NOT ATTEMPT TO FULLY EJECT PC BOARD FROM CONNECTOR WHILE DISENGAGING LATCHES. DAMAGE TO LATCHES AND OR CONNECTOR MAY OCCUR

OBSELETE	1-1364271-2
OBSELETE	1-1364271-0
OBSELETE	1364271-8
OBSELETE	1364271-7
OBSELETE	1364271-6
OBSELETE	1364271-4
OBSELETE	1364271-3
OBSELETE	1364271-2
OBSELETE	1364271-1

PART NUMBER	
-------------	--

7. OBSELETE PARTS: OBSELETE CIS STREAMLINING PER D.RENAUD/D.SINISI

23 & 24	-	4.75 [.187]	31.57 [1.243]	59.51 [2.343]	70	36	35	57.15 [2.250]	123.17 [4.85]	104.9 [4.13]	92.68 [3.649]	100.18 [3.944]	86.36 [3.400]	34	35	1-1364271-1
27 & 28	67	4.75 [.187]	23.95 [.843]	69.67 [2.743]	70	36	35	67.31 [2.650]	123.17 [4.85]	104.9 [4.13]	92.68 [3.649]	100.18 [3.944]	86.36 [3.400]	34	35	1364271-9
7 & 8	11	3.51 [.125]	46.81 [1.843]	18.87 [.743]	50	26	25	16.51 [.650]	97.79 [3.85]	79.5 [3.13]	67.28 [2.649]	74.78 [2.944]	60.96 [2.400]	24	25	1364271-5
MOLDED-IN KEYING FEATURE LOCATED BETWEEN PINS		OMITTED PIN	S	P	N	K	J	H	G	F	E	D	C	B	A	NO OF DUAL POSITIONS
																PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm [INCHES]

TOLERANCES UNLESS OTHERWISE SPECIFIED:

0 PLC ± -

1 PLC ± -

2 PLC ± 0.13[.005]

3 PLC ± -

4 PLC ± -

ANGLES ± 2°

MATERIAL SEE NOTE 1

FINISH SEE NOTE 2

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN AWF/L.A.MAYER

CHK A.W.FRANTUM

APVD S.FLICKINGER

PRODUCT SPEC 108-9039

APPLICATION SPEC 114-13018

WEIGHT -

CUSTOMER DRAWING

SCALE 5:1

SHEET 1 OF 2

REV G1

Tyco Electronics Corporation
Harrisburg, PA 17105-3608

CONNECTOR ASSEMBLY,
STANDARD EDGE II,
DUAL POSITION, 2.54[.100] CENTERLINE

SIZE CAGE CODE DRAWING NO RESTRICTED TO

A1 00779 1364271

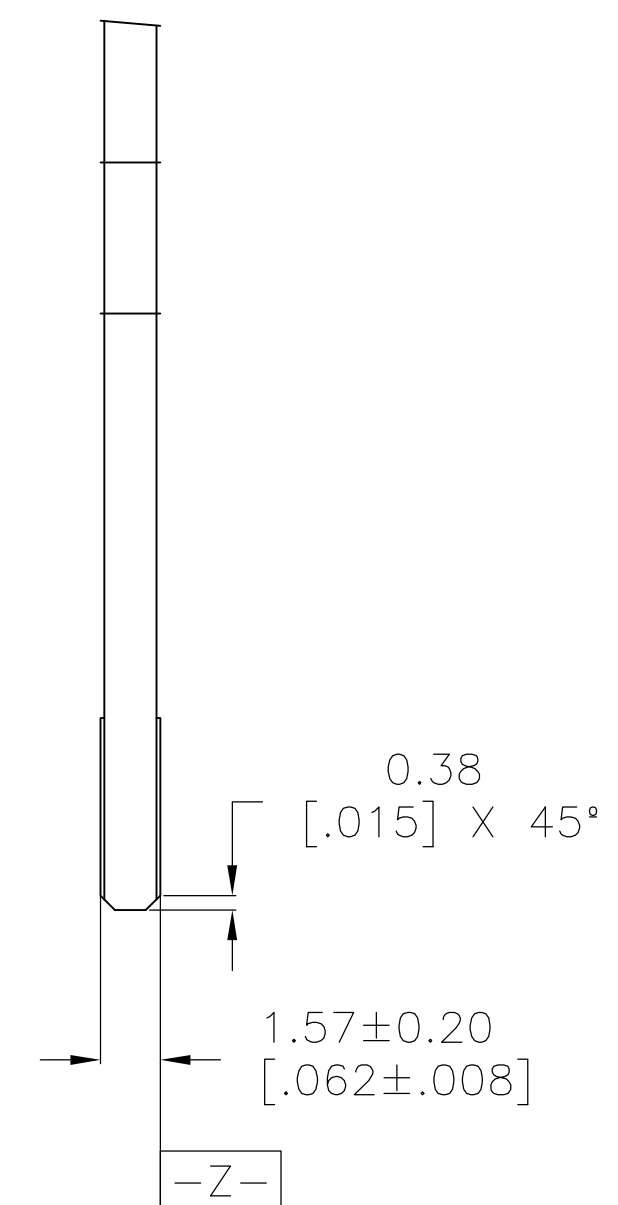


Diagram illustrating the recommended PC board hole layout for a 40-pin DIP package. The layout shows two rows of 20 holes each. Key dimensions and features include:

- Center-to-center pitch: 0.102 ± 0.003 TYP
- Hole diameter: 0.040 ± 0.003 TYP
- Maximum hole diameter: 0.020 ± 0.008 (M) X Y
- Maximum distance between mounting holes: 5.08 ± 0.200

The diagram also shows the recommended placement of the package pins, with Pin 1 and Pin H indicated.

Mouser Electronics

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[1-1364271-1](#)