# Emitter common (dual digital transistors)

### **EMG8 / UMG8N / FMG8A**

#### ● Features

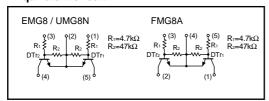
- 1) Two DTC143Z chips in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

#### ●Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both the DTr<sub>1</sub> and DTr<sub>2</sub>.

#### ●Equivalent circuit

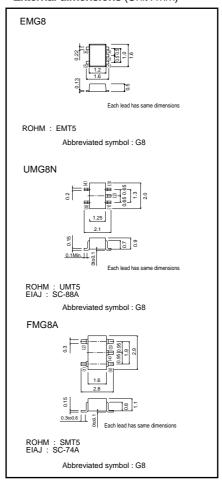


#### ● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	30	V	
		VIN	-5		
Output current		lo	100	mA	
		Ic (Max.)	100	mA	
Power dissipation	EMG8, UMG8N	Pd	150 (TOTAL)	*1 mW *2	
	FMG8A	Fu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

- \*1 120mW per element must not be exceeded.
- \*2 200mW per element must not be exceeded.

#### ●External dimensions (Unit:mm)



#### ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	-	-	0.5	V	Vcc=5V, Io=100μA	
input voltage	VI (on)	1.3	-	-	v	Vo=0.3V, Io=5mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo=5mA, l⊫0.25mA	
Input current	lı	-	-	1.8	mA	V <sub>I</sub> =5V	
Output current	lo (off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	80	-	-	-	Vo=5V, Io=10mA	
Transition frequency	f⊤	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz *	
Input resistance	R <sub>1</sub>	3.29	4.7	6.11	kΩ	=	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	8	10	12	-	=	

<sup>\*</sup> Transition frequency of the device

#### Packaging specifications

	Package	Taping				
	Code	T2R	TR	T148		
Туре	Basic ordering unit (pieces)	8000	3000	3000		
EMG8		0	_	_		
UMG8N		_	0	_		
FMG8A		_	_	0		

#### •Electrical characteristic curves

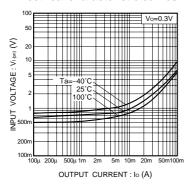


Fig.1 Input voltage vs. output current (ON characteristics)

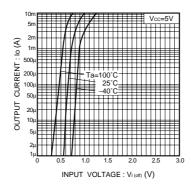


Fig.2 Output current vs. input voltage (OFF characteristics)

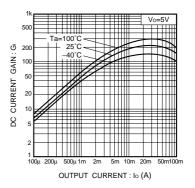


Fig.3 DC current gain vs. output current

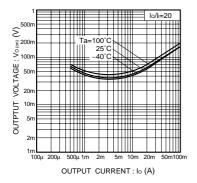


Fig.4 Output voltage vs. output current

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