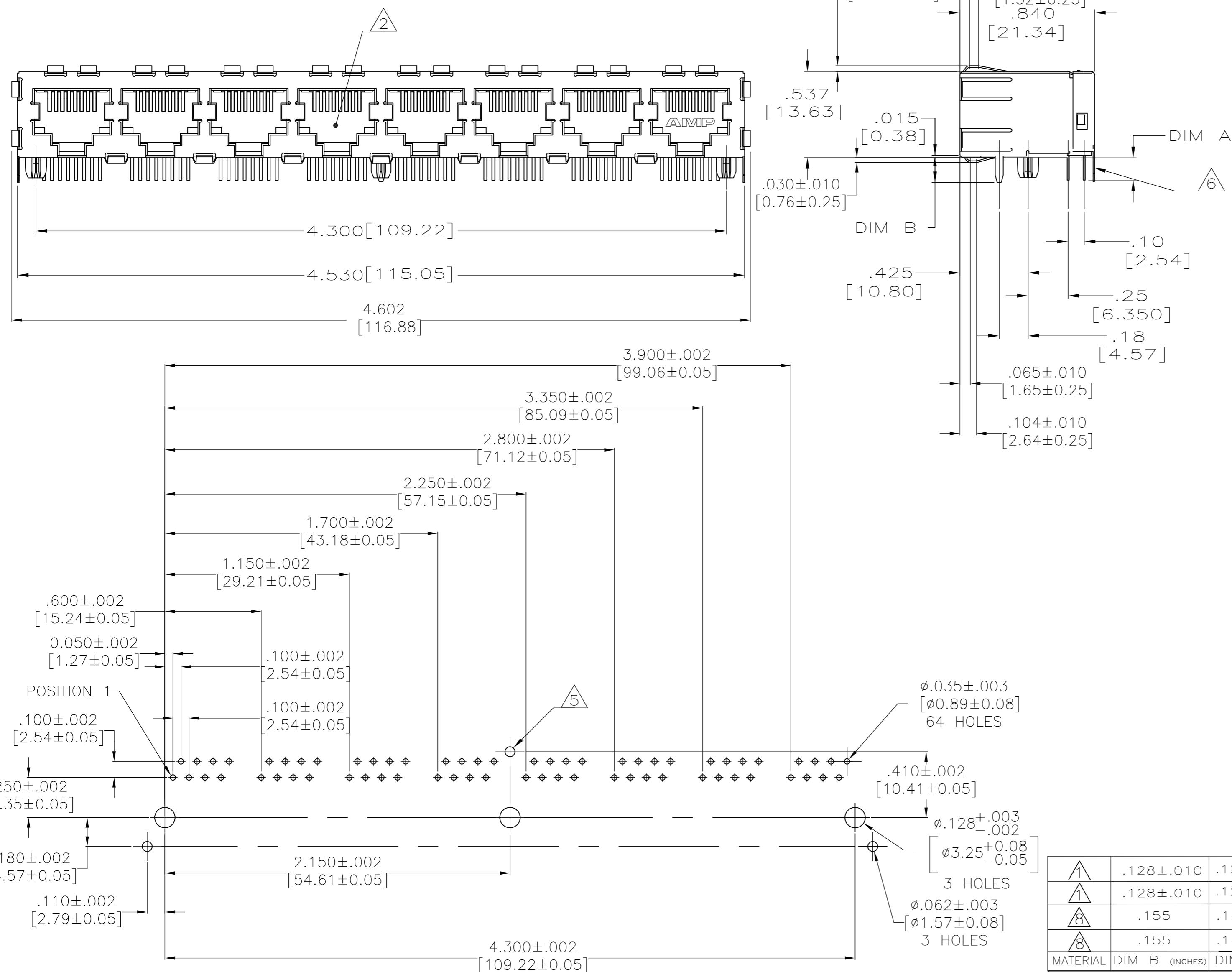


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LOC	DIST	REVISIONS			
		P	LTR	DESCRIPTION	DATE
AA	00	F6		ECR-10-016042	03AUG2010 KS M.W



FOR P/N: 5406491-1 & -2
SUGGESTED PC BOARD LAYOUT
COMPONENT SIDE
SEE APPLICATION SPECIFICATION

1. MATERIAL:
HOUSING - NYLON, COLOR: BLACK, UL94V-0.
OVERMOLDED TERMINAL ARRAY - HTN, BROWN.
TERMINALS - .013 [0.33] THICK PHOS BRONZE
PLATED WITH .000050 [1.27 μm] THICK HARD
GOLD IN LOCALIZED AREA AND .000150 [3.81 μm] MIN
THICK MATTE TIN IN SOLDER AREA OVER .000050
[1.27 μm] MIN THICK NICKEL UNDERPLATE
SHIELDS - .010 [0.254] THICK COPPER ALLOY
PLATED WITH 1.27 μm [0.00050] MINIMUM NICKEL
AND 2.03 μm [0.00080] MINIMUM HOT TIN DIP ON
PCB GROUND TABS.

2. JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS, PART 68 SUBPART F.

3. ALL DIMENSIONS SHOWN ARE NOMINAL UNLESS
OTHERWISE SPECIFIED.

4. ALL DIMENSIONS SHOWN IN BRACKETS [] ARE
IN MILLIMETERS.

5. THIS HOLE IS NEEDED FOR -1 AND -3 ONLY.

6. THIS TAB REMOVED ON -2.

7. THE PART IS PRELIMINARY

8. MATERIAL:
HOUSING - NYLON, COLOR: BLACK, UL94V-0.
OVERMOLDED TERMINAL ARRAY - PBT, BROWN.
TERMINALS - .013 [0.33] THICK PHOS BRONZE
PLATED WITH .000050 [1.27 μm] THICK HARD
GOLD IN LOCALIZED AREA AND .000150 [3.81 μm] MIN
THICK MATTE TIN IN SOLDER AREA OVER .000050
[1.27 μm] MIN THICK NICKEL UNDERPLATE
SHIELDS - .010 [0.254] THICK COPPER ALLOY
PLATED WITH 1.27 μm [0.00050] MINIMUM NICKEL
AND 2.03 μm [0.00080] MINIMUM HOT TIN DIP ON
PCB GROUND TABS.

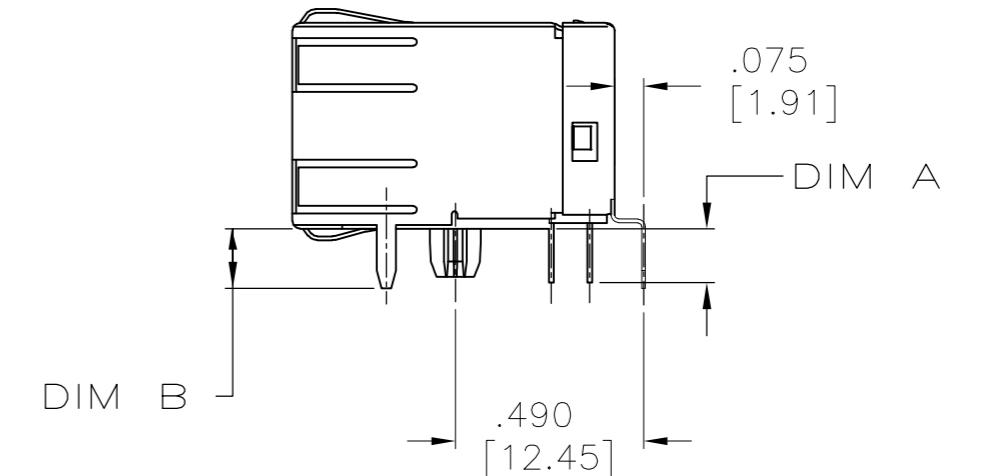
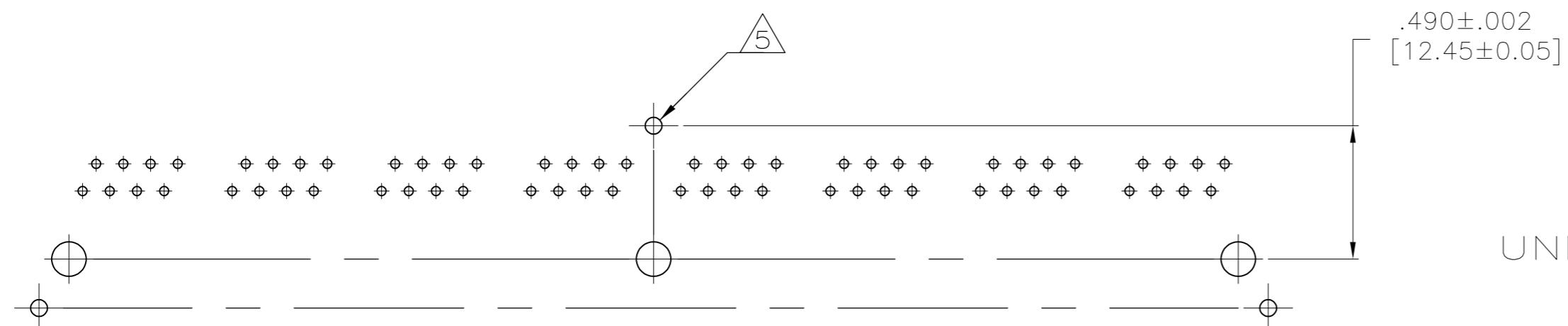
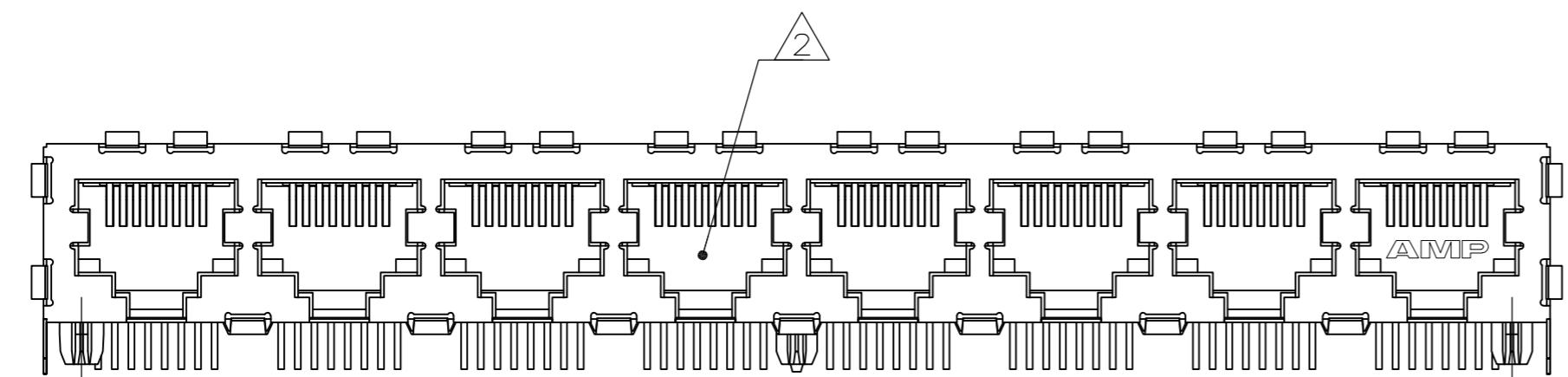
9. OBSOLETE PARTS: OBSOLETE CIS STREAMLINING
PER D.RENAUD/D.SINISI

MATERIAL	DIM B (INCHES)	DIM A (INCHES)	DESCRIPTION	PART NO.
▲	.128±.010	.128±.010	WITH REAR PC BOARD GROUND TAB	5406491-5
▲	.128±.010	.128±.010	WITH REAR PC BOARD GROUND TAB	5406491-4
▲	.155	.140±.015	WITHOUT REAR PC BOARD GROUND TAB	5406491-2
▲	.155	.140±.015	WITH REAR PC BOARD GROUND TAB	5406491-1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DINN Francis Lee CHK Denny Zhu APVD Steven Yao	14APRIL2009 14APRIL2009 14APRIL2009 14APRIL2009	Tyco Electronics Corporation Harrisburg, Pa 17105-3608
DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:			
INCHES				
0 PLC	± -			
1 PLC	± -			
2 PLC	± .01[0.254]			
3 PLC	± .005[0.127]			
4 PLC	± -			
ANGLES				
108-1163-2				
APPLICATION SPEC				
114-2048				
MATERIAL	FINISH			
SEE NOTE 1	SEE NOTE 1			
SEE NOTE 1	SEE NOTE 1			
CUSTOMER DRAWING	SCALE	2:1	1 OF	2 REV F6

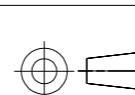
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LOC AA DIST 00
 P LTR
 SEE SHEET 1
 REVISIONS
 DESCRIPTION DATE DWN APVD



UNMARKED DIMENSIONS SAME AS P/N:5406491-1

FOR P/N:5406491-4;-5
 SUGGESTED PC BOARD LAYOUT
 COMPONENT SIDE
 SEE APPLICATION SPECIFICATION

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN 14APRIL2009 Francis Lee		Tyco Electronics Corporation	
DIMENSIONS: INCHES		TOLERANCES UNLESS OTHERWISE SPECIFIED:		CHK 14APRIL2009 Denny Zhu	
 0 PLC ± - 1 PLC ± - 2 PLC ± .01[.0254] 3 PLC ± .005[.0127] 4 PLC ± - ANGLES ± -		APVD 14APRIL2009 Steven Yao		NAME MODULAR JACK ASSEMBLY, PANEL GROUND, 8 PORT, 8 POSITION, RIGHT ANGLE, SHIELDED, CATEGORY 5	
PRODUCT SPEC 108-1163-2		APPLICATION SPEC 114-2048		SIZE CAGE CODE DRAWING NO A2 00779 C-5406491	
MATERIAL SEE NOTE 1		FINISH SEE NOTE 1		RESTRICTED TO —	
WEIGHT —		CUSTOMER DRAWING		SCALE 2:1 SHEET 2 OF 2 REV F6	

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[5406491-5](#)