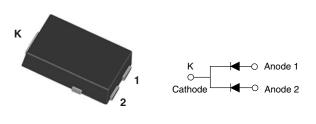
Hyper Fast Rectifier, 2 x 4 A FRED Pt[®]



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SMPC (TO-277A)

PRODUCT SUMMARY					
Package	SMPC (TO-277A)				
I _{F(AV)}	2 x 4 A				
V _R	200 V				
V _F at I _F	0.72 V				
t _{rr (typ.)}	25 ns				
T _J max.	175 °C				
Diode variation	Dual die				

FEATURES

- Hyper fast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyper fast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyper fast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage		V _{RRM}		200	V		
Average rectified forward current	per device		T _{Sp} = 160 °C	8			
Average rectilied forward current	per diode	IF(AV)		4	A		
Non repetitive peak aurge ourrept	per device	I	T _ 25 °C	130			
Non-repetitive peak surge current	per diode	IFSM	T _J = 25 °C	70			
Operating junction and storage temperatures		T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Forward valtage, par diada	V	$I_F = 4 A$	-	0.89	0.95	V	
Forward voltage, per diode	V _F	I _F = 4 A, T _J = 150 °C	-	0.72	0.78		
Povoroo lookago ourrent, por diado		$V_{R} = V_{R}$ rated	-	-	2		
Reverse leakage current, per diode	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	80	μΑ	
Junction capacitance	CT	V _R = 200 V	-	17	-	pF	

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RoHS

COMPLIANT

HALOGEN

FREE



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
			$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		25	-		
Reverse recovery time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	25		
Reverse recovery lime	t _{rr}	T _J = 25 °C		-	18	-	ns	
		T _J = 125 °C		-	27	-		
Peak recovery current		T _J = 25 °C	$I_F = 4 A$	-	2	-	А	
Peak recovery current I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _R = 160 V	-	3.6	-	~		
Reverse recovery charge Q _{rr}	T _J = 25 °C		-	18	-	nC		
	T _J = 125 °C		-	50	-			

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to solder pad, per leg	R _{thJ-Sp}		-	2.5	3.5	°C/W
Thermal resistance, junction to ambient, per leg	R _{thJA}		-	80	-	°C/W
Approximate weight				0.1		g
				0.0035 0.		oz.
Marking device		Case style SMPC (TO-277A)		QC	CH2	

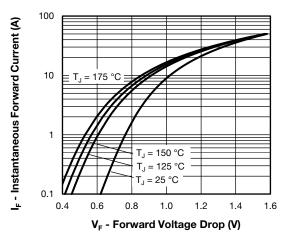


Fig. 1 - Typical Forward Voltage Drop Characteristics

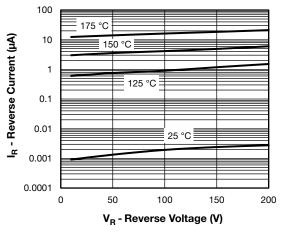
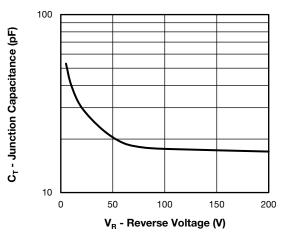


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage





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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

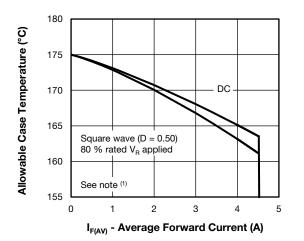
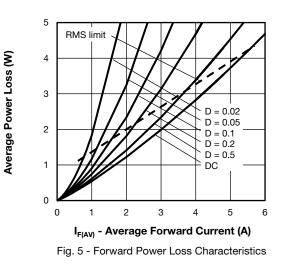


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



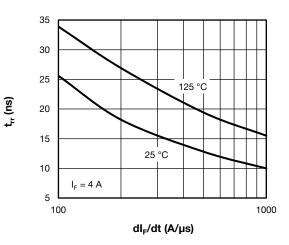


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

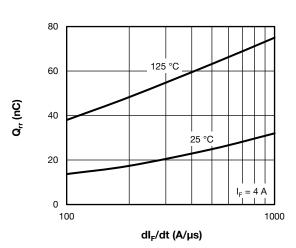


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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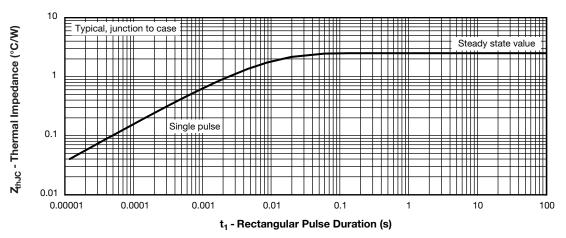
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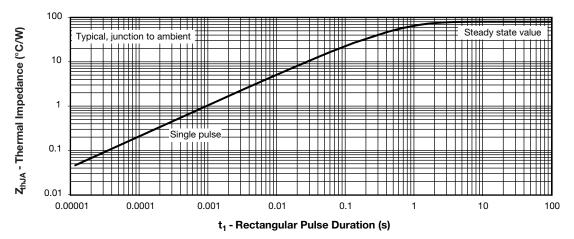
VS-8CSH02-M3

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Fig. 8 - Typical Transient Thermal Impedance, Junction to Case





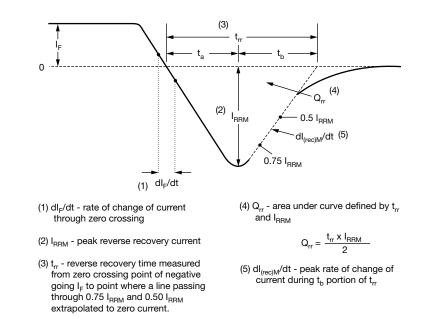


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	vs-	8	С	s	н	02	-M3
	1	2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng (8 = 8	8 A)		
	3	- Circ	uit conf	iguratior	า:		
		C =	commo	n catho	de		
	4	- S=	SMPC	package	9		
	5	- Pro	cess typ	be,			
		H =	hyper fa	ast reco	very		
	6	- Vol	age coo	de (02 =	200 V)		
	7	M3	= halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-8CSH02-M3/86A	1500	1500	7" diameter plastic tape and reel				
VS-8CSH02-M3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95570				
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96095				

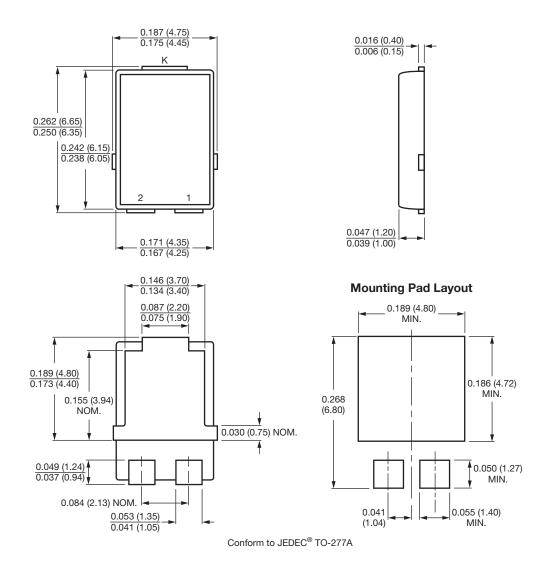
Outline Dimensions





TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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