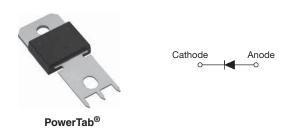
# **VS-80EBU04**

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**Vishay Semiconductors** 

# Ultrafast Soft Recovery Diode, 80 A FRED Pt®



PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	80 A			
V <sub>R</sub>	400 V			
V <sub>F</sub> at I <sub>F</sub>	0.92 V			
t <sub>rr</sub> (typ.)	See recovery table			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			

### FEATURES

- Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- $\bullet$  Designed and qualified according to JEDEC  $^{\textcircled{B}}\mbox{-}JESD$  47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

### **DESCRIPTION / APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL TEST CONDITIONS		MAX.	UNITS
Cathode to anode voltage	V <sub>R</sub>		400	V
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 101 °C	80	
Single pulse forward current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	800	А
Maximum repetitive forward current	I <sub>FRM</sub>	Square wave, 20 kHz	160	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>r</sub>	I <sub>R</sub> = 100 μA		-	-	
		I <sub>F</sub> = 80 A	-	1.1	1.3	v
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 80 A, T <sub>J</sub> = 175 °C	-	0.92	1.08	
		I <sub>F</sub> = 80 A, T <sub>J</sub> = 125 °C		0.98	1.15	
Deverse lectrose everent	I <sub>R</sub>	$V_{R} = V_{R}$ rated	-	-	50	μA
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	2	mA
Junction capacitance	CT	V <sub>R</sub> = 200 V		50	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 3.5 -		nH		

Revision: 09-Jun-15 1 Document Number: 93025 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



ROHS COMPLIANT

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CON	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/1000 \text{ A}/10000 \text{ A}/1000 \text{ A}/10000 \text{ A}/1000 \text{ A}/10000 \text{ A}/100000\text{ A}/100000\text{ A}/100000\text{ A}/100000\text{ A}/100000\text{ A}/1000000\text{ A}/1000000\text{ A}/1000000\text{ A}/1000000000000000000000000000000000000$	, dI <sub>F</sub> /dt = 200 A/µs, V <sub>R</sub> = 30 V		50	60	
Reverse recovery time t <sub>rr</sub>	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 80 A V <sub>R</sub> = 200 V dI <sub>F</sub> /dt = 200 A/μs	-	87	-	ns
		T <sub>J</sub> = 125 °C		-	151	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	9.3	-	A
		T <sub>J</sub> = 125 °C		-	17.2	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	405	-	nC
		T <sub>J</sub> = 125 °C		-	1300	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.70	°C/W
Thermal resistance, junction to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	0/11
Weight			-	-	5.02	g
weight			-	0.18	-	oz.
Mounting torque			1.2 (10)	-	2.4 (20)	N · m (lbf · in)
Marking device		Case style PowerTab <sup>®</sup>	80EBU04		•	

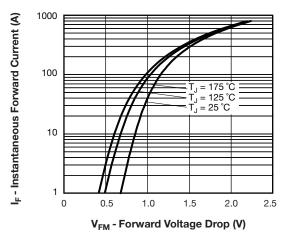
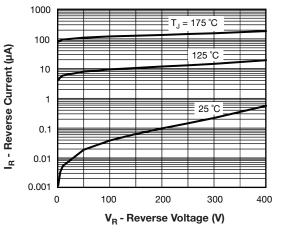
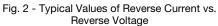


Fig. 1 - Maximum Forward Voltage Drop Characteristics





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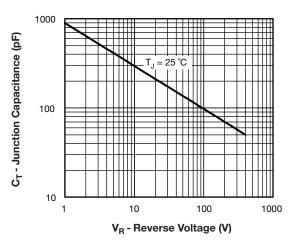


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

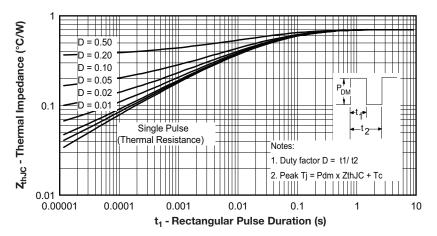
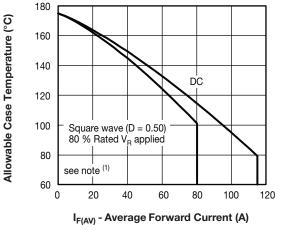
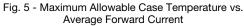


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics





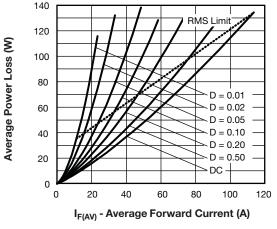


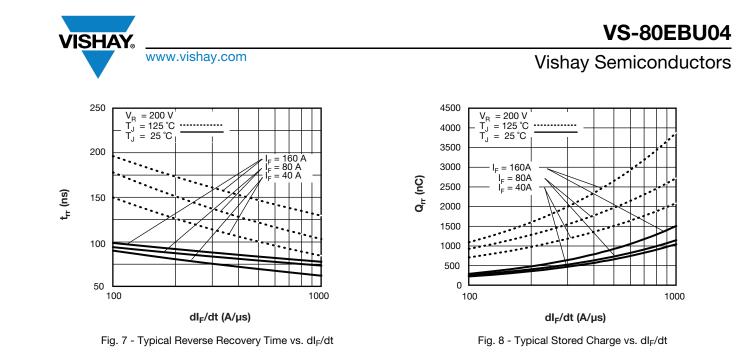
Fig. 6 - Forward Power Loss Characteristics

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<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$ 

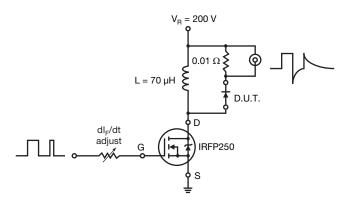


Fig. 9 - Reverse Recovery Parameter Test Circuit



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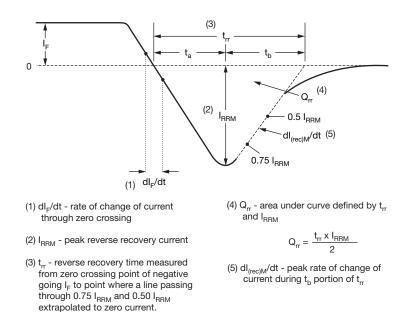
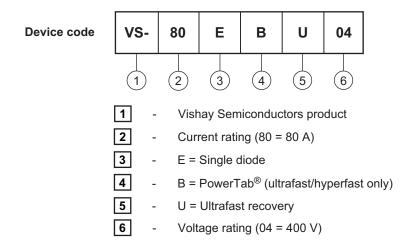


Fig. 10 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

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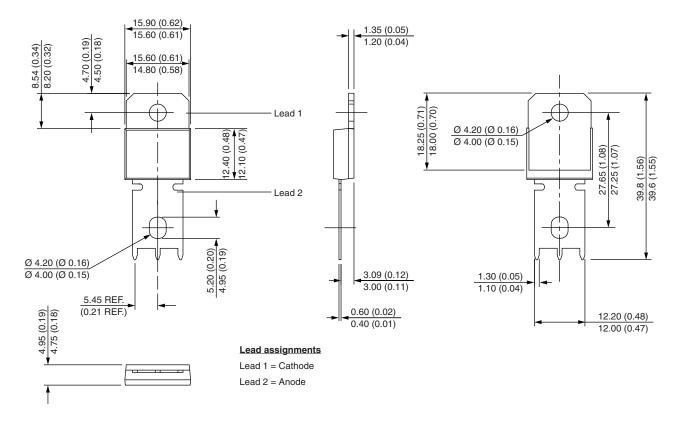
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95370			
Application note	www.vishay.com/doc?95179			



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**PowerTab**<sup>®</sup>

## **DIMENSIONS** in millimeters (inches)





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