# **Switching Transistor**

# **PNP Silicon**

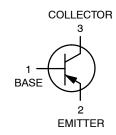
#### Features

- Moisture Sensitivity Level: 1
- ESD Rating: Human Body Model; 4 kV, Machine Model; 400 V
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



# **ON Semiconductor®**

http://onsemi.com



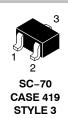
#### MAXIMUM RATINGS

| Rating                         | Symbol           | Value | Unit |
|--------------------------------|------------------|-------|------|
| Collector-Emitter Voltage      | V <sub>CEO</sub> | -40   | Vdc  |
| Collector-Base Voltage         | V <sub>CBO</sub> | -40   | Vdc  |
| Emitter-Base Voltage           | V <sub>EBO</sub> | -5.0  | Vdc  |
| Collector Current – Continuous | Ι <sub>C</sub>   | -600  | mAdc |

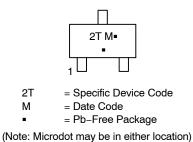
#### THERMAL CHARACTERISTICS

| Characteristic   | Symbol                            | Max         | Unit |
|--|-----------------------------------|-------------|------|
| Total Device Dissipation FR-5 Board<br>T <sub>A</sub> = 25°C | P <sub>D</sub>                    | 150         | mW   |
| Thermal Resistance,<br>Junction-to-Ambient                   | $R_{\theta JA}$                   | 833         | °C/W |
| Junction and Storage Temperature                             | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150 | °C   |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



#### MARKING DIAGRAM



\*Date Code orientation may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

| Device       | Package            | Shipping <sup>†</sup> |
|--------------|--------------------|-----------------------|
| MMBT4403WT1G | SC–70<br>(Pb–Free) | 3000 /<br>Tape & Reel |

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

| Characteristic   | Symbol               | Min  | Max  | Unit |  |  |
|--|----------------------|------|------|------|--|--|
| OFF CHARACTERISTICS  |                      |      |      |      |  |  |
| Collector-Emitter Breakdown Voltage (Note 1) ( $I_C = -1.0$ mAdc, $I_B = 0$ )        | V <sub>(BR)CEO</sub> | -40  | -    | Vdc  |  |  |
| Collector-Base Breakdown Voltage ( $I_C = -0.1 \text{ mAdc}, I_E = 0$ )              | V <sub>(BR)CBO</sub> | -40  | -    | Vdc  |  |  |
| Emitter-Base Breakdown Voltage ( $I_E = -0.1 \text{ mAdc}, I_C = 0$ )                | V <sub>(BR)EBO</sub> | -5.0 | -    | Vdc  |  |  |
| Base Cutoff Current ( $V_{CE} = -35$ Vdc, $V_{EB} = -0.4$ Vdc)                       | I <sub>BEV</sub>     | -    | -0.1 | μAdc |  |  |
| Collector Cutoff Current (V <sub>CE</sub> = $-35$ Vdc, V <sub>EB</sub> = $-0.4$ Vdc) | I <sub>CEX</sub>     | -    | -0.1 | μAdc |  |  |

#### **ON CHARACTERISTICS**

| $ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = -0.1 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}) \\ (I_{C} = -1.0 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}) \\ (I_{C} = -10 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}) \\ (I_{C} = -150 \text{ mAdc}, V_{CE} = -2.0 \text{ Vdc}) \text{ (Note 1)} \\ (I_{C} = -500 \text{ mAdc}, V_{CE} = -2.0 \text{ Vdc}) \text{ (Note 1)} \end{array} $ | h <sub>FE</sub>      | 30<br>60<br>100<br>100<br>20 | -<br>-<br>300<br>- | -   |
|--|----------------------|------------------------------|--------------------|-----|
| Collector-Emitter Saturation Voltage (Note 1)<br>( $I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}$ )<br>( $I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}$ )  | V <sub>CE(sat)</sub> |                              | -0.4<br>-0.75      | Vdc |
| Base – Emitter Saturation Voltage (Note 1)<br>( $I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}$ )<br>( $I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}$ )   | V <sub>BE(sat)</sub> | -0.75<br>-                   | -0.95<br>-1.3      | Vdc |

#### SMALL-SIGNAL CHARACTERISTICS

| Current-Gain – Bandwidth Product ( $I_C$ = –20 mAdc, $V_{CE}$ = –10 Vdc, f = 100 MHz)          | f <sub>T</sub>  | 200 | -   | MHz                |
|--|-----------------|-----|-----|--------------------|
| Collector-Base Capacitance ( $V_{CB}$ = -10 Vdc, $I_E$ = 0, f = 1.0 MHz)                       | C <sub>cb</sub> | -   | 8.5 | pF                 |
| Emitter–Base Capacitance ( $V_{BE}$ = -0.5 Vdc, $I_C$ = 0, f = 1.0 MHz)                        | C <sub>eb</sub> | -   | 30  | pF                 |
| Input Impedance (I <sub>C</sub> = -1.0 mAdc, V <sub>CE</sub> = -10 Vdc, f = 1.0 kHz)           | h <sub>ie</sub> | 1.5 | 15  | kΩ                 |
| Voltage Feedback Ratio ( $I_C = -1.0 \text{ mAdc}$ , $V_{CE} = -10 \text{ Vdc}$ , f = 1.0 kHz) | h <sub>re</sub> | 0.1 | 8.0 | X 10 <sup>-4</sup> |
| Small-Signal Current Gain (I <sub>C</sub> = -1.0 mAdc, V <sub>CE</sub> = -10 Vdc, f = 1.0 kHz) |                 | 60  | 500 | -                  |
| Output Admittance ( $I_C = -1.0 \text{ mAdc}$ , $V_{CE} = -10 \text{ Vdc}$ , f = 1.0 kHz)      | h <sub>oe</sub> | 1.0 | 100 | μmhos              |

#### SWITCHING CHARACTERISTICS

| Delay Time   | (V <sub>CC</sub> = −30 Vdc, V <sub>FB</sub> = −2.0 Vdc,          | t <sub>d</sub> | - | 15  | 20 |
|--------------|--|----------------|---|-----|----|
| Rise Time    | $I_{\rm C} = -150 \text{ mAdc}, I_{\rm B1} = -15 \text{ mAdc}$ ) | t <sub>r</sub> | - | 20  | ns |
| Storage Time | (V <sub>CC</sub> = −30 Vdc, I <sub>C</sub> = −150 mAdc,          | ts             | - | 225 | 20 |
| Fall Time    | $I_{B1} = I_{B2} = -15 \text{ mAdc}$                             | t <sub>f</sub> | - | 30  | ns |

1. Pulse Test: Pulse Width  $\leq$  300  $\mu s,$  Duty Cycle  $\leq$  2.0%.

#### SWITCHING TIME EQUIVALENT TEST CIRCUIT

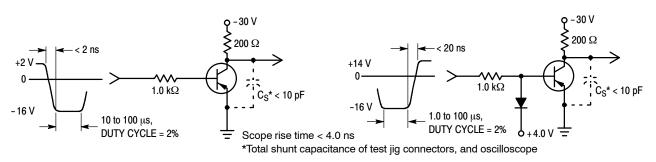
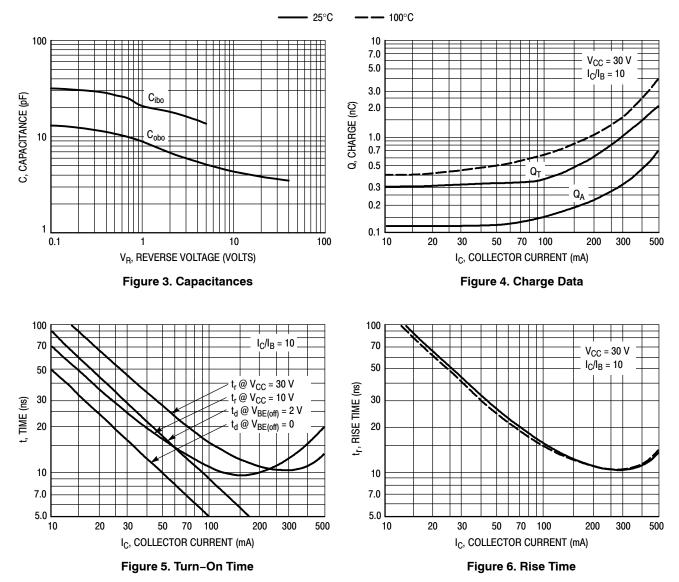


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

#### TRANSIENT CHARACTERISTICS



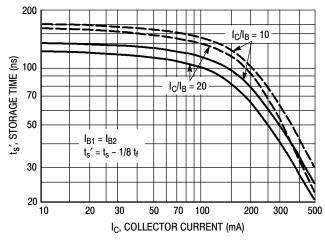
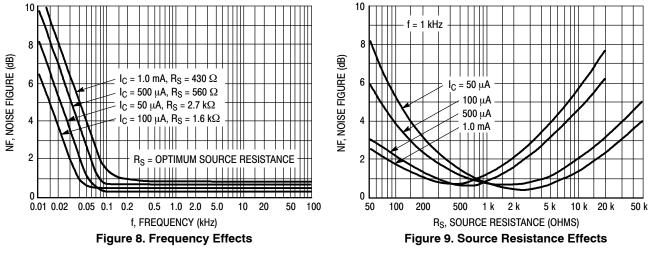


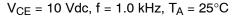
Figure 7. Storage Time

#### SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

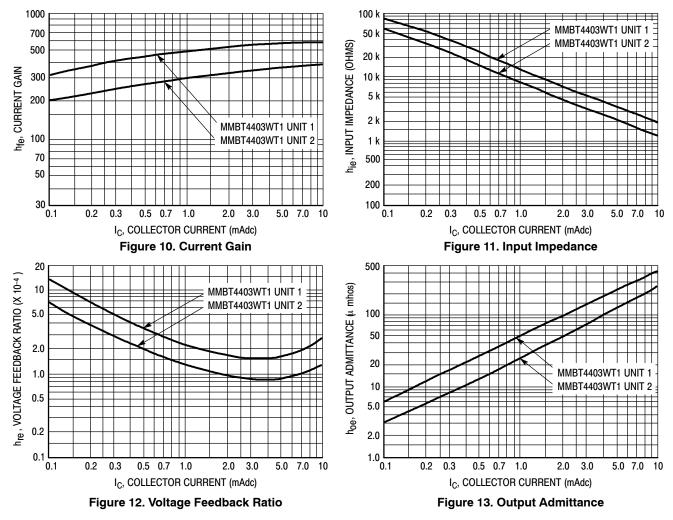
 $V_{CE} = -10$  Vdc,  $T_A = 25^{\circ}C$ ; Bandwidth = 1.0 Hz

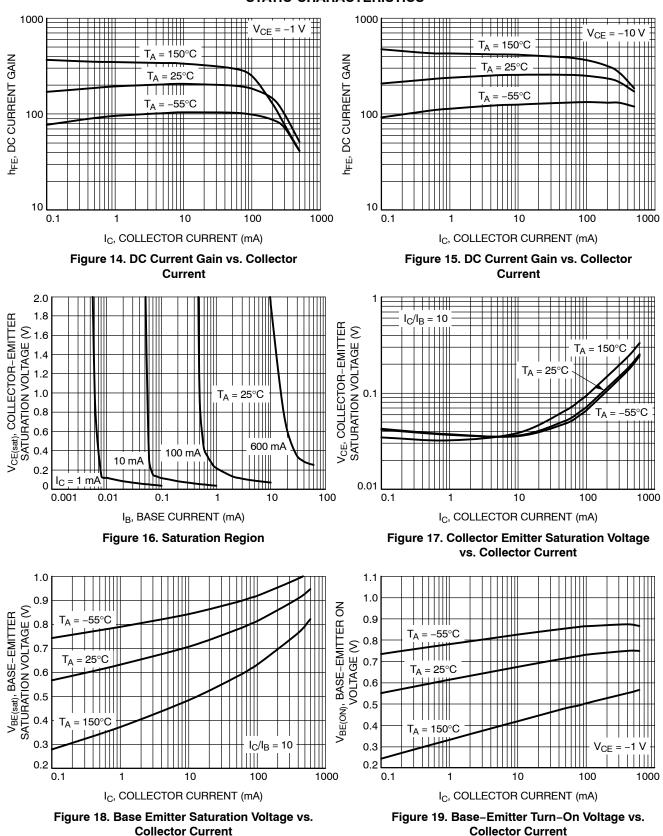


h PARAMETERS



This group of graphs illustrates the relationship between  $h_{fe}$  and other "h" parameters for this series of transistors. To obtain these curves, a high–gain and a low–gain unit were selected from the MMBT4403WT1 lines, and the same units were used to develop the correspondingly numbered curves on each graph.





#### STATIC CHARACTERISTICS

#### 1000 0.5 0.0001 0.001 s 0 I<sub>C</sub>, COLLECTOR CURRENT (mA) $\theta_{\text{VC}}$ for $\text{V}_{\text{CE(sat)}}$ 0.1 COEFFICIENT (mV/°C) 0.5 100 0.01 1.0 1.5 10 + $\theta_{VS}$ for $V_{BE}$ 2.0 Single Pulse Test at $T_A$ = 25°C 1 1 2.5 1 10 100 0.1 0.2 0.5 1.0 2.0 5.0 10 20 50 100 200 500 V<sub>CE</sub>, COLLECTOR EMITTER VOLTAGE (V) I<sub>C</sub>, COLLECTOR CURRENT (mA)

#### STATIC CHARACTERISTICS

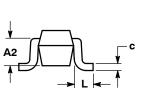
Figure 20. Safe Operating Area

Figure 21. Temperature Coefficients

#### PACKAGE DIMENSIONS

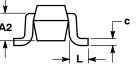
SC-70 (SOT-323) CASE 419-04 **ISSUE N** 

D e1  $H_{E}$ b Δ 0.05 (0.002) Δ1



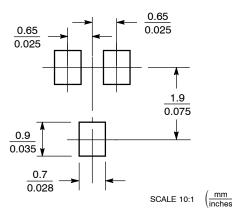
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2 CONTROLLING DIMENSION: INCH.

|     | MILLIMETERS |          |      | INCHES    |           |       |  |
|-----|-------------|----------|------|-----------|-----------|-------|--|
| DIM | MIN         | NOM      | MAX  | MIN       | MAX       |       |  |
| Α   | 0.80        | 0.90     | 1.00 | 0.032     | 0.035     | 0.040 |  |
| A1  | 0.00        | 0.05     | 0.10 | 0.000     | 0.002     | 0.004 |  |
| A2  |             | 0.70 REF |      |           | 0.028 REF |       |  |
| b   | 0.30        | 0.35     | 0.40 | 0.012     | 0.014     | 0.016 |  |
| С   | 0.10        | 0.18     | 0.25 | 0.004     | 0.007     | 0.010 |  |
| D   | 1.80        | 2.10     | 2.20 | 0.071     | 0.083     | 0.087 |  |
| Е   | 1.15        | 1.24     | 1.35 | 0.045     | 0.049     | 0.053 |  |
| е   | 1.20        | 1.30     | 1.40 | 0.047     | 0.051     | 0.055 |  |
| e1  |             | 0.65 BSC |      | 0.026 BSC |           |       |  |
| L   | 0.20        | 0.38     | 0.56 | 0.008     | 0.015     | 0.022 |  |
| HE  | 2.00        | 2.10     | 2.40 | 0.079     | 0.083     | 0.095 |  |



STYLE 3: PIN 1. BASE 2 EMITTER 3. COLLECTOR

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, incluing without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights of others. SCILLC products are not designed, intended, or authorized for use as components. in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws in any mann

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your loca Sales Representative

# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: MMBT4403WT1G